

INCH-POUND  
MIL-M-38510/170C  
18 June 2004

SUPERSEDING  
MIL-M-38510/170B  
30 April 1984

MILITARY SPECIFICATION  
MICROCIRCUITS, DIGITAL, CMOS, AND GATES,  
MONOLITHIC SILICON, POSITIVE LOGIC

Reactivated after 18 June 2004 and may be used for new and existing designs and acquisitions.

This specification is approved for use by all Departments  
and Agencies of the Department of Defense.

The requirements for acquiring the product herein shall consist of this specification sheet and MIL-PRF 38535

## 1. SCOPE

1.1 Scope. This specification covers the detail requirements for monolithic silicon, CMOS, logic microcircuits. Two product assurance classes and a choice of case outlines, lead finishes, and radiation hardness assurance (RHA) are provided and are reflected in the complete Part or Identifying Number (PIN). For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535 (see 6.3).

1.2 Part or identifying number (PIN). The PIN is in accordance with MIL-PRF-38535 and as specified herein.

1.2.1 Device types. The device types are as follows:

<u>Device type</u>	<u>Circuit</u>
01	Quad 2-input AND gate
02	Dual 4-input AND gate
03	Triple 3-input AND gate

1.2.2 Device class. The device class is the product assurance level as defined in MIL-PRF-38535.

1.2.3 Case outlines. The case outlines are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
A	GDFP5-F14 or CDFP6-F14	14	Flat pack
C	GDIP1-T14 or CDIP2-T14	14	Dual-in-line
D	GDFP1-F14 or CDFP2-F14	14	Flat pack
T	CDFP3-F14	14	Flat pack
X <u>1/ 2/</u>	GDFP5-F14 or CDFP6-F14	14	Flat pack, except A dimension equals 0.1" (2.54 mm) max
Y <u>1/ 2/</u>	GDFP1-F14 or CDFP2-F14	14	Flat pack, except A dimension equals 0.1" (2.54 mm) max

- 1/ As an exception to MIL-PRF-38535, appendix A, for case outlines X and Y only, the leads of bottom brazed ceramic packages (i.e., configuration 2 of case outlines A and D) may have electroless nickel undercoating which should be 50 to 200 microinches (1.27 to 5.08 µm) thick provided the lead finish is hot solder dip (i.e., finish letter A) and provided that, after any lead forming, an additional hot solder dip coating is applied which should extend from the outer tip of the lead to no more than 0.015 inch (0.38 mm) from the package edge.  
2/ For bottom or side brazed packages, case outlines X and Y only, the S<sub>1</sub> dimension may go to .000 inch (.00 mm) minimum.

Comments, suggestions, or questions on this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAC, P.O. Box 3990, Columbus, OH 43218-3990, or email CMOS@dsc.dla.mil. Since contact information can change, you may want to verify the currency of this address information using the ASSIST Online database at [www.dodssp.daps.mil](http://www.dodssp.daps.mil).

**1.3 Absolute maximum ratings.**

Supply voltage range ( $V_{DD}$ - $V_{SS}$ ).....	-0.5 V dc to +18.0 V dc
Input current (each input) .....	$\pm 10$ mA
Input voltage range .....	$(V_{SS} - 0.5) \leq V_I \leq (V_{DD} + 0.5)$
Storage temperature range ( $T_{STG}$ ).....	-65° to +175°C
Maximum power dissipation ( $P_D$ ) .....	200 mW
Lead temperature (soldering, 10 seconds) .....	+300°C
Thermal resistance, junction to case ( $\theta_{JC}$ ).....	See MIL-STD-1835
Junction temperature ( $T_J$ ) .....	175°C

**1.4 Recommended operating conditions.**

Supply voltage range ( $V_{DD}$ - $V_{SS}$ ).....	4.5 V dc to 15.0 V dc
Input low voltage range ( $V_{IL}$ ) .....	$V_{OL} = 10\% V_{DD}$ , $V_{OH} = 90\% V_{DD}$ 0.0 V to 1.5 V dc @ $V_{DD} = 5.0$ V 0.0 V to 2.0 V dc @ $V_{DD} = 10.0$ V dc 0.0 V to 4.0 V dc @ $V_{DD} = 15.0$ V dc
Input high voltage range ( $V_{IH}$ ).....	$V_{OL} = 10\% V_{DD}$ , $V_{OH} = 90\% V_{DD}$ 3.5 V to 5.0 V dc @ $V_{DD} = 5.0$ V 8.0 V to 10.0 V dc @ $V_{DD} = 10.0$ V dc 11 V to 15.0 V dc @ $V_{DD} = 15.0$ V dc
Operating temperature range ( $T_A$ ) .....	-55°C to +125°C

**2. APPLICABLE DOCUMENTS**

**2.1 General.** The documents listed in this section are specified in sections 3, 4, or 5 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document users are cautioned that they must meet all specified requirements of documents cited in sections 3, 4, or 5 of this specification, whether or not they are listed.

**2.2 Government documents.**

**2.2.1 Specifications and Standards.** The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

**DEPARTMENT OF DEFENSE SPECIFICATION**

MIL-PRF-38535 - Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

**DEPARTMENT OF DEFENSE STANDARDS**

MIL-STD-883 - Test Method Standard Microcircuits.  
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

(Copies of these documents are available online at <http://assist.daps.dla.mil/quicksearch/> or [www.dodssp.daps.mil](http://www.dodssp.daps.mil) or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

**2.3 Order of precedence.** In the event of a conflict between the text of this document and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

### 3. REQUIREMENTS

3.1 Qualification. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.4).

3.2 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

3.3 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein. Although eutectic die bonding is preferred, epoxy die bonding may be performed. However the resin used shall be Dupont 5504 Conductive Silver Paste, or equivalent, which is cured at  $200^{\circ}\text{C} \pm 10^{\circ}\text{C}$  for a minimum of 2 hours. The use of equivalency epoxies or cure cycles shall be approved by the qualifying activity. Equivalency shall be demonstrated in data submitted to the qualifying activity for verification.

3.3.1 Logic diagrams and terminal connections. The logic diagrams and terminal connections shall be as specified on figure 1.

3.3.2 Truth tables and logic equations. The truth tables and logic equations shall be as specified on figure 2.

3.3.3 Switching test circuit and waveforms. The switching test circuit and waveforms shall be as specified on figure 3.

3.3.4 Schematic circuits. The schematic circuits shall be maintained by the manufacturer and made available to the qualifying activity or preparing activity upon request.

3.3.5 Case outlines. The case outlines shall be as specified in 1.2.3.

3.4 Lead material and finish. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).

3.5 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I, and apply over the full recommended ambient operating temperature range.

3.6 Electrical test requirements. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.

3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.

3.8 Microcircuit group assignment. The devices covered by this specification shall be in microcircuit group number 36 (see MIL-PRF-38535, appendix A).

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $V_{SS} = 0 \text{ V}$ $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$ Unless otherwise specified	Device type	Limits		Unit
				Min	Max	
Positive clamping input to $V_{DD}$	$V_{IC(\text{POS})}$	$T_A = +25^\circ\text{C}$ , $V_{DD} = \text{GND}$ $V_{SS} = \text{Open}$ , Output = Open $I_{IN} = 1 \text{ mA}$	All		1.5	V dc
Negative clamping input to $V_{SS}$	$V_{IC(\text{NEG})}$	$T_A = +25^\circ\text{C}$ , $V_{DD} = \text{Open}$ $V_{SS} = \text{GND}$ , Output = Open $I_{IN} = -1 \text{ mA}$	All		-6.0	V dc
Quiescent supply current	$I_{SS}$	$V_{DD} = 18 \text{ V dc}$ $V_{IN} = V_{SS} \text{ or } V_{DD}$ All input combinations	All		-750	nA
High level output voltage	$V_{OH1}$	$V_{DD} = 15 \text{ V dc}$ $V_{IN} = V_{DD}$ $ I_O  \leq 1 \mu\text{A}$	All	14.95		V dc
Low level output voltage	$V_{OL1}$	$V_{DD} = 15 \text{ V dc}$ Any one input = $V_{DD}$ All others = $V_{SS}$ $ I_O  \leq 1 \mu\text{A}$	All		0.05	V dc
Input high voltage	$V_{IH1}$	$V_{DD} = 5 \text{ V dc}$ See table III $ I_O  \leq 1 \mu\text{A}$	All	3.5		V dc
	$V_{IH2}$	$V_{DD} = 10 \text{ V dc}$ See table III $ I_O  \leq 1 \mu\text{A}$	All	7.0		V dc
	$V_{IH3}$	$V_{DD} = 15 \text{ V dc}$ See table III $ I_O  \leq 1 \mu\text{A}$	All	11.0		V dc
Input low voltage	$V_{IL1}$	$V_{DD} = 5 \text{ V dc}$ Any one input = 1.5 V All others = 3.5 V $V_O = 0.5 \text{ V dc}$ $ I_O  \leq 1 \mu\text{A}$	All		1.5	V dc
	$V_{IL2}$	$V_{DD} = 10 \text{ V dc}$ Any one input = 3.0 V All others = 7.0 V $V_O = 1.0 \text{ V dc}$ $ I_O  \leq 1 \mu\text{A}$	All		3.0	V dc
	$V_{IL3}$	$V_{DD} = 15 \text{ V dc}$ Any one input = 4.0 V All others = 11.0 V $V_O = 1.5 \text{ V dc}$ $ I_O  \leq 1 \mu\text{A}$	All		4.0	V dc

TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions $V_{SS} = 0 \text{ V}$ $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$ Unless otherwise specified	Device type	Limits		Unit
				Min	Max	
Output high (source) current	$I_{OH1}$	$V_{DD} = 5 \text{ V dc}$ $V_{IN} = V_{DD}$ $V_{OH} = 4.6 \text{ V dc}$	All	-0.36		mA dc
	$I_{OH2}$	$V_{DD} = 15 \text{ V dc}$ $V_{IN} = V_{DD}$ $V_{OH} = 13.5 \text{ V dc}$	All	-2.4		mA dc
Output low (sink) current	$I_{OL1}$	$V_{DD} = 5 \text{ V dc}$ Any one input = $V_{DD}$ All other inputs = $V_{SS}$ $V_{OL} = 0.4 \text{ V dc}$	All	0.36		mA dc
	$I_{OL2}$	$V_{DD} = 15 \text{ V dc}$ Any one input = $V_{DD}$ All other inputs = $V_{SS}$ $V_{OL} = 1.5 \text{ V dc}$	All	2.4		mA dc
Input leakage 1/ current	$I_{IH}$	$V_{DD} = 18 \text{ V dc}$ Measure inputs sequentially Connect all unused inputs to $V_{DD}$	All		100	nA
Input leakage 1/ current	$I_{IL}$	$V_{DD} = 18 \text{ V dc}$	All		-100	nA
Input capacitance	$C_i$	$V_{DD} = 0 \text{ V dc}$ , $f = 1 \text{ MHz}$ , $T_A = 25^\circ\text{C}$	All		7.5	pF
Propagation delay time, high to low level	$t_{PHL}$	$V_{DD} = 5 \text{ V dc}$ , $C_L = 50 \text{ pF}$ $R_L = 200 \text{ k}\Omega$ (See figure 3)	All	13	350	ns
		$V_{DD} = 10 \text{ V dc}$ , $C_L = 50 \text{ pF}$ $R_L = 200 \text{ k}\Omega$ $T_A = +25^\circ\text{C}$ (See figure 3)	All	6	120	ns
Propagation delay time, low to high level	$t_{PLH}$	$V_{DD} = 5 \text{ V dc}$ , $C_L = 50 \text{ pF}$ $R_L = 200 \text{ k}\Omega$ (See figure 3)	All	13	350	ns
		$V_{DD} = 10 \text{ V dc}$ , $C_L = 50 \text{ pF}$ $R_L = 200 \text{ k}\Omega$ $T_A = +25^\circ\text{C}$ (See figure 3)	All	6	120	ns
Transition time, high to low level	$t_{TTL}$	$V_{DD} = 5 \text{ V dc}$ , $C_L = 50 \text{ pF}$ $R_L = 200 \text{ k}\Omega$ (See figure 3)	All	10	280	ns
		$V_{DD} = 10 \text{ V dc}$ , $C_L = 50 \text{ pF}$ $R_L = 200 \text{ k}\Omega$ $T_A = +25^\circ\text{C}$ (See figure 3)	All	5	100	ns
Transition time, low to high level	$t_{TTH}$	$V_{DD} = 5 \text{ V dc}$ , $C_L = 50 \text{ pF}$ $R_L = 200 \text{ k}\Omega$ (See figure 3)	All	10	280	ns
		$V_{DD} = 10 \text{ V dc}$ , $C_L = 50 \text{ pF}$ $R_L = 200 \text{ k}\Omega$ $T_A = +25^\circ\text{C}$ (See figure 3)	All	5	100	ns

1/ Input current of one input node.

TABLE II. Electrical test requirements.

Line no.	MIL-PRF-38535 test requirements	Class S device 1/			Class B device 1/		
		Ref. par.	Table III Subgroups 2/	Table IV delta limits 3/	Ref. par.	Table III subgroups 2/	Table IV delta limits 3/
1	Interim electrical parameters		1			1	
2	Static burn-in I (method 1015)	4.2c 4.5.2					
3	Same as line 1		1	Δ			
4	Static burn-in II (method 1015)	4.2c 4.5.2			4.2c 4.5.2	4/	
5	Same as line 1	4.2e	1*	Δ	4.2e	1*	Δ
6	Dynamic burn-in (method 1015)	4.2c 4.5.2					
7	Same as line 1	4.2e	1*	Δ			
8	Final electrical parameters (method 5004)		1*, 2, 3, 9			1*, 2, 3, 9, 10, 11	
9	Group A test requirements (method 5005)	4.4.1	1, 2, 3, 4, 9, 10, 11, 12		4.4.1	1, 2, 3, 4, 9	
10	Group B test when using method 5005 QCI option	4.4.2	1, 2, 3, 9, 10, 11	Δ			
11	Group C end-point electrical parameters (method 5005)				4.4.3	1, 2, 3	Δ
12	Group D end-point electrical parameters (method 5005)	4.4.4	1, 2, 3		4.4.4	1, 2, 3	

1/ Blank spaces indicate tests are not applicable.

2/ \* indicates PDA applies to subgroup 1 (see 4.2.1).

3/ Δ indicates delta limits shall be required only on table III, subgroup 1, where specified, and the delta values shall be computed with reference to the previous interim electrical parameters (line 1).

4/ The device manufacturer may at his option either perform delta measurements or within 24 hours after burn-in (or removal of bias) perform the final electrical parameter measurements.

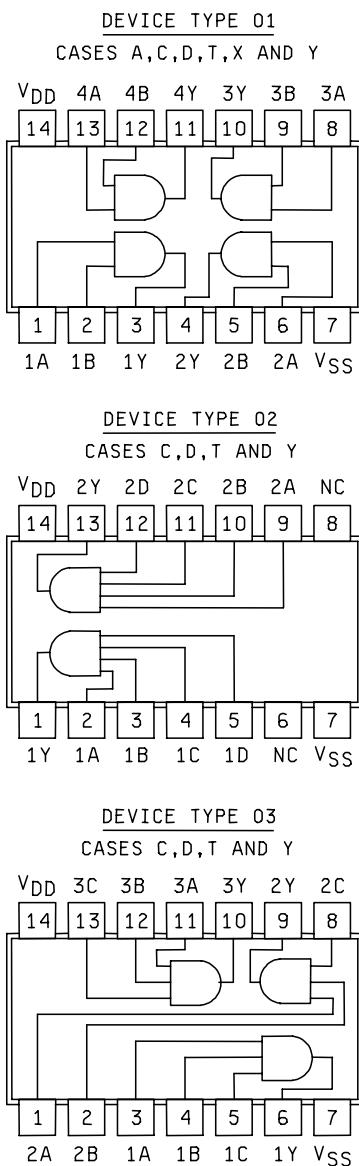


FIGURE 1. Logic diagrams and terminal connections.

## Device type 01

Truth table each gate		
Input		Output
A	B	Y
L	L	L
H	L	L
L	H	L
H	H	H

Positive logic  $Y = A \bullet B$ 

## Device type 02

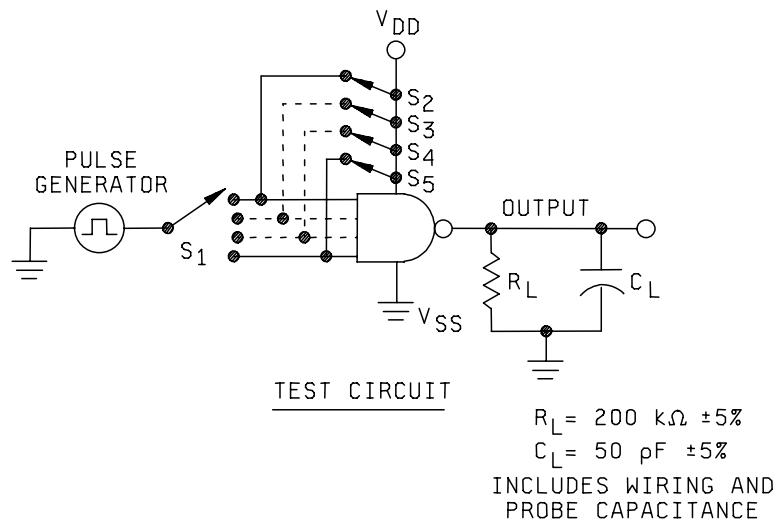
Truth table each gate				Output
Input				Output
A	B	C	D	Y
L	L	L	L	L
H	L	L	L	L
L	H	L	L	L
H	H	L	L	L
L	L	H	L	L
H	L	H	L	L
L	H	H	L	L
H	H	H	L	L
L	L	L	H	L
H	L	L	H	L
L	H	L	H	L
H	H	L	H	L
L	L	H	H	L
H	L	H	H	L
L	H	H	H	L
H	H	H	H	H

Positive logic  $Y = A \bullet B \bullet C \bullet D$ 

## Device type 03

Truth table each gate			
Input			Output
A	B	C	Y
L	L	L	L
H	L	L	L
L	H	L	L
H	H	L	L
L	L	H	L
H	L	H	L
L	H	H	L
H	H	H	H

Positive logic  $Y = A \bullet B \bullet C$ FIGURE 2. Truth tables and logic equations.



NOTE: All unused inputs  
must be tied to  $V_{DD}$ .

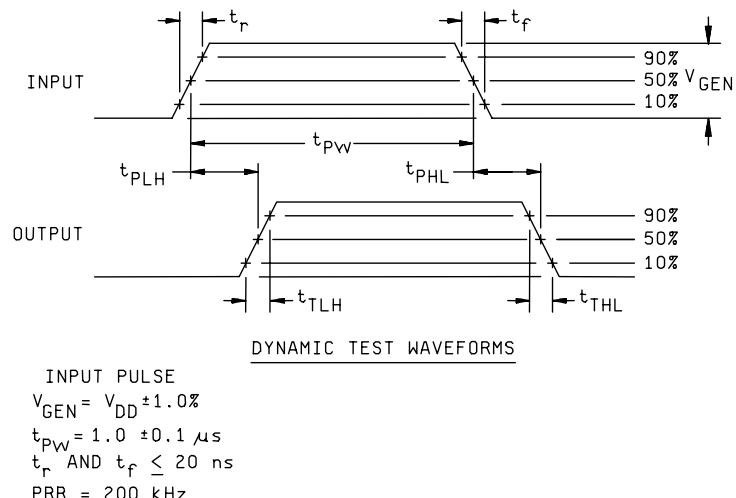


FIGURE 3. Switching test circuit and waveforms.

#### 4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 Screening. Screening shall be in accordance with, MIL-PRF-38535 and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:

- a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
- b. Delete the sequence specified as initial (pre-burn-in) electrical parameters through interim (post-burn-in) electrical parameters of method 5004 and substitute lines 1 through 7 of table II herein.
- c. Burn-in (method 1015 of MIL-STD-883).
  - (1) Unless otherwise specified in the manufacturers QM plan for static tests (test condition A), ambient temperature ( $T_A$ ) shall be +125°C minimum. Test duration for each static test shall be 24 hours minimum for class S devices and in accordance with table I of method 1015 for class B devices.
    - i. For static burn-in I, all inputs shall be connected to GND.
    - ii. For static burn-in II, all inputs shall be connected to  $V_{DD}$ .  $V_{DD} = 15$  V minimum and 18 V maximum.
    - iii. Except for  $V_{DD}$  and  $V_{SS}$ , terminals shall be connected through resistors whose value is 2 kΩ to 47 kΩ. The actual measured value of the resistor selected shall not exceed ±20% of its branded value due to use, heat or age.
    - iv. Output may be open or connected to  $V_{DD}/2$ .
    - v.  $V_{DD}/2 = V_{DD}/2 \pm 1.0$  V.
  - (2) Unless otherwise specified in the manufacturers QM plan for dynamic test (test condition D), ambient temperature shall be +125°C minimum. Test duration shall be in accordance with table I of method 1015.
    - i. Except for  $V_{DD}$  and  $V_{SS}$ , terminals shall be connected through resistors whose value is 2 kΩ to 47 kΩ. The actual measured value of the resistor selected shall not exceed ±20% of its branded value due to use, heat or age.
    - ii. Input signal requirements: Square wave, 50% duty cycle; 25 kHz < PRR < 1 MHz;  $t_{TLH}$  and  $t_{THL} < 1$  μs. Voltage level: Minimum =  $V_{SS} - 0.5$  V, +10%  $V_{DD}$ ; Maximum =  $V_{DD} + 0.5$  V, -10%  $V_{DD}$ .
      - iii.  $V_{DD} = V_{DD}/2 \pm 1.0$  V.
    - d. Interim and final electrical test parameters shall be as specified in table II.
    - e. For class S devices, post dynamic burn-in, or class B devices, post static burn-in, electrical parameter measurements may, at the manufacturer's option, be performed separately or included in the final electrical parameter requirements.

4.2.1 Percent defective allowable (PDA).

- a. The PDA for class S devices shall be 5 percent for static burn-in and 5 percent for dynamic burn-in, based on the exact number of devices submitted to each separate burn-in.
- b. Static burn-in I and II failure shall be cumulative for determining the PDA.
- c. The PDA for class B devices shall be in accordance with MIL-PRF-38535 for static burn-in. Dynamic burn-in is not required.
- d. Those devices whose measured characteristics, after burn-in, exceed the specified delta ( $\Delta$ ) limits or electrical parameter limits specified in table III, subgroup 1, are defective and shall be removed from the lot. The verified failures divided by the total number of devices in the lot initially submitted to burn-in shall be used to determine the percent defective for the lot and the lot shall be accepted or rejected based on the specified PDA.

4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.

4.4 Technology Conformance inspection (TCI). Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4.1 Group A inspection. Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:

- a. Tests shall be performed in accordance with table II herein.
- b. Subgroups 5, 6, 7, and 8 shall be omitted.
- c. Subgroup 4 ( $C_i$  measurement) shall be measured only for initial qualification and after process or design changes that may affect input capacitance. Capacitance shall be measured between the designated terminal and  $V_{SS}$  at a frequency of 1 MHz.
- d. Subgroups 12 shall be added to the group A inspection requirements for class S devices using sample size series of 15 (acceptance 0) and consist of procedures, test conditions, and limits specified in table III.
- e. Subgroups 9 and 11 shall be measured only for initial qualification and after process or design changes which may affect dynamic performance.

4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of MIL-PRF-38535.

4.4.3 Group C inspection. Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein. Delta limits shall apply only to subgroup 1 of group C inspection and shall consist of tests specified in table IV herein.
- b. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.

TABLE III. Group A inspection for device type 01.

Symbol	MIL-STD-833 method	Cases A,C,D,T, and X, and Y	Terminal conditions 1/														Measured terminal	Test limits						Unit	
			1	2	3	4	5	6	7	8	9	10	11	12	13	14		Subgroup 1 T <sub>A</sub> = 25°C	Subgroup 2 T <sub>A</sub> = 125°C	Subgroup 3 T <sub>A</sub> = -55°C					
			Symbol	1A	1B	1Y	2Y	2B	2A	V <sub>SS</sub>	3A	3B	3Y	4Y	4B	4A	V <sub>DD</sub>	Min	Max	Min	Max	Min	Max		
V <sub>IC</sub> (pos)		1	1 mA	1 mA				1 mA	1 mA						1 mA	GND	1A	1.5						V	
		2															"	"						"	
		3															"	"						"	
		4															"	"						"	
		5															"	"						"	
		6															"	"						"	
		7															"	"						"	
		8															"	"						"	
V <sub>IC</sub> (neg)		9	-1 mA	-1 mA				-1 mA	-1 mA	GND								1A	-6.0						"
		10															"	"						"	
		11															"	"						"	
		12															"	"						"	
		13															"	"						"	
		14															"	"						"	
		15															"	"						"	
		16															"	"						"	
I <sub>SS</sub> 2/	3005	17	GND	18.0V	18.0V			18.0V	GND	"	GND	18.0V	18.0V	18.0V		18.0V	GND	18.0V	V <sub>SS</sub>	-25.0		-750.0		nA	
		18																	V <sub>SS</sub>	-25.0		-750.0		nA	
		19																	V <sub>SS</sub>	-25.0		-750.0		nA	
V <sub>OL1</sub>	3007	20	15.0V	GND	15.0V				GND	GND	GND	GND	GND	GND	GND	GND	GND	15.0V	1Y	0.05		0.05		0.05	V
		21																"	"					"	
		22																"	"					"	
		23																"	"					"	
		24																"	"					"	
		25																"	"					"	
		26																"	"					"	
		27																"	"					"	
V <sub>OH1</sub>	3006	28	15.0V	GND	15.0V			GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	15.0V	1Y	14.95		14.95		14.95	"
		29																"	"			"		"	
		30																"	"			"		"	
		31																"	"			"		"	
V <sub>IL1</sub>		32	1.5V	3.5V	1.5V	3.5V				GND	GND	GND	GND	GND	GND	GND	GND	5.0V	1Y	0.5		0.5		0.5	V
		33																"	"					"	
		34																"	"					"	
		35																"	"					"	
		36																"	"					"	
		37																"	"					"	
		38																"	"					"	
		39																"	"					"	
V <sub>IL2</sub>		40	3.0	7.0V	3.0V	7.0V												10V	1Y	1.0		1.0		1.0	"
		41																"	"			"		"	
		42																"	"			"		"	
		43																"	"			"		"	
		44																"	"			"		"	
		45																"	"			"		"	
		46																"	"			"		"	
		47																"	"			"		"	

See footnotes at end of table.

TABLE III. Group A inspection for device type 01 – Continued.

Symbol	MIL-STD-833 method	Cases A,C,D, T, X, and Y	Terminal conditions 1/														Measured terminal	Test limits						Unit	
			1	2	3	4	5	6	7	8	9	10	11	12	13	14		Subgroup 1 $T_A = 25^\circ\text{C}$		Subgroup 2 $T_A = 125^\circ\text{C}$		Subgroup 3 $T_A = -55^\circ\text{C}$			
			Symbol	1A	1B	1Y	2Y	2B	2A	$V_{SS}$	3A	3B	3Y	4Y	4B	4A	$V_{DD}$	Min	Max	Min	Max	Min	Max		
V <sub>IL3</sub>			48	4.0V	11.0V			GND	GND	GND	GND			GND	GND	15V	1Y		1.5	1.5	1.5	1.5	1.5	V	
			49	11.0V	4.0V			GND	GND	"	"			GND	GND	"	"		"	"	"	"	"	"	
			50	GND	GND			11.0V	4.0V	"	"			GND	GND	"	"		"	"	"	"	"	"	
			51	"	"			4.0V	11.0V	"	"			GND	GND	"	"		"	"	"	"	"	"	
			52	"	"			GND	GND	"	"			GND	GND	"	"		"	"	"	"	"	"	
			53	"	"			"	"					GND	GND	"	"		"	"	"	"	"	"	
			54	"	"			"	"					GND	GND	"	"		"	"	"	"	"	"	
			55	"	"			"	"					GND	GND	"	"		"	"	"	"	"	"	
			56	3.5V	3.5V			"	"	"	"			GND	GND	5.0V	1Y	4.5		4.5		4.5			"
			57	GND	GND			3.5V	3.5V	"	"			GND	GND	"	"		"	"		"	"		"
V <sub>IH1</sub>			58	"	"			GND	GND	"	"			GND	GND	"	"		"	"		"	"		"
			59	"	"			"	"					GND	GND	"	"		"	"		"	"		"
			60	7.0V	7.0V			"	"	"	"			GND	GND	10V	1Y	9.0		9.0		9.0			"
V <sub>IH2</sub>			61	GND	GND			7.0V	7.0V	"	"			GND	GND	"	"		"	"		"	"		"
			62	"	"			GND	GND	"	"			GND	GND	"	"		"	"		"	"		"
			63	"	"			"	"					GND	GND	"	"		"	"		"	"		"
V <sub>IH3</sub>			64	11V	11V			"	"	"	"			GND	GND	15V	1Y	13.5		13.5		13.5			"
			65	GND	GND			11V	11V	"	"			GND	GND	"	"		"	"		"	"		"
			66	"	"			GND	GND	"	"			GND	GND	"	"		"	"		"	"		"
			67	"	"			"	"					GND	GND	"	"		"	"		"	"		"
I <sub>OL1</sub>			68	"	5.0V	0.4V	0.4V	"	"	"	"			GND	GND	5.0V	1Y	0.51		0.36		0.64			mA
			69	5.0V	GND	0.4V	0.4V	"	"	"	"			GND	GND	"	"		"	"		"	"		"
			70	GND	GND	"	"	5.0V	5.0V	"	"			GND	GND	"	"		"	"		"	"		"
			71	"	"			GND	GND	"	"			GND	GND	"	"		"	"		"	"		"
			72	"	"			"	"					GND	GND	"	"		"	"		"	"		"
			73	"	"			"	"					GND	GND	"	"		"	"		"	"		"
			74	"	"			"	"					GND	GND	"	"		"	"		"	"		"
			75	"	"			"	"					GND	GND	"	"		"	"		"	"		"
			76	15V	"	1.5V	1.5V	"	"	"	"			GND	GND	15.0V	1Y	3.4		2.4		4.2			"
			77	GND	15V	GND	GND	"	"	"	"			GND	GND	"	"		"	"		"	"		"
I <sub>OL2</sub>			78	"	"			1.5V	1.5V	"	"			GND	GND	"	"		"	"		"	"		"
			79	"	"			"	"					GND	GND	"	"		"	"		"	"		"
			80	"	"			"	"					GND	GND	"	"		"	"		"	"		"
			81	"	"			"	"					GND	GND	"	"		"	"		"	"		"

See footnotes at end of table.

TABLE III. Group A inspection for device type 01 – Continued.

Symbol	MIL-STD-833 method	Cases A,C,D, T, X, and Y	Terminal conditions 1/														Measured terminal	Test limits						Unit	
			1	2	3	4	5	6	7	8	9	10	11	12	13	14		Subgroup 1 $T_A = 25^\circ C$		Subgroup 2 $T_A = 125^\circ C$		Subgroup 3 $T_A = -55^\circ C$			
			Symbol	1A	1B	1Y	2Y	2B	2A	$V_{ss}$	3A	3B	3Y	4Y	4B	4A	$V_{DD}$	Min	Max	Min	Max	Min	Max		
			Test No.																						
$I_{OL2}$			82 83	GND GND	GND GND			GND GND	GND GND	GND “	GND “	GND “		1.5V 1.5V	15.0V 15V	GND 15.0V	15.0V 15.0V	4Y 4Y	3.4 3.4	2.4 2.4	4.2 4.2	4.2 4.2	4.2 4.2	mA “	
$I_{OH1}$			84 85 86 87	5.0V GND “ “	5.0V GND “ “	4.6V 4.6V		GND 5.0V GND “	GND 5.0V GND “	“ “ “	“ 5.0V GND	“ 5.0V GND	4.6V 4.6V	“ 5.0V GND	“ 5.0V GND	“ 5.0V GND	“ 5.0V GND	1Y 2Y 3Y 4Y	-0.51 “ “ “	-0.36 “ “ “	-0.64 “ “ “	-0.64 “ “ “	-0.64 “ “ “	“	
$I_{OH2}$			88 89 90 91	15.0V GND “ “	15.0V GND “ “	13.5V 13.5V		“ 15.0V GND “	“ 15.0V GND “	“ 15.0V GND “	“ 15.0V GND “	13.5V 13.5V	“ 15.0V GND “	“ 15.0V GND “	“ 15.0V GND “	“ 15.0V GND “	1Y 2Y 3Y 4Y	-3.4 “ “ “	-2.4 “ “ “	-4.2 “ “ “	-4.2 “ “ “	-4.2 “ “ “	“		
$I_{IL1}$	3010	3/	92	18.0V	18.0V			18.0V	18.0V	“	18.0V	18.0V		18.0V	18.0V	18.0V	18.0V	All inputs together	800						nA
$I_{IL2}$	3010		93 94 95 96 97 98 99 100	“ “ “ “ “ “ “ “	“ “ “ “ “ “ “ “			“ “ “ “ “ “ “ “	“ “ “ “ “ “ “ “	“ “ “ “ “ “ “ “				“ “ “ “ “ “ “ “	“ “ “ “ “ “ “ “	1A 1B 2B 2A 3A 3B 4B 4A	100 “ “ “ “ “ “ “	100 “ “ “ “ “ “ “	100 “ “ “ “ “ “ “	100 “ “ “ “ “ “ “	100 “ “ “ “ “ “ “	“			
$I_{IL1}$	3009	101	GND	GND				GND	GND	“	GND	GND		GND	GND	“	All inputs together	-800						“	
$I_{IL2}$	3009	102 103 104 105 106 107 108 109	GND 18.0V 18.0V “ “ “ “ “ “	18.0V GND 18.0V “ “ “ “ “ “				18.0V 18.0V GND 18.0V	18.0V “ “ “ “ “ “ “	18.0V “ “ “ “ “ “ “	18.0V “ “ “ “ “ “ “	18.0V “ “ “ “ “ “ “		18.0V 18.0V GND 18.0V	18.0V “ “ “ “ “ “ “	1A 1B 2B 2A 3A 3B 4B 4A	-100 “ “ “ “ “ “ “	-100 “ “ “ “ “ “ “	-100 “ “ “ “ “ “ “	-100 “ “ “ “ “ “ “	-100 “ “ “ “ “ “ “	“			
$C_i$	3012		110 111 112 113 114 115 116 117	4/ 4/ 4/ 4/ 4/ 4/ 4/ 4/				GND “ “ “ “ “ “ “	GND “ “ “ “ “ “ “	4/ 4/ 4/ 4/ 4/ 4/ 4/ 4/				4/ 4/ 4/ 4/ 4/ 4/ 4/ 4/	GND “ “ “ “ “ “ “	1A 1B 2B 2A 3A 3B 4B 4A	7.5 “ “ “ “ “ “ “						pF “		

See footnotes at end of table.

TABLE III. Group A inspection for device type 01 – Continued.

Symbol	MIL-STD-833 method	Cases A,C,D,T,X, and Y	Terminal conditions 1/														Measured terminal	Test limits						Unit		
			1	2	3	4	5	6	7	8	9	10	11	12	13	14		Subgroup 9 T <sub>A</sub> = 25°C		Subgroup 10 T <sub>A</sub> = 125°C		Subgroup 11 T <sub>A</sub> = -55°C				
			Symbol	1A	1B	1Y	2Y	2B	2A	V <sub>SS</sub>	3A	3B	3Y	4Y	4B	4A	V <sub>DD</sub>	Min	Max	Min	Max	Min	Max			
$t_{PHL}$	3003 Fig. 3	118	IN	5.0V	OUT	OUT		5.0V	5.0V	GND	5.0V	5.0V			5.0V	5.0V	5.0V	1A to 1Y	13	250	18	350	13	250	ns	
		119	IN	5.0V	OUT	OUT		5.0V	5.0V	"	"	"	"	"	"	"	"	1B to 1Y	"	"	"	"	"	"	"	
		120	"	5.0V			OUT	OUT	5.0V	IN	5.0V	"	"	"	"	"	"	2B to 2Y	"	"	"	"	"	"	"	
		121	"	"					"	"	"	"	"	"	"	"	"	2A to 2Y	"	"	"	"	"	"	"	
		122	"	"					"	"	IN	5.0V	"	"	"	"	"	3A to 3Y	"	"	"	"	"	"	"	
		123	"	"					"	"	5.0V	IN	"	"	"	"	"	3B to 3Y	"	"	"	"	"	"	"	
		124	"	"					"	"	5.0V	5.0V	"	"	"	"	"	4B to 4Y	"	"	"	"	"	"	"	
		125	"	"					"	"	5.0V	5.0V	"	"	"	"	"	4A to 4Y	"	"	"	"	"	"	"	
$t_{PLH}$	3003 Fig. 3	126	IN	5.0V	"	OUT	OUT		"	"	"	"	5.0V	"	"	"	5.0V	"	1A to 1Y	"	"	"	"	"	"	"
		127	IN	5.0V	"	OUT	OUT		OUT	OUT	IN	5.0V	"	"	"	"	"	1B to 1Y	"	"	"	"	"	"	"	
		128	"	5.0V							IN	5.0V	"	"	"	"	"	2B to 2Y	"	"	"	"	"	"	"	
		129	"	"							"	"	"	"	"	"	"	2A to 2Y	"	"	"	"	"	"	"	
		130	"	"							"	"	IN	5.0V	"	"	"	3A to 3Y	"	"	"	"	"	"	"	
		131	"	"							"	"	5.0V	IN	"	"	"	3B to 3Y	"	"	"	"	"	"	"	
		132	"	"							"	"	5.0V	5.0V	"	"	"	4B to 4Y	"	"	"	"	"	"	"	
		133	"	"							"	"	5.0V	5.0V	"	"	"	4A to 4Y	"	"	"	"	"	"	"	
$t_{THL}$	3004 Fig. 3	134	IN	5.0V	"	OUT	OUT		"	"	"	"	5.0V	"	"	"	5.0V	"	1Y	10	200	14	280	10	200	"
		135	IN	5.0V	"	OUT	OUT		OUT	OUT	IN	5.0V	"	"	"	"	"	1Y	"	"	"	"	"	"	"	
		136	"	5.0V							IN	5.0V	"	"	"	"	"	2Y	"	"	"	"	"	"	"	
		137	"	"							"	"	IN	5.0V	"	"	"	2Y	"	"	"	"	"	"	"	
		138	"	"							"	"	5.0V	IN	"	"	"	3Y	"	"	"	"	"	"	"	
		139	"	"							"	"	5.0V	5.0V	"	"	"	3Y	"	"	"	"	"	"	"	
		140	"	"							"	"	5.0V	5.0V	"	"	"	4Y	"	"	"	"	"	"	"	
		141	"	"							"	"	5.0V	5.0V	"	"	"	4Y	"	"	"	"	"	"	"	
$t_{TLH}$	3004 Fig. 3	142	IN	5.0V	"	OUT	OUT		"	"	"	"	5.0V	"	"	"	5.0V	"	1Y	"	"	"	"	"	"	"
		143	IN	5.0V	"	OUT	OUT		OUT	OUT	IN	5.0V	"	"	"	"	"	1Y	"	"	"	"	"	"	"	
		144	"	5.0V							IN	5.0V	"	"	"	"	"	2Y	"	"	"	"	"	"	"	
		145	"	"							"	"	IN	5.0V	"	"	"	2Y	"	"	"	"	"	"	"	
		146	"	"							"	"	5.0V	IN	"	"	"	3Y	"	"	"	"	"	"	"	
		147	"	"							"	"	5.0V	5.0V	"	"	"	3Y	"	"	"	"	"	"	"	
		148	"	"							"	"	5.0V	5.0V	"	"	"	4Y	"	"	"	"	"	"	"	
		149	"	"							"	"	5.0V	5.0V	"	"	"	4Y	"	"	"	"	"	"	"	

See footnotes at end of table.

TABLE III. Group A inspection for device type 01 – Continued.

Symbol	MIL-STD-833 method	Cases A,C,D, T, X, and Y	Terminal conditions 1/														Measured terminal	Test limits			Unit			
			1	2	3	4	5	6	7	8	9	10	11	12	13	14		Subgroup 12 $T_A = 25^\circ\text{C}$						
			Symbol	1A	1B	1Y	2Y	2B	2A	V <sub>SS</sub>	3A	3B	3Y	4Y	4B	4A	V <sub>DD</sub>	Min	Max					
$t_{PHL}$	3003 Fig. 3	150	IN	10.0V	10.0V	OUT	OUT	10.0V	10.0V	GND	10.0V	10.0V				10.0V	10.0V	10.0 V	1A to 1Y	6	120		ns	
		151	"	"	IN 10.0V	OUT	OUT	10.0V	10.0V	"	"	"				"	"	"	1B to 1Y	"	"		"	
		152	"	"	"	OUT	OUT	10.0V	10.0V	"	"	"				"	"	"	2B to 2Y	"	"		"	
		153	"	"	"	OUT	OUT	10.0V	10.0V	"	"	"				"	"	"	2A to 2Y	"	"		"	
		154	"	"	"	OUT	OUT	10.0V	10.0V	"	"	"				"	"	"	3A to 3Y	"	"		"	
		155	"	"	"	OUT	OUT	10.0V	10.0V	"	"	"				"	"	"	3B to 3Y	"	"		"	
		156	"	"	"	OUT	OUT	10.0V	10.0V	"	"	"				"	"	"	4B to 4Y	"	"		"	
		157	"	"	"	OUT	OUT	10.0V	10.0V	"	"	"				"	"	"	4A to 4Y	"	"		"	
		158	IN	10.0V	"	OUT	OUT	"	"	"	"	"				"	10.0V	"	"	1A to 1Y	"	"		"
		159	"	IN 10.0V	"	OUT	OUT	OUT	IN 10.0V	IN 10.0V	"	"	"			"	"	"	1B to 1Y	"	"		"	
$t_{PLH}$		160	"	"	OUT	OUT	OUT	IN 10.0V	IN 10.0V	"	"	"				"	"	"	2B to 2Y	"	"		"	
		161	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	2A to 2Y	"	"		"	
		162	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	3A to 3Y	"	"		"	
		163	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	3B to 3Y	"	"		"	
		164	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	4B to 4Y	"	"		"	
		165	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	4A to 4Y	"	"		"	
		166	IN	10.0V	"	OUT	OUT	"	"	"	"	"				"	10.0V	"	"	1A to 1Y	"	"		"
		167	"	IN 10.0V	"	OUT	OUT	OUT	IN 10.0V	IN 10.0V	"	"	"			"	"	"	1B to 1Y	"	"		"	
$t_{THL}$	3004 Fig. 3	168	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	2B to 2Y	"	"		"	
		169	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	2A to 2Y	"	"		"	
		170	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	3A to 3Y	"	"		"	
		171	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	3B to 3Y	"	"		"	
		172	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	4B to 4Y	"	"		"	
		173	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	4A to 4Y	"	"		"	
		174	IN	10.0V	"	OUT	OUT	"	"	"	"	"				"	10.0V	"	"	1Y	"	"		"
$t_{TLH}$		175	"	IN 10.0V	"	OUT	OUT	OUT	IN 10.0V	IN 10.0V	"	"	"			"	"	"	1Y	"	"		"	
		176	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	2Y	"	"		"	
		177	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	2Y	"	"		"	
		178	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	3Y	"	"		"	
		179	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	3Y	"	"		"	
		180	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	4Y	"	"		"	
		181	"	"	OUT	OUT	OUT	"	"	"	"	"				"	"	"	4Y	"	"		"	

See footnotes at end of table.



TABLE III. Group A inspection for device type 02 – Continued.

Symbol	MIL-STD-833 method	Cases C,D,T, and Y	Terminal conditions 1/														Measured terminal	Test limits						Unit		
			1	2	3	4	5	6	7	8	9	10	11	12	13	14		Subgroup 1 $T_A = 25^\circ\text{C}$		Subgroup 2 $T_A = 125^\circ\text{C}$		Subgroup 3 $T_A = -55^\circ\text{C}$				
			Symbol	1Y	1A	1B	1C	1D	NC	$V_{SS}$	NC	2A	2B	2C	2D	2Y	$V_{DD}$	Min	Max	Min	Max	Min	Max			
$V_{IL3}$		Test No.	48		4.0V	11V	11V	11V	11V		GND		GND	GND	GND		15V	1Y		1.5		1.5		1.5	V	
			49		11V	4.0V	11V	4.0V	11V		"	"	"	"	"		"	1Y		"	"	"	"	"	"	
			50		11V	11V	4.0V	11V	4.0V		"	"	"	"	"		"	1Y		"	"	"	"	"	"	
			51		11V	11V	11V	4.0V	11V		"	"	"	"	"		"	1Y		"	"	"	"	"	"	
			52		GND	GND	GND	GND	GND		"	"	"	"	"		"	2Y		"	"	"	"	"	"	
			53		"	"	"	"	"		"	"	"	"	"		"	2Y		"	"	"	"	"	"	
			54		"	"	"	"	"		"	"	"	"	"		"	2Y		"	"	"	"	"	"	
			55		"	"	"	"	"		"	"	"	"	"		"	2Y		"	"	"	"	"	"	
			56		3.5V	3.5V	3.5V	3.5V	3.5V		"		GND	GND	GND		5.0V	1Y	4.5		4.5		4.5		"	
			57		GND	GND	GND	GND	GND		"		3.5V	3.5V	3.5V		5.0V	2Y	4.5		4.5		4.5		"	
$V_{IH2}$		Test No.	58		7.0V	7.0V	7.0V	7.0V	7.0V		"		GND	GND	GND		10V	1Y	9.0		9.0		9.0		"	
			59		GND	GND	GND	GND	GND		"		7.0V	7.0V	7.0V		10V	2Y	9.0		9.0		9.0		"	
$V_{IH3}$		Test No.	60		11V	11V	11V	11V	11V		"		GND	GND	GND		15V	1Y	13.5		13.5		13.5		"	
			61		GND	GND	GND	GND	GND		"		11V	11V	11V		15V	2Y	13.5		13.5		13.5		"	
$I_{OL1}$		Test No.	62	0.4	5.0V	GND	GND	GND	GND		"		GND	GND	GND		5.0V	1Y	0.51		0.36		0.64		mA	
			63	"	"	5.0V	GND	GND	GND		"		"	"	"		"	1Y	"	"	"	"	"	"	"	
			64	"	"	"	5.0V	GND	GND		"		"	"	"		"	1Y	"	"	"	"	"	"	"	
			65	"	"	"	"	5.0V	GND		"		"	"	"		"	1Y	"	"	"	"	"	"	"	
			66	"	"	"	"	"	5.0V	GND		"		"	"		"	2Y	"	"	"	"	"	"	"	
			67	"	"	"	"	"	"	GND		"		"	"		"	2Y	"	"	"	"	"	"	"	
			68	"	"	"	"	"	"	GND		"		"	"		"	2Y	"	"	"	"	"	"	"	
			69	"	"	"	"	"	"	GND		"		"	"		"	2Y	"	"	"	"	"	"	"	
			70	1.5V	15.0V	GND	15.0V	"	"		"		"	"	"		GND		15.0V	1Y	3.4		2.4		4.2	mA
$I_{OL2}$		Test No.	71	"	"	"	"	15.0V	GND		"		"	"	"		"	1Y	"	"	"	"	"	"	"	"
			72	"	"	"	"	"	15.0V	GND		"		"	"		"	1Y	"	"	"	"	"	"	"	"
			73	"	"	"	"	"	"	15.0V	GND		"		"		"	1Y	"	"	"	"	"	"	"	"
			74	"	"	"	"	"	"	"	15.0V	GND		"	"		"	1Y	"	"	"	"	"	"	"	"
			75	"	"	"	"	"	"	"	15.0V	GND		"	"		"	2Y	"	"	"	"	"	"	"	"
			76	"	"	"	"	"	"	"	15.0V	GND		"	"		"	2Y	"	"	"	"	"	"	"	"
			77	"	"	"	"	"	"	"	15.0V	GND		"	"		"	2Y	"	"	"	"	"	"	"	"
$I_{OH1}$		78	4.6V	5.0V	5.0V	5.0V	5.0V	5.0V	5.0V		"		GND	GND	GND		5.0V	1Y	-0.51		-0.36		-0.64		"	
$I_{OH1}$		79		5.0V	5.0V	5.0V	5.0V	5.0V		"		5.0V	5.0V	5.0V		4.6V	2Y	-0.51		-0.36		-0.64		"		
$I_{OH2}$		80	13.5V	15.0V	15.0V	15.0V	15.0V	15.0V		"		GND	GND	GND		15.0V	1Y	-3.4		-2.4		-4.2		"		

See footnotes at end of table.













4.4.4 Group D inspection. Group D inspection shall be in accordance with table V of MIL-PRF-38535. End-point electrical parameters shall be as specified in table II herein.

4.4.5 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.7 herein). RHA levels for device classes B and S shall be as specified in MIL-PRF-38535 and 4.5.4 herein.

4.5 Methods of inspection. Methods of inspection shall be specified and as follows:

4.5.1 Voltage and current. Unless otherwise specified, all voltages given are referenced to the microcircuit  $V_{SS}$  terminal. Currents given are conventional current and positive when flowing into the referenced terminal.

4.5.2 Burn-in and life test cool down procedures. When the burn-in and life tests are completed and prior to removal of bias voltages, the devices under test (DUT) shall be cooled to within  $25^{\circ}\text{C} \pm 3^{\circ}\text{C}$ ; then, electrical parameter end-point measurements shall be performed.

TABLE IV. Delta limits at  $25^{\circ}\text{C}$ .

Parameter 1/	Device types		
	01	02	03
$I_{SS}$	$\pm 10 \text{ nA}$	$\pm 10 \text{ nA}$	$\pm 10 \text{ nA}$
$I_{OL1}$	$\pm 15\%$	$\pm 15\%$	$\pm 15\%$
$I_{OH1}$	$\pm 15\%$	$\pm 15\%$	$\pm 15\%$

1/ Each of the above parameters shall be recorded before and after the required burn-in and life tests to determine delta ( $\Delta$ ).

4.5.3 Quiescent supply current ( $I_{SS}$  test). When performing quiescent supply current measurements ( $I_{SS}$ ), the meter shall be placed so that all currents flow through the meter.

4.5.4 Radiation hardness assurance (RHA) testing. The RHA testing shall be performed in accordance with test procedures and sampling specified in MIL-PRF-38535 and herein.

- a. Before irradiation, selected samples shall be assembled in qualified packages and pass the governing electrical parameters (group A subgroup 1 at  $25^{\circ}\text{C}$ ) and also be subjected to the threshold-voltage test in table VII in order to calculate the delta threshold ( $\Delta V_T$ ) after irradiation.
- b. The devices shall be subjected to a total radiation dose as specified in MIL-PRF-38535 for the radiation hardness assurance level being tested, and meet the end-point electrical parameters as defined in table V at  $25^{\circ}\text{C}$ , after exposure. The start and completion of the end-point electrical parameter measurements shall not exceed 2 hours following irradiation.
- c. Threshold-voltage test circuit conditions shall be as specified in table VII and on figure 5. In situ and remote testing, the tests shall be performed with the devices biased in accordance with table VI and the bias may be interrupted for up to 1 minute to remove devices to the remote bias fixture.
- d. After irradiation, the devices shall pass the truth table test as specified in subgroup 7 in table III or if subgroup 7 is not required, then an equivalent truth table test shall be performed.

TABLE V. Radiation hardened end-point electrical parameters at 25°C.

Parameter	All device types	V <sub>DD</sub>
V <sub>TN</sub>	0.3 V min	10 V
V <sub>TP</sub>	2.8 V max	10 V
ΔV <sub>T</sub>	1.4 V	10 V
I <sub>SS</sub>	100 x max limit	18 V
t <sub>PLH</sub>	1.35 x max limit	5 V
t <sub>PHL</sub>	1.35 x max limit	5 V

TABLE VI. Bias during exposure to radiation.

Device type	Pin connections 1/		
	V <sub>DD</sub> = 10 V dc (through a 30 kΩ to 60 kΩ resistor)	V <sub>SS</sub> = GND	V <sub>DD</sub> = 10 V dc
01	1, 2, 5, 6, 8, 9, 12, 13	7	14
02	2, 3, 4, 5, 9, 10, 11, 12	7	14
03	1, 2, 3, 4, 5, 8, 11, 12, 13	7	14

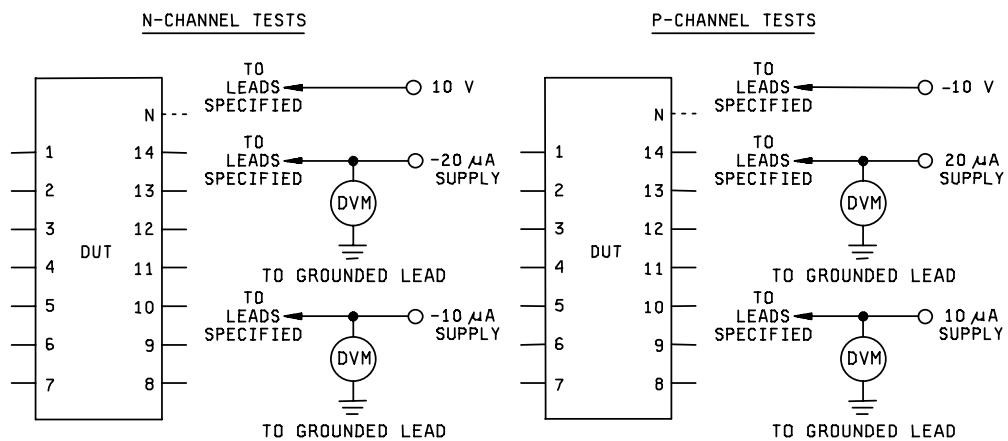
1/ Pins not designated are open, or may be tied to 10 V dc through a 30 to 60 kΩ resistor.

4.6 Data reporting. When specified in the purchase order or contract, a copy of the following data, as applicable, shall be supplied.

- a. Attributes data for all screening tests (see 4.2) and variables data for all static burn-in, dynamic burn-in, and steady-state life tests (see 3.6).
- b. A copy of each radiograph.
- c. The technology conformance inspection (TCI) data (see 4.4).
- d. Parameter distribution data on parameters evaluated during burn-in (see 3.6).
- e. Final electrical parameters data (see 4.2d).
- f. RHA delta limits.

## 5. PACKAGING

5.1 Packaging. For acquisition purposes, the packaging requirements are as specified in the contract or order (see 6.2). When actual packaging of material is to be performed by DoD or in-house contractor personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department of Defense Agency, or within the military service's system commands. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

FIGURE 4. Threshold-voltage test circuit.TABLE VII. Threshold-voltage test circuit conditions.

Device type	GND	10 V	$V_{TN}$ measured at		GND	-10 V	$V_{TP}$ measured at	
			-20 $\mu$ A supply	-10 $\mu$ A supply			20 $\mu$ A supply	10 $\mu$ A supply
01	2	1, 14		5, 6, 7, 8, 9, 12, 13	2	5, 6, 7, 8, 9, 12, 13		1, 14
02	2	3, 4, 5, 14		7, 9, 10, 11, 12	2	5, 6, 7, 8, 9, 10, 11, 12, 13		3, 4, 5, 14
03	3	4, 5, 14		1, 2, 7, 8, 11, 12, 13	3	1, 2, 7, 8, 11, 12, 13		4, 5, 14

## 6. NOTES

6.1 Intended use. Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.

6.2 Acquisition requirements. Acquisition documents should specify the following:

- a. Title, number, and date of the specification.
- b. PIN and compliance identifier, if applicable (see 1.2).
- c. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
- d. Requirements for certificate of compliance, if applicable.
- e. Requirements for notification of change of product or process to contracting activity in addition to notification to the qualifying activity, if applicable.
- f. Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
- g. Requirements for product assurance and radiation hardness assurance options.
- h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements should not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
- i. Requirements for "JAN" marking.
- j. Packaging requirements. (see 5.1).

6.3 Superseding information. The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractors parts lists.

6.4 Qualification. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, P.O. Box 3990, Columbus, Ohio 43218-3990.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535, MIL-HDBK-1331, and as follows:

C <sub>I</sub> .....	Input terminal-to-GND capacitance.
GND .....	Ground zero voltage potential.
T <sub>A</sub> .....	Free air temperature.
V <sub>DD</sub> .....	Positive supply voltage.
V <sub>SS</sub> .....	Negative supply voltage.
I <sub>SS</sub> .....	Quiescent supply current.

6.6 Logistic support. Lead materials and finishes (see 3.4) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class S for National Aeronautics and Space Administration or class B for Department of Defense (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming should not affect the part number.

**6.7 Substitutability.** The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges, post irradiation performance or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information should not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

Military device type	Generic-industry type
01	4081B
02	4082B
03	4073B

**6.8 Changes from previous.** Marginal notations are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of changes.

Custodians:

Army - CR  
Navy - EC  
Air Force - 11  
DLA - CC

Preparing activity:

DLA - CC

(Project 5962-2017)

Review activities:

Army - MI, SM  
Navy - AS, CG, MC, SH, TD  
Air Force – 03, 19, 99

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using ASSIST Online database at [www.dodssp.daps.mil](http://www.dodssp.daps.mil).