

NOTICE OF INACTIVATION
FOR NEW DESIGN

INCH-POUND

MIL-M-38510/292
NOTICE 1
28 July 1995

MILITARY SPECIFICATION
MICROCIRCUITS, DIGITAL, CMOS, 65,536-BIT STATIC RANDOM
ACCESS MEMORY (SRAM), MONOLITHIC SILICON

This notice should be filed in front of MIL-M-38510/292, dated 30 September 1987.

MIL-M-38510/292 is inactive for new design and is no longer used, except for replacement purposes.

The Qualified Products List (QPL) associated with this inactive for new design specification has been canceled; its contents have been incorporated into QML-38535.

Preparing activity:
DLA - ES

(Project 5962-1521)