

INCH-POUND

MIL-M-38510/373A  
10 August 2004  
SUPERSEDING  
MIL-M-38510/373(USAF)  
18 March 1983

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, BIPOLAR, ADVANCED LOW POWER SCHOTTKY TTL,  
NOR GATES, MONOLITHIC SILICON

Inactive for new design after 8 July 1997.

This specification is approved for use by all Departments  
and Agencies of the Department of Defense.

The requirements for acquiring the product herein shall consist of this specification sheet and MIL-PRF 38535

1. SCOPE

1.1 Scope. This specification covers the detail requirements for monolithic silicon, advanced low power Schottky TTL, positive NOR logic gating microcircuits. Two product assurance classes and a choice of case outlines and lead finishes are provided for each type and are reflected in the complete part number. For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535, (see 6.3).

1.2 Part or Identifying Number (PIN). The PIN is in accordance with MIL-PRF-38535, and as specified herein.

1.2.1 Device types. The device types are as follows:

<u>Device type</u>	<u>Circuit</u>
01	Quadruple, 2-input positive NOR gate
02	Triple, 3-input positive NOR gate

1.2.2 Device class. The device class is the product assurance level as defined in MIL-PRF-38535.

1.2.3 Case outlines. The case outlines are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
A	GDFP5-F14 or CDFP6-F14	14	Flat pack
B	GDFP4-14	14	Flat pack
C	GDIP1-T14 or CDIP2-T14	14	Dual-in-line
D	GDFP1-F14 or CDFP2-F14	14	Flat pack
2	CQCC1-N20	20	Square leadless chip carrier,

Comments, suggestions, or questions on this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAS, 3990 East Broad St., Columbus, OH 43216-5000, or emailed to bipolar@dsccl.dla.mil. Since contact information can change, you may want to verify the currency of this address information using the ASSIST Online database at [www.dodssp.daps.mil](http://www.dodssp.daps.mil).

1.3 Absolute maximum ratings.

Supply voltage range .....	-0.5 V dc to +7.0 V dc
Input voltage range .....	-1.5 V dc at -18 mA to +7.0 V dc
Storage temperature range .....	-65° to +150°C
Maximum power dissipation ( $P_D$ ), per device: <u>1/</u>	
Device 01 .....	22 mW
Device 02 .....	21.9 mW
Lead temperature (soldering, 10 seconds) .....	+300°C
Thermal resistance, junction to case ( $\theta_{JC}$ ):	
Cases A, B, C, D, and 2 .....	(See MIL-STD-1835)
Junction temperature ( $T_J$ ) <u>2/</u> .....	175°C

1.4 Recommended operating conditions.

Supply voltage ( $V_{CC}$ ) .....	4.5 V dc minimum to 5.5 V dc maximum
Minimum high level input voltage ( $V_{IH}$ ) .....	2.0 V dc
Maximum low level input voltage ( $V_{IL}$ ) .....	0.8 V dc
Case operating temperature range ( $T_C$ ) .....	-55° to +125°C

## 2. APPLICABLE DOCUMENTS

2.1 General. The documents listed in this section are specified in sections 3, 4, or 5 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document users are cautioned that they must meet all specified requirements of documents cited in sections 3, 4, or 5 of this specification, whether or not they are listed.

2.2 Government documents.

2.2.1 Specifications and standards. The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

## DEPARTMENT OF DEFENSE SPECIFICATIONS

MIL-PRF-38535 - Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

## DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard for Microelectronics.  
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines

(Copies of these documents are available online at <http://assist.daps.dla.mil/quicksearch/> or [www.dodssp.daps.mil](http://www.dodssp.daps.mil) or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.3 Order of precedence. In the event of a conflict between the text of this specification and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

1/ Must withstand the added  $P_D$  due to short-circuit test (e.g.,  $I_{OS}$ ).

2/ Maximum junction temperature should not be exceeded except in accordance with allowable short duration burn-in screening condition in accordance with MIL-PRF-38535.

### 3. REQUIREMENTS

3.1 Qualification. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.4).

3.2 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

3.3 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein.

3.3.1 Case outlines. The case outlines shall be as specified in 1.2.3.

3.3.2 Logic diagrams and terminal connections. The logic diagrams and terminal connections shall be as specified on figure 1.

3.3.3 Truth tables and logic equations. The truth tables and logic equations shall be as specified on figure 2.

3.3.4 Schematic circuits. The schematic circuits shall be maintained by the manufacturer and made available to the qualifying activity and the preparing activity upon request.

3.4 Lead material and finish. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).

3.5 Electrical performance characteristics. The electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range, unless otherwise specified.

3.6 Electrical test requirements. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.

3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.

3.8 Microcircuit group assignment. The devices covered by this specification shall be in microcircuit group number 8 (see MIL-PRF-38535, appendix A).

### 4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.

4.2 Screening. Screening shall be in accordance with MIL-PRF-38535 and shall be conducted on all devices prior to qualification and conformance inspection. The following additional criteria shall apply:

- a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
- c. Additional screening for space level product shall be as specified in MIL-PRF-38535.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T <sub>C</sub> ≤ +125°C unless otherwise specified	Device type	Limits		Unit
				Min	Max	
High level output voltage	V <sub>OH</sub>	V <sub>CC</sub> = 4.5 V, V <sub>IL</sub> = 0.8 V, I <sub>OH</sub> = -400 μA	All	2.5		V
Low level output voltage	V <sub>OL</sub>	V <sub>CC</sub> = 4.5 V, I <sub>OL</sub> = 4 mA, V <sub>IH</sub> = 2.0 V	All		0.4	V
Input clamp voltage	V <sub>IC</sub>	V <sub>CC</sub> = 4.5 V, I <sub>IN</sub> = -18 mA, T <sub>C</sub> = +25°C	All		-1.5	V
Low level input current	I <sub>IL</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0.4 V	All	0	-100	μA
High level input current	I <sub>IH1</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 2.7 V	All		20	μA
	I <sub>IH2</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 7.0 V	All		100	μA
Output current <sup>1/</sup>	I <sub>O</sub>	V <sub>CC</sub> = 5.5 V, V <sub>O</sub> = 2.25 V	All	-20	-112	mA
High level supply current	I <sub>CCH</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0 V	01		2.2	mA
			02		1.8	
Low level supply current	I <sub>CCL</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 4.5 V	All		4.0	mA
Propagation delay time low-to-high level	t <sub>PLH</sub>	V <sub>CC</sub> = 5.0 V, C <sub>L</sub> = 50 pF, R <sub>L</sub> = 500Ω	01	3	14	ns
			02	4	22	
Propagation delay time, high-to-low level	t <sub>PHL</sub>		01	3	11	ns
			02	3	10	

<sup>1/</sup> The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I<sub>OS</sub>.

TABLE II. Electrical test requirements.

MIL-PRF-38535 test requirements	Subgroups (see table III)	
	Class S devices	Class B devices
Interim electrical parameters	1	1
Final electrical test parameters	1*, 2, 3, 9, 10, 11	1*, 2, 3, 9
Group A test requirements	1, 2, 3, 9, 10, 11	1, 2, 3, 9, 10, 11
Group B electrical test parameters when using the method 5005 QCI option	1, 2, 3, 9, 10, 11	N/A
Group C end-point electrical parameters	1, 2, 3, 9, 10, 11	1, 2, 3
Group D end-point electrical parameters	1, 2, 3	1, 2, 3

\*PDA applies to subgroup 1.

4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.

4.4 Technology Conformance Inspection (TCI). Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).

4.4.1 Group A inspection. Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, 6, 7, and 8 shall be omitted.

4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of MIL-PRF-38535.

4.4.3 Group C inspection. Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.

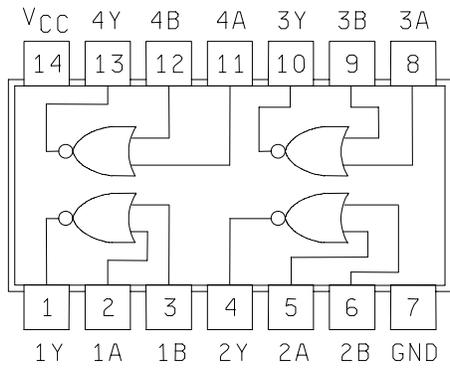
4.4.4 Group D inspection. Group D inspection shall be in accordance with table V of MIL-PRF-38535. End-point electrical parameters shall be as specified in table II herein.

4.5 Methods of inspection. Methods of inspection shall be specified as follows:

4.5.1 Voltage and current. All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

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DEVICE TYPE 01  
CASES A,B,C,AND D



DEVICE TYPE 02  
CASES A,B,C AND D

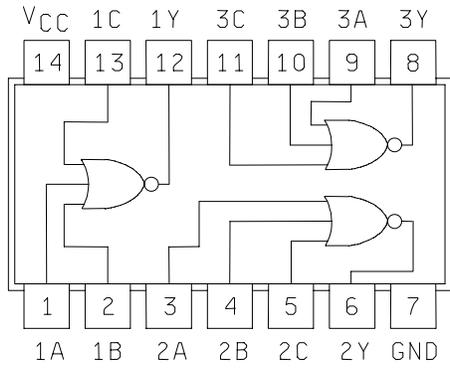
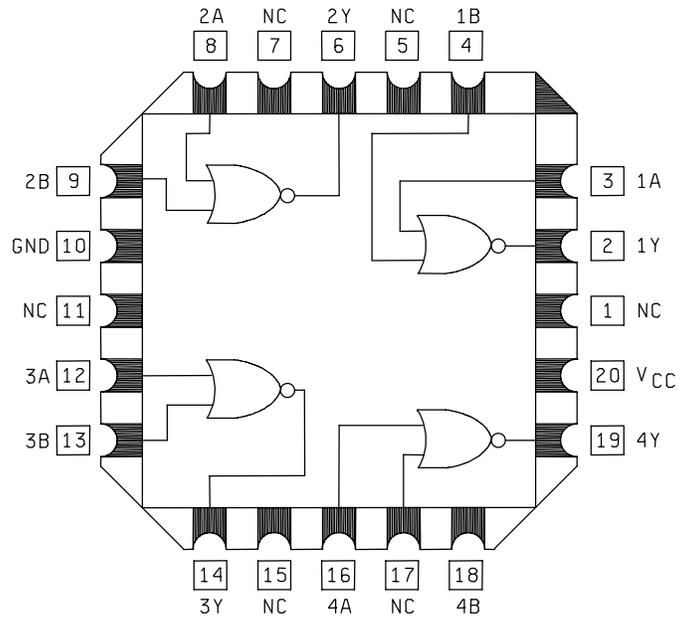


FIGURE 1. Logic diagram and terminal connections (top views).

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DEVICE TYPE 01

CASE 2



DEVICE TYPE 02

CASE 2

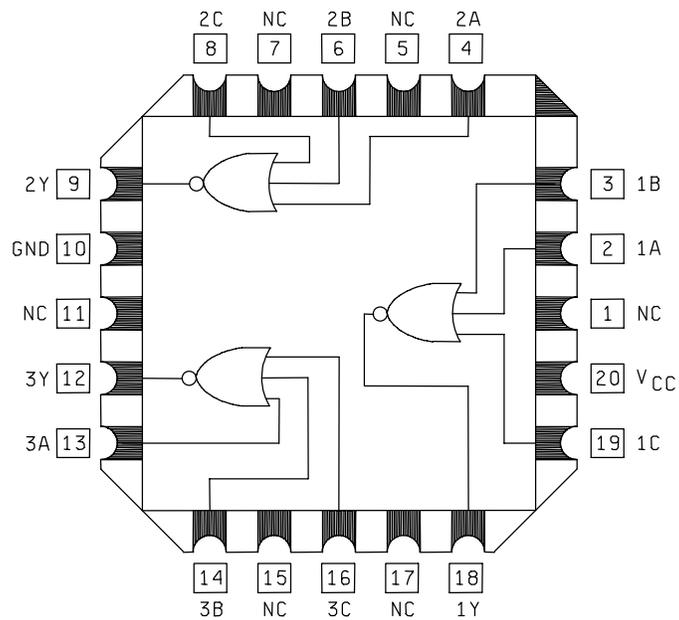


FIGURE 1. Logic diagram and terminal connections (top views) - Continued.

Device type 01

Truth table (each gate)		
INPUTS		OUTPUT
A	B	Y
L	L	H
L	H	L
H	L	L
H	H	L

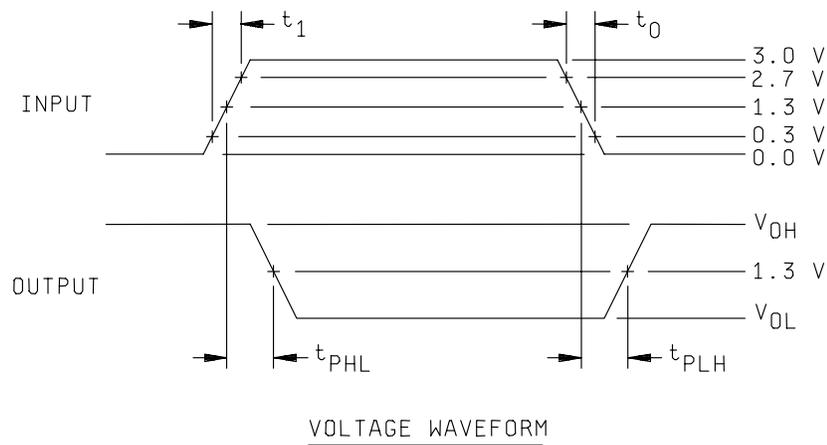
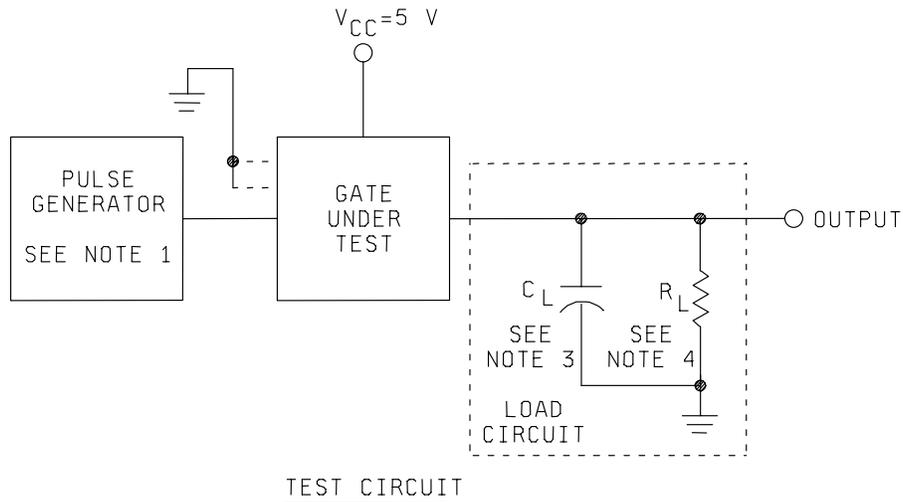
$$Y = \overline{A + B}$$

Device type 02

Truth table (each gate)			
INPUTS			OUTPUT
A	B	C	Y
L	L	L	H
H	L	L	L
L	H	L	L
L	L	H	L
H	H	L	L
H	L	H	L
L	H	H	L
H	H	H	L

$$Y = \overline{A + B + C}$$

FIGURE 2. Truth tables and logic equations.



NOTES:

1. Input pulse has the following characteristics:  
 $t_1 = t_0 = 6 \pm 1.5\text{ ns}$ ,  $\text{PRR} = 1.0\text{ MHz}$ ,  $Z_{\text{OUT}} \cong 50\Omega$ .
2. Inputs not under test are at ground.
3.  $C_L = 50\text{ pF} \pm 10\%$ , including scope probe, wiring and stray capacitance without package in test fixture.
4.  $R_L = 499\Omega \pm 1\%$ .
5. Voltage measurements are to be made with respect to network ground terminal.

FIGURE 3. Switching time test circuit and waveforms for device types 01 through 06.

TABLE III. Group A inspection for device type 01.  
Terminal conditions (pins not designated may be high  $\geq 2.0$  V or low  $\leq 0.8$  V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases A,B,C,D Case 1/2	Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Measured terminal	Limits		Unit			
					2	3	4	5	6	7	8	9	10	11	12	13	14	Min		Max					
1 T <sub>c</sub> = 25°C	V <sub>OH</sub>	3006	1	1	1Y	1A	1B	2Y	2A	2B	GND	3A	3B	3Y	4A	4B	4Y	V <sub>CC</sub>	1Y	2.5		V			
				2	-0.4 mA	0.8 V	0.8 V	-0.4 mA	0.8 V	0.8 V											2Y	"		"	
				3											0.8 V	0.8 V	-0.4 mA	0.8 V	-0.4 mA		3Y	"		"	
				4																	4Y	"		"	
	V <sub>OL</sub>	3007	5	5	4.0 mA	2.0 V	GND												1Y	0.4		"			
				6	4.0 mA	GND	2.0 V														1Y	"		"	
				7				4.0 mA	2.0 V	GND											2Y	"		"	
				8				4.0 mA	GND	2.0 V												2Y	"		"
				9							4.0 mA	2.0 V	GND		2.0 V	GND	4.0 mA					3Y	"		"
				10											GND	2.0 V	4.0 mA				3Y	"		"	
				11														2.0 V	GND	4.0 mA	4Y	"		"	
				12														GND	2.0 V	4.0 mA	4Y	"		"	
				13																	4Y	"		"	
				14																		1A	-1.5		"
				15																		1B	"		"
					I <sub>IL</sub>	3009	21	21															1A	2/	2/
22																					1B	"		"	
23																					2A	"		"	
24												0.4 V										2B	"		"
25													0.4 V									3A	"		"
26																0.4 V						3B	"		"
27																		0.4 V				4A	"		"
28																						4A	"		"
29																						4B	"		"
30																						1A	"	20	"
31													2.7 V									1B	"		"
32														2.7 V								2A	"		"
	I <sub>IH1</sub>	3010	32	32															2B	"		"			
				33																	3A	"		"	
				34																	3A	"		"	
				35																	3B	"		"	
				36																	4A	"		"	
				37																	4A	"		"	
				38																	4B	"		"	
				39																		1A	100		"
				40																		1B	"		"
				41																		2A	"		"
				42																		2B	"		"
				43																		3A	"		"
44																		3B	"		"				
																		4A	"		"				
																		4B	"		"				
																			7.0 V			"			

See footnotes at end of table.

TABLE III. Group A inspection for device type 01 – Continued.  
Terminal conditions (pins not designated may be high  $\geq 2.0V$  or low  $\leq 0.8V$  or open).

Subgroup	Symbol	MIL-STD-883 method	Cases A,B,C,D Case 1/ 2	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Measured terminal		Limits		Unit					
																		Min	Max	Min	Max						
1 $T_c = 25^\circ C$	$I_o$ <sup>3/</sup>	3011	Test no. 45	1Y	1A	1B	2Y	2A	2B	GND	3A	3B	3Y	4A	4B	4Y	$V_{CC}$	1Y	-20 4/	-112 4/	mA						
				2.25 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	2.25 V	"	"	"	"	"	"	"					
2	$I_{OCH}$ $I_{OCL}$	3005 3005	Case 1/ 2		GND	GND	GND	4.5 V	GND	GND	4.5 V	GND	GND	GND	GND	GND	$V_{CC}$	$V_{CC}$	2.2	4.0	"						
3				Same tests, terminal conditions and limits as for subgroup 1, except $T_c = +125^\circ C$ and $V_{IC}$ tests are omitted.																							
9 $T_c = 25^\circ C$	$t_{PH}$	3003 Fig. 3	Case 1/ 2	51	OUT	IN	GND				GND							5.0 V	1A to 1Y	3	12	ns					
				52	OUT	IN															1B to 1Y	"	"	"			
				53				OUT	IN	GND											2A to 2Y	"	"	"	"		
				54				OUT	IN												2B to 2Y	"	"	"	"		
				55										IN	GND	OUT					3A to 3Y	"	"	"	"		
				56										GND	IN	OUT					3B to 3Y	"	"	"	"		
				57												IN	GND	OUT			4A to 4Y	"	"	"	"		
				58													GND	IN	OUT			4B to 4Y	"	"	"	"	
				59	OUT	IN	GND															1A to 1Y	"	10	"		
				60	OUT	GND	IN															1B to 1Y	"	"	"	"	
				61				OUT	IN	GND												2A to 2Y	"	"	"	"	
				62				OUT	IN													2B to 2Y	"	"	"	"	
				63											IN	GND	OUT					3A to 3Y	"	"	"	"	
				64											GND	IN	OUT					3B to 3Y	"	"	"	"	
65														IN	GND	OUT		4A to 4Y	"	"	"	"					
66														GND	IN	OUT		4B to 4Y	"	"	"	"					
10	$t_{PH}$ $t_{PHL}$		Case 1/ 2	Same tests and terminal conditions as subgroup 9, except $T_c = +125^\circ C$ .																							
11			Case 1/ 2	Same tests, terminal conditions and limits as for subgroup 10, except $T_c = -55^\circ C$ .																							

1/ For case 2, pins not referenced are N/C.

2/  $I_{IL}$  limits shall be as follows:

Parameters	Min/Max limits in $\mu A$ for circuit		
	A	B	C
$I_{IL}$	0/-100	0/-100	0/-100

3/ Method 3011 of MIL-STD-883 shall be used, except the output shall be as specified herein, and the output current shall be operating rather than short circuit current. The output conditions have been chosen to produce a current that closely approximates one half of the true short circuit output current,  $I_{OS}$ .

4/  $I_o$  limits for circuit C shall be -30 to -110 mA.



TABLE III. Group A inspection for device type 02 – Continued.  
Terminal conditions (pins not designated may be high  $\geq 2.0$  V or low  $\leq 0.8$  V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases A,B,C,D Case 1/2	Terminal conditions														Limits		Unit						
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	Measured terminal	Min		Max					
1 $T_c = 25^\circ\text{C}$	$I_o$	3011	1A GND	2A GND	2B GND	2C GND	2Y 2.25 V	3A GND	3Y 2.25 V	3A GND	3B GND	3C GND	1Y 2.25 V	1C GND	14 5.5 V	1A to 1Y	-20 $\frac{A}{V}$	-112 $\frac{A}{V}$	mA							
		"	1B GND	2A GND	2B GND	2C GND	2Y 2.25 V	3A GND	3Y 2.25 V	3A GND	3B GND	3C GND	1Y 2.25 V	1C GND	14 5.5 V	1A to 1Y	"	"	"							
		"	1A GND	2A GND	2B GND	2C GND	2Y 2.25 V	3A GND	3Y 2.25 V	3A GND	3B GND	3C GND	1Y 2.25 V	1C GND	14 5.5 V	1A to 1Y	"	"	"	"						
		$I_{ocH}$	3005	1A GND	2A GND	2B GND	2C GND	2Y 2.25 V	3A GND	3Y 2.25 V	3A GND	3B GND	3C GND	1Y 2.25 V	1C GND	14 5.5 V	1A to 1Y	"	"	"	"					
		$I_{ocL}$	3005	1A GND	2A GND	2B GND	2C GND	2Y 2.25 V	3A GND	3Y 2.25 V	3A GND	3B GND	3C GND	1Y 2.25 V	1C GND	14 5.5 V	1A to 1Y	"	"	"	"					
2	Same tests, terminal conditions and limits as for subgroup 1, except $T_c = +125^\circ\text{C}$ and $V_{ic}$ tests are omitted.																									
3	Same tests, terminal conditions and limits as for subgroup 1, except $T_c = 55^\circ\text{C}$ and $V_{ic}$ tests are omitted.																									
9 $T_c = 25^\circ\text{C}$	$I_{pH}$	3003	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	4	15	ns		
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
		"	54 IN	55 GND	56 GND	57 IN	58 GND	59 GND	60 GND	61 GND	62 GND	63 IN	64 GND	65 GND	66 GND	67 GND	68 GND	69 GND	70 GND	71 GND	1A to 1Y	"	"	"	"	"
	10	$I_{pH}$	"	Same tests and terminal conditions as subgroup 9, except $T_c = +125^\circ\text{C}$ .																	4	22	"			
	$I_{pL}$	"	Same tests and terminal conditions as subgroup 9, except $T_c = +125^\circ\text{C}$ .																	3	10	"				
11	Same tests, terminal conditions and limits as for subgroup 10, except $T_c = -55^\circ\text{C}$ .																									

1/ For case 2, pins not referenced are N/C.

2/  $I_{iL}$  limits shall be as follows:

Parameters	Min/Max limits in $\mu\text{A}$ for circuit		
	A	B	C
$I_{iL}$	0/-100	0/-100	0/-100

3/ Method 3011 of MIL-STD-883 shall be used, except the output shall be as specified herein, and the output current shall be operating rather than short circuit current. The output conditions have been chosen to produce a current that closely approximates one half of the true short circuit output current,  $I_{os}$ .

4/  $I_o$  limits for circuit C shall be -30 to -110 mA.

## 5. PACKAGING

5.1 Packaging requirements. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When actual packaging of materiel is to be performed by DoD or in-house contractor personnel, these personnel need to contact the responsible packaging activity to ascertain packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Service or Defense Agency, or within the military service's system command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

## 6. NOTES

6.1 Intended use. Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.

6.2 Acquisition requirements. Acquisition documents should specify the following:

- a. Title, number, and date of the specification.
- b. PIN and compliance identifier, if applicable (see 1.2).
- c. Requirements for delivery of one copy of the conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
- d. Requirements for certificate of compliance, if applicable.
- e. Requirements for notification of change of product or process to contracting activity in addition to notification to the qualifying activity, if applicable.
- f. Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
- g. Requirements for product assurance options.
- h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements should not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
- i. Requirements for "JAN" marking.
- j. Packaging requirements (see 5.1).

6.3 Superseding information. The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractor's parts lists.

6.4 Qualification. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, 3990 E. Broad Street, Columbus, Ohio 43123-1199.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535, MIL-HDBK-1331, and as follows:

GND ..... Ground zero voltage potential  
 $V_{IN}$  ..... Voltage level at an input terminal  
 $I_{IN}$  ..... Current flowing into an input terminal

6.6 Logistic support. Lead materials and finishes (see 3.4) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming should not affect the part number.

6.7 Substitutability. The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-35810 device types and may have slight physical variations in relation to case size. The presence of this information should not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

Military device type	Generic-industry type
01	54ALS02
02	54ALS27

6.8 Manufacturers' designation. Manufacturers' circuits which form a part of this specification are designated with an "X" as shown in table IV herein.

TABLE IV. Manufacturers' designations.

Device type	Circuits		
	A	B	C
	Texas Instruments	Motorola Inc.	National Semiconductor/ Fairchild Semiconductor
01	X	X	X
02	X	X	X

6.9 Changes from previous issue. Marginal notations are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of the changes.

Custodians:  
 Army - CR  
 Navy - EC  
 Air Force - 11  
 DLA - CC

Preparing activity:  
 DLA - CC  
 (Project 5962-2055)

Review activities:  
 Army - MI, SM  
 Navy - AS, CG, MC, SH, TD  
 Air Force - 03, 19, 99

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at [www.dodssp.daps.mil](http://www.dodssp.daps.mil).