

INCH-POUND
MIL-M-38510/55F
AMENDMENT 3
<u>28 APRIL 1999</u>
SUPERSEDING
AMENDMENT 2
30 September 1988

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, CMOS, BUFFER/CONVERTER,
TRUE/COMPLEMENT BUFFER, MONOLITHIC SILICON

Inactive for new design after 9 August 1996

This amendment forms a part of MIL-M-38510/55F, dated 30 April 1984,
and is approved for use by all Departments and Agencies of the Department of Defense.

PAGE 1

1.2.3, add new case outlines as follows:

"N	F-5A (16-lead, .440" x .285" x .115"), flat package
T	F-2A (14-lead, .390" x .260" x .115"), flat package"

The attached insertable replacement pages listed below are replacements for stipulated pages. When the new pages have been entered in the document, insert the amendment as the cover sheet to the specification.

<u>Replacement page</u>	<u>Page replaced</u>
5	5
6	6
25	25
26	26
29	29
30	30
33	33
34	34
35	35
36	36

1.3, delete and substitute:

“1.3 Absolute maximum ratings.

Device types 01 and 02

Supply voltage range ($V_{DD} - V_{SS}$):

$V_{CC} \leq V_{DD}$ ----- -0.5 V to +15.5 V

Output load capacitance (each output) ----- 200 pF when $V_{CC} > 10$ V

Input voltage range ----- $(V_{SS} - 0.5\text{ V}) \leq VI \leq (V_{DD} + 0.5\text{ V})$

Device types 51 and 52

Supply voltage range ($V_{DD} - V_{SS}$):

$V_{CC} \leq V_{DD}$ ----- -0.5 V to +18.0 V

Output load capacitance (each output) ----- 200 pF when $V_{CC} > 10$ V

Input voltage range ----- $(V_{SS} - 0.5\text{ V}) \leq VI \leq (V_{DD} + 0.5\text{ V})$

DC output source or sink current per pin ----- $I_{OH} = -4.0\text{ mA}$

----- $I_{OL} = +12.0\text{ mA}$

DC supply current, per pin (I_{DD}, I_{CC}) ----- -25.0 mA

DC ground current (I_{GND}) ----- +50.0 mA

Device type 03 and 04

Supply voltage range ($V_{CC} - V_{SS}$) ----- -0.5 V to +15.5 V

Input voltage range ----- $(V_{SS} - 0.5\text{ V}) \leq VI \leq (V_{DD} + 0.5\text{ V})$

Device type 53 and 54

Supply voltage range ($V_{CC} - V_{SS}$) ----- -0.5 V to +18.0 V

Input voltage range ----- $(V_{SS} - 0.5\text{ V}) \leq VI \leq (V_{DD} + 0.5\text{ V})$

DC output source or sink current per pin ----- I_{OH} or $I_{OL} = \pm 12.0\text{ mA}$

DC supply or ground current per pin (I_{CC}, I_{GND}) ----- $\pm 50\text{ mA}$

Device type 05

Supply voltage range ($V_{DD} - V_{SS}$) ----- -0.5 V to +15.0 V

Input voltage range ----- $(V_{SS} - 0.5\text{ V}) \leq VI \leq (V_{DD} + 0.5\text{ V})$

Device type 55

Supply voltage range ($V_{DD} - V_{SS}$) ----- -0.5 V to +18.0 V

Input voltage range ----- $(V_{SS} - 0.5\text{ V}) \leq VI \leq (V_{DD} + 0.5\text{ V})$

DC output source or sink current per pin:

True compliment ----- I_{OH} or $I_{OL} = \pm 12.0\text{ mA}$

Compliment output ----- I_{OH} or $I_{OL} = \pm 6.0\text{ mA}$

DC supply or ground current per pin (I_{DD}, I_{GND}) ----- $\pm 50\text{ mA}$

All device types

Input current (each input) ----- $\pm 10\text{ mA}$

Storage temperature range ----- -65°C to +175°C

Maximum power dissipation (P_D) ----- 200 mW

Lead temperature (soldering, 10 seconds) ----- +300°C

Thermal resistance, junction-to-case ----- See MIL-M-38510, appendix C

Junction temperature ----- $T_J = +175^\circ\text{C}$ "

PAGE 4

4.2a: Delete "3.1.9 through 3.1.13" and substitute "initial (pre-burn-in) electrical parameters (3.1.10) through interim (post-burn-in) electrical parameters (3.1.14)".

PAGE 7

Table I, t_{PHL} , t_{PLH} , t_{THL} , and t_{TLH} , min limits column: Delete all entries and substitute "6".

PAGE 8

Table II, line 9: Insert "10 and 11" in subgroup column for class B.

Line 12: Delete entire line.

Line 13: Delete "13" and substitute "12".

PAGE 9

4.4.1, insert 4.4.1d and 4.4.1e as follows:

"d. Subgroups 9 and 11 shall be measured only for initial qualification and after process or design changes which may affect dynamic performance.

e. When the 01 through 03 device types are qualified by extension (see 4.3.1), these device types will be inspected (QCI) according to the 51 through 53 device type requirements respectively."

PAGES 15 16

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

PAGE 16

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

*TABLE III, I_{IH1} , Max limits column: change "6" to "600".

*TABLE III, I_{IH2} , Max limits column: change "1.0" and "45.0" to "100.0" and "100.0" respectively.

*TABLE III, I_{IL1} , Max limits column: change "-6" to "-600.0".

*TABLE III, I_{IL2} , Max limits column: change "-1.0" and "-45.0" to "-100.0" and "-100.0" respectively.

PAGE17

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

Table III, t_{PHL} , min limits columns: Delete "7/2" throughout and substitute "6", "9", "6", respectively. t_{PLH} , min limits columns: Delete "10/8" throughout and substitute "6", "9", "6", respectively. t_{THL} , min limits columns: Delete "20/5" throughout and substitute "6", "9", "6", respectively. t_{TLH} , min limits columns: Delete "20" throughout and substitute "6", "9", "6", respectively.

PAGE 18

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

PAGE 19

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

*TABLE III, I_{IH1} , Max limits column: change "6" to "600".

*TABLE III, I_{IH2} , Max limits column: change "1.0" and "45.0" to "100.0" and "100.0" respectively.

*TABLE III, I_{IL1} , Max limits column: change "-6" to "-600.0".

*TABLE III, I_{IL2} , Max limits column: change "-1.0" and "-45.0" to "-100.0" and "-100.0" respectively.

PAGE 20

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

Table III, t_{PHL} , min limits columns: Delete "7/11" throughout and substitute "6", "9", "6", respectively. t_{PLH} , min limits columns: Delete "10/17" throughout and substitute "6", "9", "6", respectively. t_{THL} , min limits columns: Delete "20/5" throughout and substitute "6", "9", "6", respectively. t_{TLH} , min limits columns: Delete "20" throughout and substitute "6", "9", "6", respectively.

PAGE 23

Table III, test 112, pin column 11: Delete "OUT"; pin 12: Delete "IN" and substitute "OUT"; pin column 13: Add "IN".

Table III, t_{PHL} , min limits columns: Delete "8" throughout and substitute "6", "9", "6", respectively. t_{PLH} , min limits columns: Delete "9" throughout and substitute "6", "9", "6", respectively. t_{THL} , min limits columns: Delete "8" throughout and substitute "6", "9", "6", respectively. t_{TLH} , min limits columns: Delete "9" throughout and substitute "6", "9", "6", respectively.

PAGE 27

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

*TABLE III, I_{IH2} , Max limits column: change "1.0" and "45.0" to "100.0" and "100.0" respectively.

*TABLE III, I_{IL1} , Max limits column: change "-6" to "-600.0".

*TABLE III, I_{IL2} , Max limits column: change "-1.0" and "-45.0" to "-100.0" and "-100.0" respectively.

PAGE 28

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

Table III: t_{PHL} , min limits columns: Delete "5/2", "7/2", "5/2" and substitute "6", "9", "6", respectively. t_{PLH} , min limits columns: Delete "7/8", "10/8", "7/8" and substitute "6", "9", "6", respectively. t_{THL} , min limits columns: Delete "3/3", "10/8", "3/3" and substitute "6", "9", "6", respectively. t_{TLH} , min limits columns: Delete "13", "20", "13" and substitute "6", "9", "6", respectively.

PAGE 31

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

*TABLE III, I_{IH2} , Max limits column: change "1.0" and "45.0" to "100.0" and "100.0" respectively.

*TABLE III, I_{IL1} , Max limits column: change "-6" to "-600.0".

*TABLE III, I_{IL2} , Max limits column: change "-1.0" and "-45.0" to "-100.0" and "-100.0" respectively.

PAGE 32

Table III: Delete "Cases E, F, Z" and substitute "Cases E, F, N, and Z".

Table III: t_{PHL} , min limits columns: Delete "5/7", "7/11", "5/7" and substitute "6", "9", "6", respectively. t_{PLH} , min limits columns: Delete "7/11", "10/17", "7/11" and substitute "6", "9", "6", respectively. t_{THL} , min limits columns: Delete "3", "4/5", "3" and substitute "6", "9", "6", respectively. t_{TLH} , min limits columns: Delete "13", "20", "13" and substitute "6", "9", "6", respectively.

PAGE 37

4.4.3: Delete 4.4.3c and substitute as follows:

"c. When the 01 through 03 device types are qualified by extension (see 4.3.1), these device types will be inspected (QCI) according to the 51 through 53 device type requirements respectively."

6.3: Delete “ V_{DD} ” and substitute “ V_{DD} and V_{CC} ”.

After I_{SS} add:

“ I_{DD} and I_{CC} ----- DC supply current.
 I_{GND} ----- DC ground current.”

The margins of this amendment are marked with an asterisk to indicate where changes (additions, modifications, corrections, deletions) from the previous amendment were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous amendment.

CONCLUDING MATERIAL

Custodians:

Army - CR
Navy - EC
Air Force - 11
NASA - NA
DLA – CC

Preparing activity:
DLA - CC

Review activities:

Navy - AS, CG, MC, OS, SH
Army - AR, MI, SM
Air Force - 19, 85, 99

(Project 5962-1838)

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $V_{SS} = 0 \text{ V}$, $-55^\circ\text{C} \leq T_C \leq +125^\circ\text{C}$ unless otherwise specified	Device Type 1/	Limits		Unit
				Min	Max	
Positive clamping input to V_{DD} or V_{CC}	$V_{IC(pos)}$	$T_C = +25^\circ\text{C}$, V_{DD} and $V_{CC} = \text{GND}$ $V_{SS} = \text{Open}$, Output = Open, $I_I = 1 \text{ mA}$	01, 02, 05, 51, 52, 55		+1.5	V dc
Negative clamping input to V_{SS}	$V_{IC(neg)}$	$T_C = +25^\circ\text{C}$, V_{DD} and $V_{CC} = \text{GND}$ $V_{SS} = \text{Open}$, Output = Open, $I_I = 1 \text{ mA}$	All		-6.0	V dc
Quiescent supply current	I_{SS}	V_{DD} and $V_{CC} = 15 \text{ V dc}$, any combination of inputs	01 - 05		-750	nA dc
		V_{DD} and $V_{CC} = 15 \text{ V dc}$, any combination of inputs	51 - 55		-750	nA dc
High level output voltage	V_{OH1}	V_{DD} and $V_{CC} = 4.5 \text{ V dc}$ $I_{OH} = -0.1 \text{ mA}$ (see table III)	01 - 05	2.50		V dc
	V_{OH2}	V_{DD} and $V_{CC} = 5.0 \text{ V dc}$ $I_{OH} = -0.35 \text{ mA}$ (see table III)	01 - 05	4.5		V dc
	V_{OH3}	V_{DD} and $V_{CC} = 5.0 \text{ V dc}$ $I_{OH} = -0.0 \text{ mA}$ (see table III)	01 - 05	4.95		V dc
	V_{OH4}	V_{DD} and $V_{CC} = 12.5 \text{ V dc}$ $I_{OH} = -0.0 \text{ mA}$ (see table III)	01 - 05	11.25		V dc
	V_{OH5}	V_{DD} and $V_{CC} = 15.0 \text{ V dc}$ $I_{OH} = -0.1 \text{ mA}$ (see table III)	51 - 55	14.95		V dc
Low level output voltage	V_{OL1}	V_{DD} and $V_{CC} = 5.5 \text{ V dc}$ $I_{OL} = -0.23 \text{ mA}$ (see table III)	01 - 05		0.5	V dc
	V_{OL2}	V_{DD} and $V_{CC} = 5.0 \text{ V dc}$ $I_{OL} = -2.1 \text{ mA}$ (see table III)	01 - 05		0.5	V dc
	V_{OL3}	V_{DD} and $V_{CC} = 5.0 \text{ V dc}$ $I_{OL} = -0.0 \text{ mA}$ (see table III)	01 - 05		0.05	V dc
	V_{OL4}	V_{DD} and $V_{CC} = 12.5 \text{ V dc}$ $I_{OL} = -0.0 \text{ mA}$ (see table III)	01 - 05		1.25	V dc
	V_{OL5}	V_{DD} and $V_{CC} = 15.0 \text{ V dc}$ $I_{OL} = -0.0 \text{ mA}$ (see table III)	51 - 55		0.05	V dc

See footnotes at end of table.

Supersedes page 5 of MIL-M-38510/55F
Of 30 April 1984

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $V_{SS} = 0 \text{ V}$, $-55^\circ\text{C} \leq T_C \leq +125^\circ\text{C}$ unless otherwise specified	Device Type 1/	Limits		Unit
				Min	Max	
Input high voltage	V_{IH1}	$V_{DD} \text{ and } V_{CC} = 5.0 \text{ V dc}$	1, 53	4.0		V dc
		$V_O = (\text{see table III}), I_O \leq 1.0 \mu\text{A}$	52, 54, 55	3.5		
	V_{IH2}	$V_{DD} \text{ and } V_{CC} = 10.0 \text{ V dc}$	51, 53	8.0		V dc
		$V_O = (\text{see table III}), I_O \leq 1.0 \mu\text{A}$	52, 54, 55	7.0		
	V_{IH3}	$V_{DD} \text{ and } V_{CC} = 15.0 \text{ V dc}$	51, 53	12.0		V dc
		$V_O = (\text{see table III}), I_O \leq 1.0 \mu\text{A}$	52, 54, 55	11.0		
Input low voltage	V_{IL1}	$V_{DD} \text{ and } V_{CC} = 5.0 \text{ V dc}$	51, 53		1.0	V dc
		$V_O = (\text{see table III}), I_O \leq 1.0 \mu\text{A}$	52, 54, 55		1.5	
	V_{IL2}	$V_{DD} \text{ and } V_{CC} = 10.0 \text{ V dc}$	51, 53		2.0	V dc
		$V_O = (\text{see table III}), I_O \leq 1.0 \mu\text{A}$	52, 54, 55		3.0	
	V_{IL3}	$V_{DD} \text{ and } V_{CC} = 15.0 \text{ V dc}$	51, 53		2.5	V dc
		$V_O = (\text{see table III}), I_O \leq 1.0 \mu\text{A}$	52, 54, 55		4.0	
Output low (sink) current	I_{OL1}	$V_{DD} \text{ and } V_{CC} = 5.0 \text{ V dc}$	51 - 54	2.2		mA dc
		$V_{IN} = (\text{see table III})$	55	1.2		
		$V_{OL} = 0.4 \text{ V dc}$		0.55		
	I_{OL2}	$V_{DD} \text{ and } V_{CC} = 5.0 \text{ V dc}$	51 - 54	17.0		mA dc
		$V_{IN} = (\text{see table III})$	55	8.0		
		$V_{OL} = 0.4 \text{ V dc}$		3.0		
	I_{OH1}	$V_{DD} \text{ and } V_{CC} = 5.0 \text{ V dc}$	51 - 54	2.2		mA dc
		$V_{IN} = (\text{see table III})$	55	1.2		
		$V_{OL} = 0.4 \text{ V dc}$		0.55		
Output high (sink) current	I_{OH2}	$V_{DD} \text{ and } V_{CC} = 5.0 \text{ V dc}$	51 - 54	17.0		mA dc
		$V_{IN} = (\text{see table III})$	55	8.0		
		$V_{OL} = 0.4 \text{ V dc}$		3.0		
*Input leakage current	I_{IH}	$V_{DD} \text{ and } V_{CC} = 15.0 \text{ V dc}$	01 - 05		100.0	nA
		$V_{DD} \text{ and } V_{CC} = 18.0 \text{ V dc}$	51 - 55			
	I_{IL}	$V_{DD} \text{ and } V_{CC} = 15.0 \text{ V dc}$	01 - 05		-100.0	nA
		$V_{DD} \text{ and } V_{CC} = 18.0 \text{ V dc}$	51 - 55			
Input test voltage	V_{ZAP}	$C_1 = 100 \text{ pF}, R_Z = 1.5 \text{ k}\Omega, (\text{see 4.5.3})$	All	400		V

See footnote at end of table.

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of 30 April 1984

MIL-M-38510/55F

Amendment 3

TABLE III. Group A inspection for device types 51 and 53.

See footnotes at end of device type 55 , Supersedes page 25 of MIL-M-38510/55F dated 30 April 1984

MIL-M-38510/55F

Amendment 3

TABLE III. Group A inspection for device types 51 and 53 – Continued.

Symbol	MIL-STD-883 method	Cases E, F, Z	For terminal conditions and limits see 1/												Measured terminal	Test limits	Unit	
			Subgroup 1 TC = 25°C						Subgroup 2 TC = 125°C			Subgroup 3 TC = -55°C						
			1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16 2/
V _{L1}	45	5V	Y1	A1	Y2	A2	Y3	A3	V _{SS}	A4	Y4	A5	Y5	NC	A6	Y6	V _{DD}	
V _{L1}	46	5V	1.0 V GND	GND	1.0 V GND	GND	1.0 V GND	GND	GND	GND	GND	1.0 V GND	GND	5V	Y1	1.0	1.0	
V _{L1}	47	5V	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	Y2	1.0	1.0	Vdc	
V _{L1}	48	5V	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	Y3	1.0	1.0	1.0	
V _{L1}	49	5V	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	Y4	1.0	1.0	1.0	
V _{L1}	50	5V	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	1.0 V GND	Y5	1.0	1.0	1.0	
V _{L1}	51	10V	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	GND	10V	Y1	2.0	2.0
V _{L1}	52	10V	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	Y2	2.0	2.0	2.0	
V _{L1}	53	10V	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	Y3	2.0	2.0	2.0	
V _{L1}	54	10V	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	Y4	2.0	2.0	2.0	
V _{L1}	55	10V	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	Y5	2.0	2.0	2.0	
V _{L1}	56	10V	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	2.0 V GND	Y6	2.0	2.0	2.0	
V _{L1}	57	15V	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	GND	15V	Y1	2.5	2.5
V _{L1}	58	15V	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	Y2	2.5	2.5	2.5	
V _{L1}	59	15V	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	Y3	2.5	2.5	2.5	
V _{L1}	60	15V	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	Y4	2.5	2.5	2.5	
V _{L1}	61	15V	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	Y5	2.5	2.5	2.5	
V _{L1}	62	15V	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	2.5 V GND	Y6	2.5	2.5	2.5	
I _{O1}	63	5V	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	GND	5V	Y1	3.2	2.2
I _{O1}	64	5V	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	Y2	4.0	4.0	mA	
I _{O1}	65	5V	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	Y3	4.0	4.0	mA	
I _{O1}	66	5V	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	Y4	4.0	4.0	mA	
I _{O1}	67	5V	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	Y5	4.0	4.0	mA	
I _{O1}	68	5V	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	5V GND	0.4 V	Y6	4.0	4.0	mA	
I _{O12}	69	15V	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	GND	15V	Y1	24.0	17.0
I _{O12}	70	15V	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	Y2	15V	Y1	30.0	30.0
I _{O12}	71	15V	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	Y3	15V	Y1	30.0	30.0
I _{O12}	72	15V	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	Y4	15V	Y1	30.0	30.0
I _{O12}	73	15V	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	Y5	15V	Y1	30.0	30.0
I _{O12}	74	15V	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	15V GND	1.5 V	Y6	15V	Y1	30.0	30.0
I _{O12}	75	5V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	GND	5V	Y1	-0.51	-0.64
I _{O12}	76	5V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	Y2	5V	Y1	-0.51	-0.64
I _{O12}	77	5V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	Y3	5V	Y1	-0.51	-0.64
I _{O12}	78	5V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	Y4	5V	Y1	-0.51	-0.64
I _{O12}	79	5V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	Y5	5V	Y1	-0.51	-0.64
I _{O12}	80	5V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	4.6 V	Y6	5V	Y1	-0.51	-0.64
I _{H1}	81	15V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	GND	15V	Y1	-3.4	-2.4
I _{H1}	82	15V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	Y2	15V	Y1	-3.4	-2.4
I _{H1}	83	15V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	Y3	15V	Y1	-3.4	-2.4
I _{H1}	84	15V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	Y4	15V	Y1	-3.4	-2.4
I _{H1}	85	15V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	Y5	15V	Y1	-3.4	-2.4
I _{H1}	86	15V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	13.5 V	Y6	15V	Y1	-3.4	-2.4
*I _{H1}	3010	87	18V	18V	18V	18V	18V	18V	18V	18V	18V	18V	18V	18V	18V	18V	600	nA

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TABLE III. Group A inspection for device types 52 and 54.

Symbol	ML-STD-883 method	Cases E, F, N, And Z	For terminal conditions and limits see 1/																Measured terminal	Test limits	Unit	
			Subgroup 1 TC = 25°C				Subgroup 2 TC = 125°C				Subgroup 3 TC = -55°C											
			1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16 2/				
V _{ICP05}	2/	1	GND	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	GND	A1 A2 A3 A4 A5 A6	V _{dc}	
V _{ICN05}	8	9	10	11	12														"	"	"	
I _{SS}	3005 4/ 3005 4/	13	18 V	18 V	18 V	18 V	18 V	18 V	18 V	18 V	18 V	18 V	18 V	18 V	18 V	18 V	18 V	18 V	A1 A2 A3 A4 A5 A6	-6.0 -4.0 -4.0 -4.0 -4.0 -4.0	V _{dc}	
V _{OH5}	3006	15	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	Y1 Y2 Y3 Y4 Y5 Y6	-750 -750	nAdc	
VOL5	3007	21	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	15 V	Y1 Y2 Y3 Y4 Y5 Y6	14.95 14.95 14.95 14.95 14.95 14.95	V _{dc}	
VIH1	27	5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	3.5 V	Y1 Y2 Y3 Y4 Y5 Y6	4.5 4.5 4.5 4.5 4.5 4.5	4.5	
VIH2	33	10 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	7.0 V	Y1 Y2 Y3 Y4 Y5 Y6	9.0 9.0 9.0 9.0 9.0 9.0	9.0	
VIH3	39	15 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	11.0 V	Y1 Y2 Y3 Y4 Y5 Y6	13.5 13.5 13.5 13.5 13.5 13.5	13.5	

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TABLE III. Group A inspection for device types 52 and 54 – Continued.

Symbol	MIL-STD-883 method Cases E, F, Z	For terminal conditions and limits see 1/												Measured terminal	Test limits	Unit					
		1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 2/						Subgroup 1 TC = 25°C		Subgroup 2 TC = 125°C		Subgroup 3 TC = -55°C									
		Test No.	V _{CC}	Y1	A1	Y2	A2	Y3	A3	V _{SS}	A4	Y4	A5	Y5	NC	A6	Y6	V _{DD}			
V _{L1}	45 46 47 48 49 50	5V	1.5V GND	GND 1.5V GND	GND 1.5V GND	GND 1.5V GND	GND 1.5V GND	GND 1.5V GND	GND 1.5V GND	GND 1.5V GND	GND 1.5V GND	GND 1.5V GND	GND 1.5V GND	5V	Y1 Y2 Y3 Y4 Y5 Y6	Y1 Y2 Y3 Y4 Y5 Y6	0.5 0.5 0.5 0.5 0.5 0.5	0.5 0.5 0.5 0.5 0.5 0.5	Vdc		
V _{L2}	51 52 53 54 55 56	10V	3.0V GND	3.0V GND	3.0V GND	3.0V GND	3.0V GND	3.0V GND	3.0V GND	3.0V GND	3.0V GND	3.0V GND	3.0V GND	3.0V	10V	Y1 Y2 Y3 Y4 Y5 Y6	Y1 Y2 Y3 Y4 Y5 Y6	1.0 1.0 1.0 1.0 1.0 1.0	1.0 1.0 1.0 1.0 1.0 1.0	uA	
V _{L3}	57 58 59 60 61 62	15V	4.0V GND	4.0V GND	4.0V GND	4.0V GND	4.0V GND	4.0V GND	4.0V GND	4.0V GND	4.0V GND	4.0V GND	4.0V GND	4.0V	15V	Y1 Y2 Y3 Y4 Y5 Y6	Y1 Y2 Y3 Y4 Y5 Y6	1.5 1.5 1.5 1.5 1.5 1.5	1.5 1.5 1.5 1.5 1.5 1.5	uA	
I _{O1}	63 64 65 66 67 68	5V	0.4V	0.4V	0.4V	0.4V	0.4V	0.4V	0.4V	0.4V	0.4V	0.4V	0.4V	0.4V	GND	5V	Y1 Y2 Y3 Y4 Y5 Y6	Y1 Y2 Y3 Y4 Y5 Y6	3.2 3.2 3.2 3.2 3.2 3.2	2.2 2.2 2.2 2.2 2.2 2.2	mA
I _{O2}	69 70 71 72 73 74	15V	1.5V	1.5V	1.5V	1.5V	1.5V	1.5V	1.5V	1.5V	1.5V	1.5V	1.5V	1.5V	15V	Y1 Y2 Y3 Y4 Y5 Y6	Y1 Y2 Y3 Y4 Y5 Y6	24.0 24.0 24.0 24.0 24.0 24.0	17.0 17.0 17.0 17.0 17.0 17.0	mA	
I _{OH}	75 76 77 78 79 80	5V	4.6V	5.0V GND	4.6V	5.0V GND	4.6V	5.0V GND	4.6V	5.0V GND	4.6V	5.0V GND	4.6V	5.0V	5V	Y1 Y2 Y3 Y4 Y5 Y6	Y1 Y2 Y3 Y4 Y5 Y6	-0.51 -0.51 -0.51 -0.51 -0.51 -0.51	-0.64 -0.64 -0.64 -0.64 -0.64 -0.64	mA	
*I _{HI} 2/	81 82 83 84 85 86	15V	13.5V	15V GND	13.5V	15V GND	13.5V	15V GND	13.5V	15V GND	13.5V	15V GND	13.5V	15V	15V	Y1 Y2 Y3 Y4 Y5 Y6	Y1 Y2 Y3 Y4 Y5 Y6	-3.4 -3.4 -3.4 -3.4 -3.4 -3.4	-2.4 -2.4 -2.4 -2.4 -2.4 -2.4	uA	
	3010	87	18V		18V		18V		18V		18V		18V		18V	All Inputs together		600		nA	

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TABLE III. Group A inspection for device type 55.

Symbol	MIL-STD-883 method	Cases E, F, N, And Z	For terminal conditions and limits see 1/												Measured terminal	Test limits	Unit			
			Subgroup 1 TC = 25°C				Subgroup 2 TC = 125°C				Subgroup 3 TC = -55°C									
			Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max						
V _{IPOS} _u	3/	1	Y1	Y ₁	A1	Y ₂	Y ₂	A2	V _{SS}	Y ₃	Y ₃	A3	Y ₄	A4	V _{DD}	GND	1.5			
V _{IPOS} _u	2/	2			1 mA				1 mA							A1 A2 A3 A4	u	u		
V _{IPOS} _u	3/	3															u	u		
V _{IPOS} _u	4/	4															u	u		
V _{ICNEG} _u	6	5			-1 mA				GND								u	u		
V _{ICNEG} _u	6	6							GND								u	u		
V _{ICNEG} _u	7	7							GND								u	u		
V _{ICNEG} _u	8	8							GND								u	u		
V _{ICNEG} _u	9	9							GND								u	u		
V _{ICNEG} _u	10	10							GND								u	u		
V _{OHS} _u	11	3006	11		15 V				15 V								Y1 Y2 Y3 Y4	14.95		
V _{OHS} _u	12	12							15 V								u	u		
V _{OHS} _u	13	13							15 V								u	u		
V _{OHS} _u	14	14							15 V								u	u		
V _{OHS} _u	15	15							15 V								u	u		
V _{OHS} _u	16	16							15 V								u	u		
V _{OHS} _u	17	17							15 V								u	u		
V _{OHS} _u	18	18							15 V								u	u		
V _{OHS} _u	19	3007	19		GND				GND								Y1 Y2 Y3 Y4	0.05		
V _{OHS} _u	20	20							GND								u	u		
V _{OHS} _u	21	21							GND								u	u		
V _{OHS} _u	22	22							GND								u	u		
V _{OHS} _u	23	23							GND								u	u		
V _{OHS} _u	24	24							GND								u	u		
V _{OHS} _u	25	25							GND								u	u		
V _{OHS} _u	26	26							GND								u	u		
V _{IHT} _u	27		3.5 V	GND					GND								Y1 Y2 Y3 Y4	4.5		
V _{IHT} _u	28		3.5 V	GND					GND								u	u		
V _{IHT} _u	29		3.5 V	GND					GND								u	u		
V _{IHT} _u	30		3.5 V	GND					GND								u	u		
V _{IHT} _u	31		3.5 V	GND					GND								u	u		
V _{IHT} _u	32		3.5 V	GND					GND								u	u		
V _{IHT} _u	33		3.5 V	GND					GND								u	u		
V _{IHT} _u	34		3.5 V	GND					GND								u	u		
V _{IHT} _u	35		7.0 V	GND					GND								Y1 Y2 Y3 Y4	4.5		
V _{IHT} _u	36		7.0 V	GND					GND								u	u		
V _{IHT} _u	37		7.0 V	GND					GND								u	u		
V _{IHT} _u	38		7.0 V	GND					GND								u	u		
V _{IHT} _u	39		7.0 V	GND					GND								u	u		
V _{IHT} _u	40		7.0 V	GND					GND								u	u		
V _{IHT} _u	41		7.0 V	GND					GND								u	u		
V _{IHT} _u	42		7.0 V	GND					GND								u	u		

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TABLE III. Group A inspection for device type 55 – Continued.

Symbol	ML- STD-883 method	Cases E, F, And Z	For terminal conditions and limits see 1/												Measured terminal	Test limits					
			Subgroup 1 TC = 25°C						Subgroup 2 TC = 125°C			Subgroup 3 TC = -55°C				Min	Max	Min	Max		
			Test No.	Y1	Y1	A1	Y2	A2	V _{SS}	Y3	Y3	A3	Y4	Y4	A4	V _{DD}					
V _{Hi3} _u	43			11.0 V GND	11.0 V GND		GND		GND	11.0 V GND	11.0 V GND		GND	15 V	Y1	13.5	13.5	13.5	Vdc		
V _{Li4} _u	44			11.0 V GND	11.0 V GND		GND		GND	11.0 V GND	11.0 V GND		GND	15 V	Y1	13.5	13.5	13.5	"		
V _{Li4} _u	45			11.0 V GND	11.0 V GND		GND		GND	11.0 V GND	11.0 V GND		GND	15 V	Y1	13.5	13.5	13.5	"		
V _{Li4} _u	46			11.0 V GND	11.0 V GND		GND		GND	11.0 V GND	11.0 V GND		GND	15 V	Y1	13.5	13.5	13.5	"		
V _{Li4} _u	47			11.0 V GND	11.0 V GND		GND		GND	11.0 V GND	11.0 V GND		GND	15 V	Y1	13.5	13.5	13.5	"		
V _{Li4} _u	48			11.0 V GND	11.0 V GND		GND		GND	11.0 V GND	11.0 V GND		GND	15 V	Y1	13.5	13.5	13.5	"		
V _{Li4} _u	49			11.0 V GND	11.0 V GND		GND		GND	11.0 V GND	11.0 V GND		GND	15 V	Y1	13.5	13.5	13.5	"		
V _{Li4} _u	50			11.0 V GND	11.0 V GND		GND		GND	11.0 V GND	11.0 V GND		GND	15 V	Y1	13.5	13.5	13.5	"		
V _{Li2} _u	51			1.5 V GND	1.5 V GND		GND		GND	1.5 V GND	1.5 V GND		GND	5 V	Y1	0.5	0.5	0.5	0.5		
V _{Li2} _u	52			1.5 V GND	1.5 V GND		GND		GND	1.5 V GND	1.5 V GND		GND	5 V	Y1	0.5	0.5	0.5	0.5		
V _{Li2} _u	53			1.5 V GND	1.5 V GND		GND		GND	1.5 V GND	1.5 V GND		GND	5 V	Y1	0.5	0.5	0.5	0.5		
V _{Li2} _u	54			1.5 V GND	1.5 V GND		GND		GND	1.5 V GND	1.5 V GND		GND	5 V	Y1	0.5	0.5	0.5	0.5		
V _{Li2} _u	55			1.5 V GND	1.5 V GND		GND		GND	1.5 V GND	1.5 V GND		GND	5 V	Y1	0.5	0.5	0.5	0.5		
V _{Li2} _u	56			1.5 V GND	1.5 V GND		GND		GND	1.5 V GND	1.5 V GND		GND	5 V	Y1	0.5	0.5	0.5	0.5		
V _{Li2} _u	57			1.5 V GND	1.5 V GND		GND		GND	1.5 V GND	1.5 V GND		GND	5 V	Y1	0.5	0.5	0.5	0.5		
V _{Li2} _u	58			1.5 V GND	1.5 V GND		GND		GND	1.5 V GND	1.5 V GND		GND	5 V	Y1	0.5	0.5	0.5	0.5		
V _{Li3} _u	59			3.0 V GND	3.0 V GND		GND		GND	3.0 V GND	3.0 V GND		GND	10 V	Y1	1.0	1.0	1.0	1.0		
V _{Li3} _u	60			3.0 V GND	3.0 V GND		GND		GND	3.0 V GND	3.0 V GND		GND	10 V	Y1	1.0	1.0	1.0	1.0		
V _{Li3} _u	61			3.0 V GND	3.0 V GND		GND		GND	3.0 V GND	3.0 V GND		GND	10 V	Y1	1.0	1.0	1.0	1.0		
V _{Li3} _u	62			3.0 V GND	3.0 V GND		GND		GND	3.0 V GND	3.0 V GND		GND	10 V	Y1	1.0	1.0	1.0	1.0		
V _{Li3} _u	63			3.0 V GND	3.0 V GND		GND		GND	3.0 V GND	3.0 V GND		GND	10 V	Y1	1.0	1.0	1.0	1.0		
V _{Li3} _u	64			3.0 V GND	3.0 V GND		GND		GND	3.0 V GND	3.0 V GND		GND	10 V	Y1	1.0	1.0	1.0	1.0		
V _{Li3} _u	65			3.0 V GND	3.0 V GND		GND		GND	3.0 V GND	3.0 V GND		GND	10 V	Y1	1.0	1.0	1.0	1.0		
V _{Li3} _u	66			3.0 V GND	3.0 V GND		GND		GND	3.0 V GND	3.0 V GND		GND	10 V	Y1	1.0	1.0	1.0	1.0		
V _{Li3} _u	67			4.0 V GND	4.0 V GND		GND		GND	4.0 V GND	4.0 V GND		GND	15 V ^a	Y1	1.5	1.5	1.5	1.5		
V _{Li3} _u	68			4.0 V GND	4.0 V GND		GND		GND	4.0 V GND	4.0 V GND		GND	15 V ^a	Y1	1.5	1.5	1.5	1.5		
V _{Li3} _u	69			4.0 V GND	4.0 V GND		GND		GND	4.0 V GND	4.0 V GND		GND	15 V ^a	Y1	1.5	1.5	1.5	1.5		
V _{Li3} _u	70			4.0 V GND	4.0 V GND		GND		GND	4.0 V GND	4.0 V GND		GND	15 V ^a	Y1	1.5	1.5	1.5	1.5		
V _{Li3} _u	71			4.0 V GND	4.0 V GND		GND		GND	4.0 V GND	4.0 V GND		GND	15 V ^a	Y1	1.5	1.5	1.5	1.5		
V _{Li3} _u	72			4.0 V GND	4.0 V GND		GND		GND	4.0 V GND	4.0 V GND		GND	15 V ^a	Y1	1.5	1.5	1.5	1.5		
V _{Li3} _u	73			4.0 V GND	4.0 V GND		GND		GND	4.0 V GND	4.0 V GND		GND	15 V ^a	Y1	1.5	1.5	1.5	1.5		
V _{Li3} _u	74			4.0 V GND	4.0 V GND		GND		GND	4.0 V GND	4.0 V GND		GND	15 V ^a	Y1	1.5	1.5	1.5	1.5		
I _{oL1} _u	75			0.4 V	0.4 V		GND		GND	0.4 V	0.4 V		GND	5 V	Y1	1.6	1.2	2.1	mA		
I _{oL1} _u	76			0.4 V	0.4 V		GND		GND	0.4 V	0.4 V		GND	5 V	Y1	1.6	1.2	2.1	mA		
I _{oL1} _u	77			0.4 V	0.4 V		GND		GND	0.4 V	0.4 V		GND	5 V	Y1	1.6	1.2	2.1	mA		
I _{oL1} _u	78			0.4 V	0.4 V		GND		GND	0.4 V	0.4 V		GND	5 V	Y1	1.6	1.2	2.1	mA		
I _{oL1} _u	79			0.4 V	0.4 V		GND		GND	0.4 V	0.4 V		GND	5 V	Y1	1.6	1.2	2.1	mA		
I _{oL1} _u	80			0.4 V	0.4 V		GND		GND	0.4 V	0.4 V		GND	5 V	Y1	1.6	1.2	2.1	mA		
I _{oL1} _u	81			0.4 V	0.4 V		GND		GND	0.4 V	0.4 V		GND	5 V	Y1	1.6	1.2	2.1	mA		
I _{oL1} _u	82			0.4 V	0.4 V		GND		GND	0.4 V	0.4 V		GND	5 V	Y1	1.6	1.2	2.1	mA		
I _{oL2} _u	83			1.5 V	1.5 V		GND		GND	1.5 V	1.5 V		GND	15 V	Y1	12.0	8.0	14.0	14		
I _{oL2} _u	84			1.5 V	1.5 V		GND		GND	1.5 V	1.5 V		GND	15 V	Y1	12.0	8.0	14.0	14		
I _{oL2} _u	85			1.5 V	1.5 V		GND		GND	1.5 V	1.5 V		GND	15 V	Y1	12.0	8.0	14.0	14		
I _{oL2} _u	86			1.5 V	1.5 V		GND		GND	1.5 V	1.5 V		GND	15 V	Y1	12.0	8.0	14.0	14		
I _{oL2} _u	87			1.5 V	1.5 V		GND		GND	1.5 V	1.5 V		GND	15 V	Y1	12.0	8.0	14.0	14		
I _{oL2} _u	88			1.5 V	1.5 V		GND		GND	1.5 V	1.5 V		GND	15 V	Y1	12.0	8.0	14.0	14		
I _{oL2} _u	89			1.5 V	1.5 V		GND		GND	1.5 V	1.5 V		GND	15 V	Y1	12.0	8.0	14.0	14		
I _{oL2} _u	90			1.5 V	1.5 V		GND		GND	1.5 V	1.5 V		GND	15 V	Y1	12.0	8.0	14.0	14		

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TABLE III. Group A inspection for device type 55 - Continued.

Symbol	MIL-STD-883 method	Cases E, F, N, And Z	For terminal conditions and limits see <u>1/</u>												Measured terminal unit	Test limits					
			Subgroup 1 TC = 25°C														Subgroup 2 TC = 125°C		Subgroup 3 TC = -55°C		
			Test No.	Y1	<u>Y1</u>	A1	Y2	<u>Y2</u>	A2	V _{ss}	Y3	<u>Y3</u>	A3	Y4	<u>Y4</u>	A4	V _{DD}	Min	Max	Min	Max
I _{OH1}	"	91	4.6 V	5.0 V GND	4.6 V	GND	5.0 V GND	4.6 V	GND	5.0 V GND	4.6 V	GND	5.0 V GND	4.6 V	GND	5.0 V GND	Y1	-1.4	-1.0	-1.75	-1.4
I _{OH1}	"	92	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH1}	"	93	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH1}	"	94	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH1}	"	95	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH1}	"	96	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH1}	"	97	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH1}	"	98	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH2}	"	99	13.5 V	15.0 V GND	13.5 V	"	15.0 V GND	13.5 V	"	15.0 V GND	13.5 V	"	15.0 V GND	13.5 V	"	15.0 V GND	Y1	-9.0	-6.0	-11.0	-11.0
I _{OH2}	"	100	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH2}	"	101	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH2}	"	102	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH2}	"	103	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH2}	"	104	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH2}	"	105	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
I _{OH2}	"	106	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
*I _{H11} <u>5/</u>	3010	107		18.0 V			18.0 V				18.0 V			18.0 V		18.0 V	All Inputs together	400			nA
*I _{H12} <u>5/</u>	"	108		18.0 V GND			GND	18.0 V GND			GND			GND		GND	A1	100	100		nA
*I _{H12} <u>5/</u>	"	109		"			"	"			"			"		"	A2	"	"	"	"
*I _{H12} <u>5/</u>	"	110		"			"	"			"			"		"	A3	"	"	"	"
*I _{H12} <u>5/</u>	"	111		"			"	"			"			"		"	A4	"	"	"	"
*I _{L1} <u>5/</u>	3009	112		"			"	"			"			GND		GND	All Inputs together	-400			"
*I _{L2} <u>5/</u>	"	113		GND	18.0 V GND		"	"			"			18.0 V GND		18.0 V GND	A1	-100	-100		"
*I _{L2} <u>5/</u>	"	114		"	"		"	"			"			"		"	A2	"	"	"	"
*I _{L2} <u>5/</u>	"	115		"	"		"	"			"			"		"	A3	"	"	"	"
*I _{L2} <u>5/</u>	"	116		"	"		"	"			"			"		"	A4	"	"	"	"
Ci	3012	117		A <u>5/</u>			A <u>5/</u>	GND			A <u>5/</u>			GND		GND	A1	12.0			pF
Ci	"	118		"			"	"			"			"		"	A2	"	"	"	"
Ci	"	119		"			"	"			"			"		"	A3	"	"	"	"
Ci	"	120		"			"	"			"			"		"	A4	"	"	"	"

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TABLE III. Group A inspection for device type 55 – Continued.

Symbol	MIL-STD-883 method	Cases E, F, N, And Z	For terminal conditions and limits see 1/												Measured terminal	Test limits						Unit		
			Test No.	Y1	$\overline{Y_1}$	A1	Y2	$\overline{Y_2}$	A2	V _{ss}	$\overline{Y_3}$	A3	Y4	$\overline{Y_4}$	A4	V _{DD}	Subgroup 1 TC = 25°C		Subgroup 2 TC = 125°C		Subgroup 3 TC = -55°C			
																Min	Max	Min	Max	Min	Max			
t_{PHL}	3003 Fig. 5	121 OUT	IN	OUT	IN	GND	OUT	IN	OUT	IN	5.0 V	A1 to Y1	6.0	6.0	9.0	172	6.0	115	ns					
		122										A2 to Y2	4	4	4	4	4	4	4	4	4	4		
		123										A3 to Y3	4	4	4	4	4	4	4	4	4	4		
		124										A4 to Y4	4	4	4	4	4	4	4	4	4	4		
		125										A1 to $\overline{Y_1}$	4	4	4	4	4	4	4	4	4	4		
		126										A2 to $\overline{Y_2}$	4	4	4	4	4	4	4	4	4	4		
		127										A3 to $\overline{Y_3}$	4	4	4	4	4	4	4	4	4	4		
		128										A4 to $\overline{Y_4}$	4	4	4	4	4	4	4	4	4	4		
t_{PLH}																								
		129 OUT	IN	OUT	IN	OUT	IN	OUT	IN	OUT	IN													
		130																						
		131																						
		132																						
		133																						
		134																						
		135																						
		136																						
t_{PLH}	3004 Fig. 5	137 OUT	IN	OUT	IN	OUT	IN	OUT	IN	OUT	IN													
		138																						
		139																						
		140																						
		141																						
		142																						
		143																						
		144																						
t_{PLH}																								
		145 OUT	IN	OUT	IN	OUT	IN	OUT	IN	OUT	IN													
		146																						
		147																						
		148																						
		149																						
		150																						
		151																						
		152																						

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TABLE III. Group A inspection for device type 55 – Continued.

- 1/ Input pins not designated may be tied to V_{CC} or GND or may be left open provided they do not influence the outcome of the measurement . Output pins not designated may be tied to the loads or may be left open provided they do not influence the outcome of the measurement.
- 2/ Terminal 16 is not connected for device types 53 and 54.
- 3/ Test parameter V_{IC(pos)} does not apply to device types 53 and 54.
- 4/ When performing quiescent supply measurements (I_{SS}), the meter shall be placed so that all currents flow through the meter.
- 5/ The device manufacturer may, at his option, measure I_{IL} and I_{IH} at 25°C for each individual input or measure all inputs together.
- 6/ (A) Capacitance bridge between measured terminal and V_{SS}; frequency = 1 MHz.
- 7/ Test limits t_{PHL}, t_{PLH}, t_{THL}, and t_{TLH} for device types 51/53 and 52/54 consists of two sets of values and are expressed XXX/XXXX in the limits column. The digits preceding the slash apply to the first device in a set.