

MIL-M-38510/653A
15 JANUARY 88
SUPERSEDING
MIL-M-38510/653
31 March 1986

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, HIGH SPEED, CMOS
FLIP-FLOPS, MONOLITHIC SILICON, POSITIVE LOGIC

This specification is approved for use by all Departments and Agencies of the Department of Defense.

1. SCOPE

1.1 Scope. This specification covers the detail requirements for monolithic silicon, high speed, CMOS, flip-flops, bistable logic microcircuits. Two product assurance classes and a choice of case outlines and lead finish are provided and are reflected in the complete part number.

1.2 Part number. The part number shall be in accordance with MIL-M-38510, and as specified herein.

1.2.1 Device type. The device types shall be as follows:

<u>Device type</u>	<u>Circuit</u>
01	Dual JK flip-flop with clear
02	Dual D positive-edge triggered flip-flops with clear and preset
03	Dual JK negative-edge triggered flip-flops with clear
04	Dual JK positive-edge triggered flip-flops with clear and preset
05	Dual JK negative-edge triggered flip-flops with clear and preset
06	Quad D-type flip-flops with 3-state outputs and clear
07	Hex D-type flip-flops with clear
08	Quad D-type flip-flops with clear
52	Dual D-type positive-edge triggered flip-flops with clear and preset (TTL compatible inputs)

1.2.2 Device class. The device class shall be the product assurance level as defined in MIL-M-38510.

1.2.3 Case outlines. The case outlines shall be designated as follows:

<u>Outline letter</u>	<u>Case outline (see MIL-M-38510, appendix C)</u>
C	D-1 (14-lead, 1/4" x 3/4"), dual-in-line package
D	F-2 (14-lead, 1/4" x 3/8"), flat package
E	D-2 (16 lead, 1/4" x 7/8"), dual-in-line package
F	F-5 (16-lead, 1/4" x 3/8"), flat package
2	C-2 (20-terminal, .350" x .350") square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage - - - - -	-0.5 V dc to +7.0 V dc
DC input voltage - - - - -	-0.5 V dc to V _{CC} +0.5 V dc
DC output voltage- - - - -	-0.5 V dc to V _{CC} +0.5 V dc
Clamp diode current- - - - -	*20 mA
DC output current per pin- - - - -	*25 mA (*35 mA for type 06)
DC V _{CC} or GND current per pin- - - - -	*50 mA (*70 mA for type 06)
Storage temperature range- - - - -	-65°C to +150°C

|Beneficial comments (recommendations, additions, deletions) and any pertinent data |
|which may be of use in improving this document should be addressed to: Rome Air |
|Development Center (RBE-2), Griffiss AFB, NY 13441-5700, by using the self- |
|addressed Standardization Document Improvement Proposal (DD Form 142C) appearing at |
|the end of this document or by letter.

AMSC N/A

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FSC 5902

Maximum power dissipation:
 Devices 01-05, 07, 08, 52- - - - - 300 mW
 Device 06- - - - - 420 mW
Lead temperature (soldering, 10 seconds): +300°C
Thermal resistance, junction-to-case (0_{JC}):
 Cases C, D, E, F, and 2- - - - - (See MIL-M-38510, appendix C)
Junction temperature (T_J): +175°C

1.4 Recommended operating conditions.

Device types 01 to 08:

Input low (V _{IL}) maximum voltage- - - - -	0.3 V at V _{CC} = 2 V
Device 06- - - - -	0.9 V at V _{CC} = 4.5 V
Device 07- - - - -	1.2 V at V _{CC} = 6 V
Input high (V _{IH}) minimum voltage - - - - -	1.5 V at V _{CC} = 2 V
Supply voltage (V _{CC}) - - - - -	3.15 V at V _{CC} = 4.5 V
Input voltage- - - - -	4.2 V at V _{CC} = 6 V
Operating temperature- - - - -	2 V dc to 6 V dc
	0 V dc to V _{CC}
	-55°C to +125°C
 Input rise and fall times (t _r , t _f) maximum:	
V _{CC} = 2 V	1000 ns
V _{CC} = 4.5 V	500 ns
V _{CC} = 6 V	400 ns
 Width of clock pulse (t _{p_clock}):	
Devices 01, 03, 04, 06, 07 - - - - -	V _{CC} = 4.5 V
Devices 02, 05 - - - - -	27 ns minimum
Device 08- - - - -	30 ns minimum
Device 08- - - - -	24 ns minimum
 Width of clear pulse (t _{p_clear}):	
Devices 01, 02, 03, 04, 05, 06 - - - - -	30 ns minimum
Devices 07, 08 - - - - -	24 ns minimum
 Width of preset pulse:	
Devices 02, 04, 05 - - - - -	30 ns minimum
Data setup time before clock (t _{setup}):	
Devices 01-08- - - - - -	30 ns minimum
 Clear or preset setup time before clock (t _{rem}):	
Devices 01, 03, 04, 05, 07, 08 - - - - -	30 ns minimum
Device 02- - - - -	38 ns minimum
Device 06- - - - -	27 ns minimum
t _{HOLD} : Devices 01-08- - - - - -	8 ns minimum

Device type 52:

Input low (V _{IL}) maximum voltage- - - - -	0.8 V at V _{CC} = 4.5 V - 5.5 V
Input high (V _{IH}) minimum voltage - - - - -	2.0 V at V _{CC} = 4.5 V - 5.5 V
Supply voltage (V _{CC}) - - - - -	4.5 V dc to 5.5 V dc
Output voltage - - - - -	0 V dc to V _{CC}
Operating ambient temperature (T _A) - - - - -	-55°C to +125°C
Width of clock pulse (t _{p_clock})- - - - -	30 ns minimum
Width of clear pulse (t _{p_clear}) - - - - -	30 ns minimum
Width of preset pulse- - - - -	30 ns minimum
Data setup time before clock (t _{setup})	30 ns minimum
t _{rem} setup time before clock	
{t _{cu} clear or t _{cu} preset)- - - - -	38 ns minimum
t _{HOLD} - - - - -	3 ns minimum
 Input rise and fall times (t _r , t _f):	
V _{CC} = 4.5 V- - - - -	500 ns maximum

2. APPLICABLE DOCUMENTS

2.1 Government documents.

2.1.1 Specification and standard. The following specification and standard form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Department of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by contractors in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this specification and the references cited herein (except for associated detail specifications, specification sheets or MS standards), the text of this specification shall take precedence. Nothing in this specification, however, shall supersede applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Detail specification. The individual item requirements shall be in accordance with MIL-M-38510, and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Logic diagrams and terminal connections. The logic diagrams and terminal connections shall be as specified on figure 1.

3.2.2 Truth tables. The truth tables shall be as specified on figure 2.

3.2.3 Schematic circuits. The schematic circuits shall be submitted to the preparing activity prior to inclusion of a manufacturer's device in this specification and shall be submitted to the qualifying activity as a prerequisite for qualification. All qualified manufacturers' schematics shall be maintained and available upon request.

3.2.4 Case outlines. The case outlines shall be as specified in 1.2.3.

3.3 Lead material and finish. The lead material and finish shall be in accordance with MIL-M-38510 and 6.4 herein.

3.4 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I, and apply over the full recommended ambient operating temperature range.

3.5 Electrical test requirements. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.

3.6 Marking. Marking shall be in accordance with MIL-M-38510. At the option of the manufacturer, marking of the country of origin may be omitted from the body of the microcircuit, but shall be retained on the initial container.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions 1/ $T_C = +125^\circ C$ unless otherwise specified	Device type 2/	V _{CC}	Limits		Unit
					Min	Max	
High-level output voltage	V _{OH1} 3/	$V_{IH} = 1.5 V$; $V_{IL} = 0.3 V$ $I_{OH} = -20 \mu A$	A11	2.0 V	1.95		V
	V _{OH2} 3/	$V_{IH} = 3.15 V$; $V_{IL} = 0.9 V$ $I_{OH} = -20 \mu A$	A11	4.5 V	4.45		V
	V _{OH3}	$V_{IH} = 4.2 V$; $V_{IL} = 1.2 V$ $I_{OH} = -20 \mu A$	A11	6.0 V	5.95		V
	V _{OH4} 3/	$V_{IH} = 3.15 V$; $V_{IL} = 0.9 V$ $I_{OH} = -4.0 mA$	A11	4.5 V	3.7		V
	V _{OH5}	$V_{IH} = 4.2 V$ $V_{IL} = 1.2 V$	01,02 03,04, 05,07,08 06	6.0 V	5.2		V
		$I_{OH} = -5.2 mA$					
		$I_{OH} = -7.8 mA$					
Low-level output voltage	V _{OL1} 3/	$V_{IL} = 0.3 V$; $V_{IH} = 1.5 V$ $I_{OH} = 20.0 \mu A$	A11	2.0 V		0.05	V
	V _{OL2} 3/	$V_{IL} = 0.9 V$; $V_{IH} = 3.15 V$ $I_{OH} = 20.0 \mu A$	A11	4.5 V		0.05	V
	V _{OL3}	$V_{IL} = 1.2 V$; $V_{IH} = 4.2 V$ $I_{OH} = 20.0 \mu A$	A11	6.0 V		0.05	V
	V _{OL4} 3/	$V_{IL} = 0.9 V$; $V_{IH} = 3.15 V$ $I_{OH} = 4.0 mA$	A11	4.5 V		0.4	V
	V _{OL5}	$V_{IL} = 1.2 V$ $V_{IH} = 4.2 V$	01,02 03,04, 05,07,08 06	6.0 V		0.4	V
		$I_{OH} = 5.2 mA$					
		$I_{OH} = 7.8 mA$					
	V _{OL6}	$V_{IL} = 0.8 V$; $V_{IH} = 2.0 V$ $I_{OL} = 20 mA$	52	4.5 V		0.05	V
	V _{OL7}	$V_{IL} = 0.8 V$; $V_{IH} = 2.0 V$ $I_{OL} = 4.0 mA$	52	5.5 V		0.4	V

See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions 1/ $T_C = +125^\circ C$ unless otherwise specified	Device type 2/	V _{CC}	Limits		Unit
					Min	Max	
Positive input clamp voltage	V _{IC+}	V _{CC} = GND; I _{IN} = 1 mA $T_C = +25^\circ C$	A11	GND		1.5	V
Negative input clamp voltage	V _{IC-}	V _{CC} = Open; I _{IN} = -1 mA $T_C = +25^\circ C$	A11	OPEN		-1.5	V
Input current low	I _{IL}	V _{IN} = GND		01-08 52	6.0 V 5.5 V		-0.1 μA
Input current high	I _{IH}	V _{IN} = V _{CC}		01-08 52 01-05, 07,08	6.0 V 5.5 V 2.0 V	-2	0.1 μA
Short circuit output current	I _{OS1} 3/	V _O = GND V _I = GND		06 01-05, 07,08,52	-60 4.5 V	-50 -15	mA
	I _{OS2} 3/			06		-150	
	I _{OS3} 3/			01-05, 07,08	6.0 V	-25	-180
	I _{OS4}			06		-210	
	I _{OS5} 3/			01-05, 07,08,52 06	4.0 V -10	-120 -135	
				52	5.5 V	-25	-180
Additional supply current quiescent in accordance with input pin lone unit load	I _{CC^A} 8/	V _{IL} = 0.8 V; V _{IH} = 2.4 V Test pin at V _{IN} = 2.4 V Other pins at 0.8 V or 15.5 V; I _O = 0 V		52	5.5 V		1.5 mA
Supply current quiescent	I _{CC}	V _I = 6.0 V		01-05, 52 06-08	6.0 V	15 20	μA
	I _{CCZ}	V _I = 6.0 V		06	6.0 V	10	μA
Three-state output leakage current low	I _{OZL}	V _{OUT} = GND I _O = V _{IH} 4/		06	6.0 V		-2.0 μA
Three-state output leakage current high	I _{OZH}	V _{OUT} = V _{CC} I _O = V _{IH} 4/		06	6.0 V		+2.0 μA

See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions 1/ $T_C = +125^\circ C$ unless otherwise specified	Device type 2/	VCC	Limits		Unit
					Min	Max	
Input capacitance control	C_C	$T_C = +25^\circ C$	A11			15	
Input capacitance	C_I		A11			10	pF
Three-state output capacitance	C_0		06	6.0 V		20	
Power dissipation capacitance	C_{PD}	2/ 3/	01 02-05 06 07 08 52			30 35 29 38 65 30	
Maximum clock frequency	f_{MAX}	$C_L = 50 \text{ pF} \pm 10\%$	5/ 6/	01,03, 04,06, 07 02,05, 52 08	4.5 V	23 21 26	MHz
Maximum propagation delay time low to high level CLK to Q	t_{PLH1}	$C_L = 50 \text{ pF} \pm 10\%$	6/ 7/	02,04, 05,52 01,03 06 07 08	4.5 V	5 5 6 5 4 4	ns
Maximum propagation delay time high to low level CLK to Q	t_{PHL1}			02,04, 05,52 01,03 06 07 08		5 5 6 5 4 4	
Maximum propagation delay time low to high level CLR or PRE to Q or \bar{Q}	t_{PLH2}			01,06 02 03 04 08 52		6 5 6 6 4 5	

See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions 1/ $T_C = +125^\circ\text{C}$ unless otherwise specified	Device type 2/	V _{CC}	Limits		Unit
					Min	Max	
Maximum propagation delay time high to low level	t _{PHL2}	$C_L = 50 \text{ pF} \pm 10\%$ 6/ 7/	01,05		6	43	ns
			02		5	54	
			03		6	51	
			04		6	54	
			06		5	41	
			07		5	42	
			08		4	43	
			52		5	47	
Maximum enable time to high or low levels	t _{PZH} or t _{TPZL}		06		5	35	
Maximum disable time to high or low levels	t _{PHZ} or t _{TPLZ}		06		5	35	
Maximum transition time, low to high or high to low	t _{TTLH} or t _{TTHL}		01-05, 07,08,52)		3	20	
			06		2	16	

1/ Complete terminal conditions shall be as specified in table III.

2/ Power dissipation capacitance (C_{PD}) per flip-flop.

3/ Guaranteed but not tested.

4/ IOZL set internal D flip-flops to high state.

IOZH set internal D flip-flops to low state.

5/ See the formula for determining maximum frequencies shown in table IA.

6/ Tested at $V_{CC} = 4.5 \text{ V}$ at 125°C for sample testing and $V_{CC} = 4.5 \text{ V}$ and 25°C for screening. Guaranteed at other V_{CC} voltages and temperatures. See tables 1A and 1B (as appropriate) and the exception in 4.4.1d.7/ For propagation and transition delay times at $V_{CC} = 2.0 \text{ V}$, increase limit by a factor of 5. For propagation and transition delay times at $V_{CC} = 6.0 \text{ V}$, decrease limit by a factor of .85.8/ Total supply current = $I_{CC} + I_{CCA}$.TABLE IA. Calculated f_{MAX} at $-55/+25^\circ\text{C}$ case temperature.

V_{CC}	$T_C = {}^\circ\text{C}$	
	+125	-55/+25
2.0 V	0.2X	0.2Y
4.5 V	X = 1	1.33X = Y
6.0 V	1.18X	1.18Y

NOTE: Normalized numbers ($+125^\circ\text{C} = 1$).

The 2.0 V and 6.0 V numbers are derived from their 4.5 V integer value (rounding off according 5/4).

TABLE IB. Calculated dynamic values at -55/+25°C case temperature.

V _{CC}	T _C = °C	
	+125	-55/+25
2.0 V	5	5 x 0.75
4.5 V	1	0.75
6.0 V	0.85	0.85 x 0.75

NOTE: Normalized numbers (+125°C = 1).
The 2.0 V and 6.0 V volt numbers are derived
from their 4.5 V integer value (rounding off
according 5/4).

3.6.1. Total dose radiation hardness identifier. The total dose radiation hardness identifier shall be in accordance with MIL-M-38510 and 4.5.4 herein.

3.6.2 Serialization. All class S devices shall be serialized in accordance with MIL-M-38510.

3.6.3 Correctness of indexing and marking. All devices shall be subjected to the final electrical tests specified in table II after part number marking to verify that they are correctly indexed and identified by part number. Optionally, an approved electrical test may be devised especially for this requirement.

3.7 Microcircuit group assignment. The devices covered by this specification shall be in microcircuit group number 38 (see MIL-M-38510, appendix E).

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-M-38510 and methods 5005 and 5007, as applicable, of MIL-STD-883, except as modified herein.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:

- a. Delete the sequence specified in 3.1.10 through 3.1.14 of method 5004 and substitute lines 1 through 7 of table II herein.
- b. Burn-in (method 1015 of MIL-STD-883).
 - (1) Static tests (test condition A) using circuit shown on figure 3, or equivalent. Ambient temperature (T_A) shall be +125°C minimum. Test duration for each static test shall be 24 hours minimum for class S devices and in accordance with table I of method 1015 for class B devices.
 - (2) Dynamic test (test condition D) using circuit shown on figure 3, or equivalent. Ambient temperature shall be +125°C minimum. Test duration shall be in accordance with table I of method 1015.
- c. Interim and final electrical parameters shall be as specified in table II herein.
- d. For class S devices, post dynamic burn-in, or class B devices, post static burn-in, electrical parameter measurements may, at the manufacturer's option, be performed separately or included in the final electrical parameter requirements.

TABLE II. Burn-in and electrical test requirements.

Line no.	Applicable tests and MIL-STD-883 test method	Reference paragraph	Class S device			Reference paragraph	Class B device		
			Table III subgroups 2/ 4/	Table IV delta limits 3/	Test circuit figure		Table III subgroups 2/ 4/	Table IV delta limits 3/	Test circuit figure
1	Interim electrical parameters (method 5004)		1				1		
2	Static burn-in I (method 1015)	4.2b 4.5.2	Reqd		3		Not reqd		
3	Same as line 1		1	Δ					
4	Static burn-in II (method 1015)	4.5.2 4.2b	Reqd		3		Reqd 5/		3
5	Same as line 1	4.2d	1*	Δ		4.2d	1*	Δ	
6	Dynamic burn-in (method 1015)	4.2b 4.5.2	Reqd		3		Not reqd		
7	Same line 1	4.2d	1	Δ					
8	Final electrical parameters (method 5005)		1*,2,3,7 8,9				1*,2,7, 9 5/		
9	Group A test requirements (method 5005)	4.4.1	1,2,3,4, 7,8,9, 10,11			4.4.1	1,2,3,4, 7,8,9, 10,11		3
10	Group B end-point electrical parameters (method 5005)	4.4.2	+1,2,3,7, 8,9,10, 11	Δ	3		+1		
11	Group C end-point electrical parameters (method 5005)					4.4.3	1,2	Δ	3
12	Group D end-point electrical parameters (method 5005)	4.4.4	1,2,3			4.4.4	1,2		

1/ Blank spaces indicate tests are not applicable.

2/ * indicates PDA applies to subgroup 1 (see 4.2.1).

3/ Δ indicates delta limit shall be required only on table III subgroup 1, where specified, and the delta values shall be computed with reference to the previous interim electrical parameters.

4/ + indicates also applies to electrostatic discharge sensitivity tests.

5/ The device manufacturer may at his option either complete subgroup 1 electrical parameter measurements, including delta measurements, within 96 hours after burn-in completion (removal of bias); or may complete subgroup 1 electrical measurements without delta measurements within 24 hours after burn-in completion (removal of bias).

4.2.1 Percent defective allowable (PDA).

- a. The PDA for class S devices shall be 5 percent for static burn-in and 5 percent for dynamic burn-in, based on the exact number of devices submitted to each separate burn-in.
- b. Static burn-in I and II failures shall be cumulative for determining the PDA.
- c. The PDA for class B devices shall be in accordance with MIL-M-38510 for static burn-in. Dynamic burn-in is not required.
- d. Those devices whose measured characteristics, after burn-in, exceed the specified delta (Δ) limits or electrical parameter limits specified in table III, subgroup 1, are defective and shall be removed from the lot. The verified failures divided by the total number of devices in the lot initially submitted to burn-in shall be used to determine the percent defective for the lot and the lot shall be accepted or rejected based on the specified PDA.

4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-M-38510. Inspections to be performed shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.5).

4.4 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-M-38510 and as specified herein. Inspections to be performed shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.5).

4.4.1 Group A inspection. Group A inspection shall be in accordance with table I of method 5005 of MIL-STD-883 and as follows:

- a. Tests shall be performed in accordance with table II herein.
- b. Subgroups 5 and 6 of table I of method 5005 of MIL-STD-883 shall be omitted.
- c. Subgroup 4 (C_1 , C_0 , and C_C where applicable), measurements shall be measured only for initial qualification and after process or design changes which may affect input capacitance. Capacitance shall be measured between the designated terminal and GND at a frequency of 1 MHz.
- d. Subgroups 9 and 11 shall be measured only for initial qualification and after process or design changes which may affect dynamic performance.

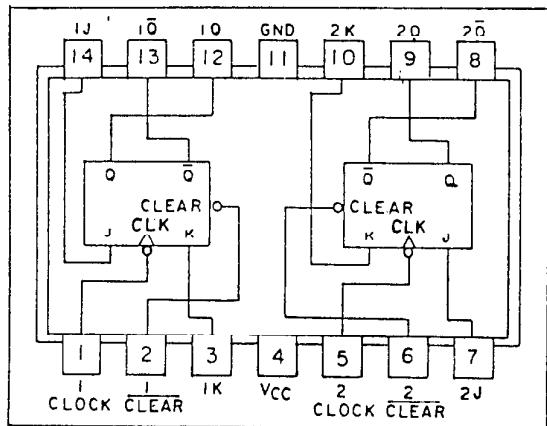
4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of method 5005 of MIL-STD-883 and as follows:

- a. Class S steady-state life test circuits shall be submitted to the qualifying activity for approval. When the alternate steady-state life test is used, the circuit on figure 3, or equivalent, shall be used.
- b. Electrostatic discharge sensitivity (ESDS) testing shall be performed in accordance with MIL-STD-883, method 3015. The option to categorize devices as ESD sensitive without performing the test is not allowed. Device types categorized as ESD sensitive shall be further tested using method 3015 modified as follows:
 1. Table I pin combinations 4 and 5 shall be deleted.
 2. The test sequence specified in paragraph 3.b shall be repeated an additional four times rather than the two specified.
 3. Only those device types that pass ESDS testing at 1,000 volts or greater shall be considered as conforming to the requirements of this specification.
- c. End-point electrical parameters shall be as specified in table II herein. Delta limits shall apply only to subgroup 5 of group B inspections for class S, and shall consist of tests specified in table IV herein.

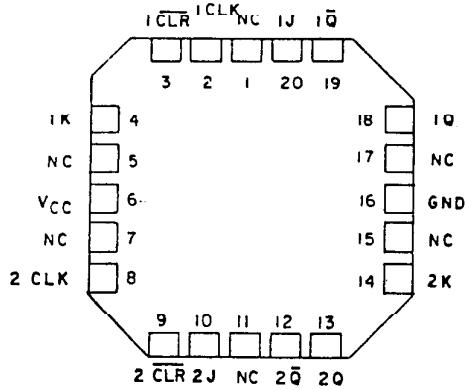
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Device type 01

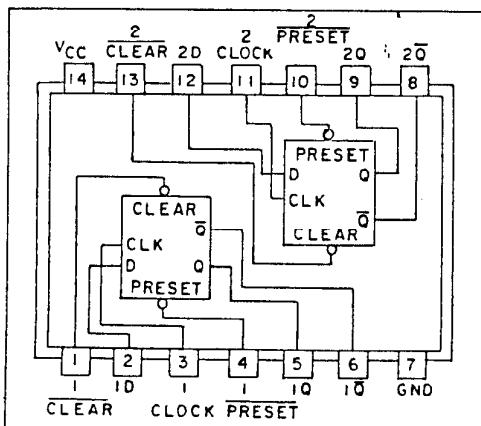
Cases C and D

Device type 01

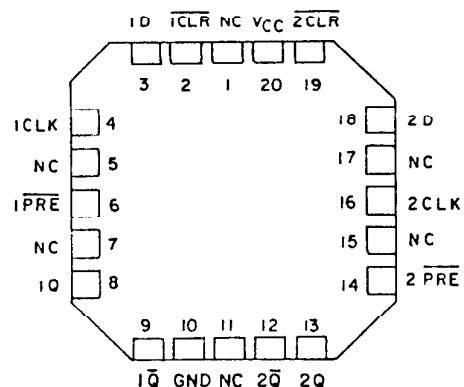
Case 2

Device types 02 and 52

Cases C and D

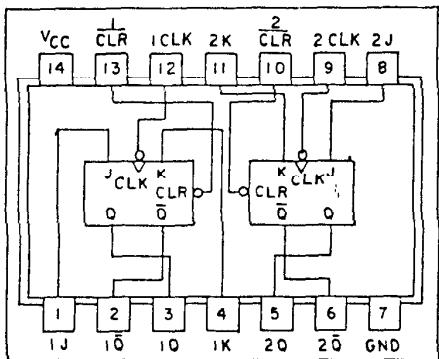
Device types 02 and 52

Case 2

FIGURE 1. Logic diagrams and terminal connections (top view).

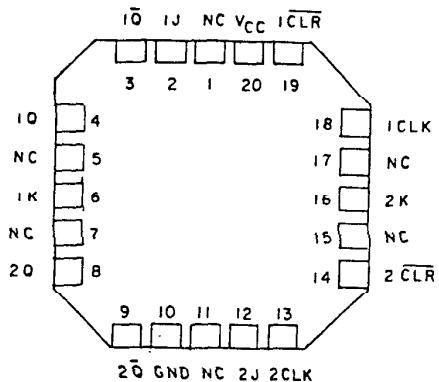
Device type 03

Cases C and D



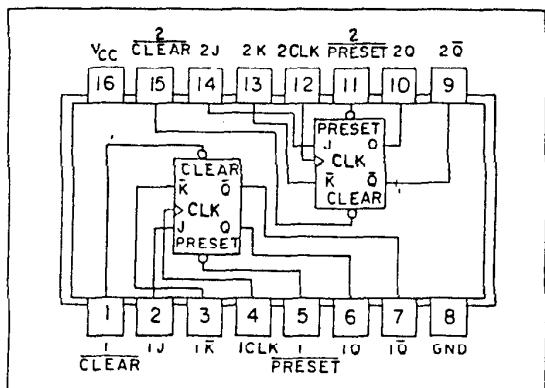
Device type 03

Case 2



Device type 04

Cases E and F



Device type 04

Case 2

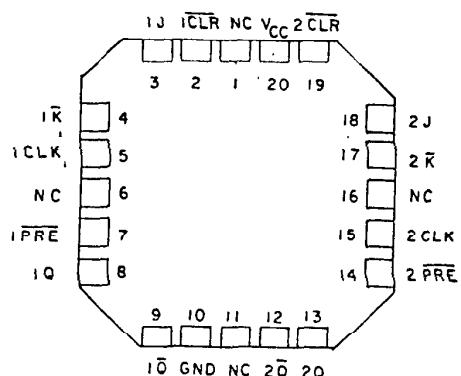


FIGURE 1. Logic diagrams and terminal connections (top view) - Continued.

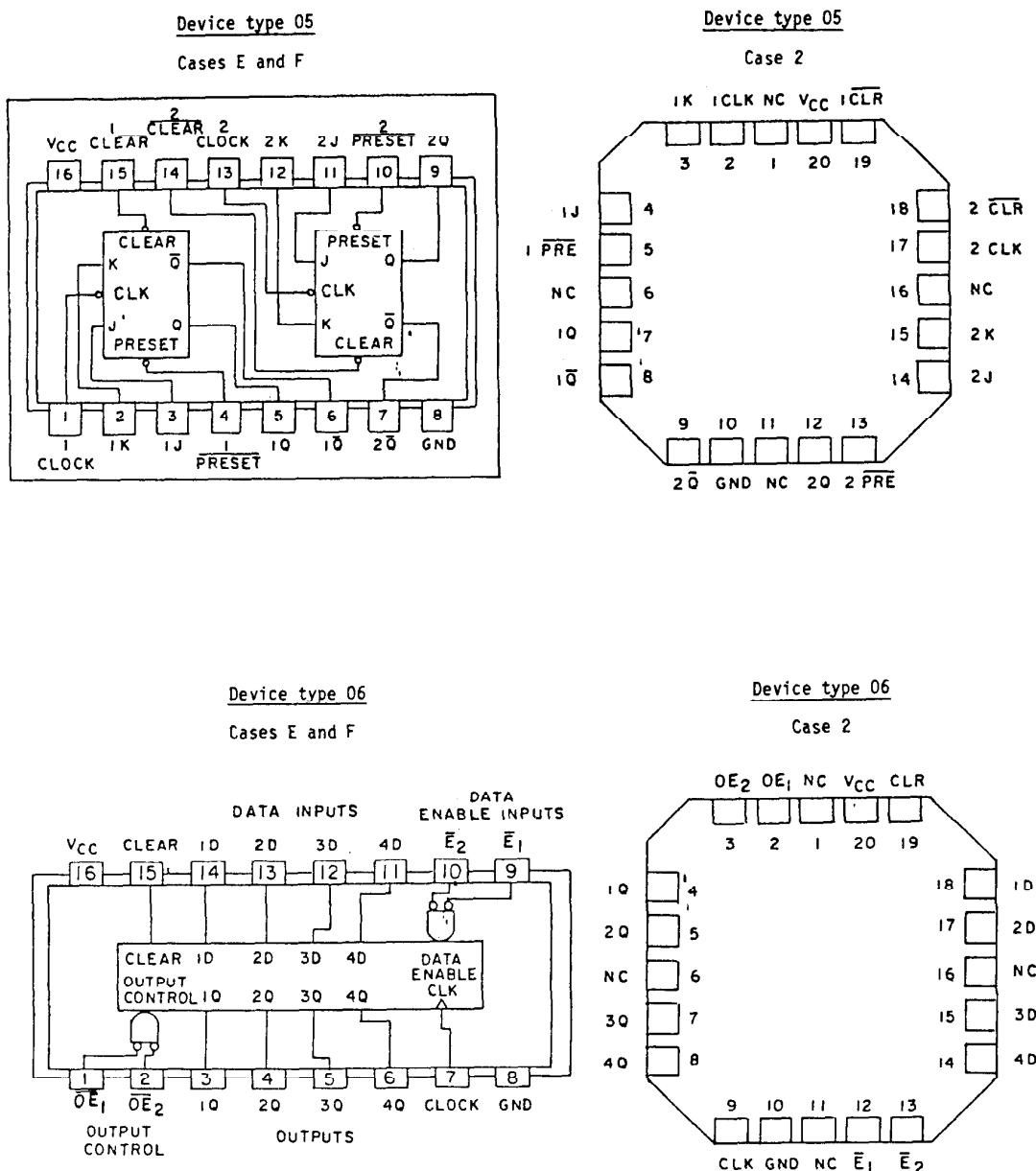
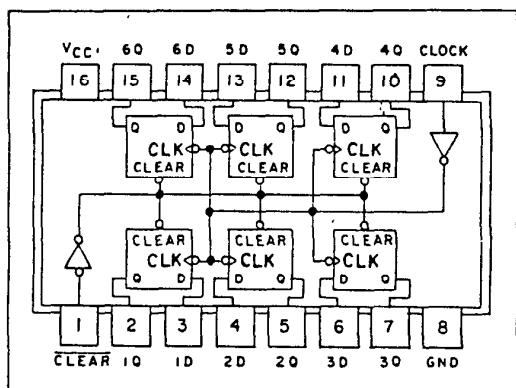


FIGURE 1. Logic diagrams and terminal connections (top view) - Continued.

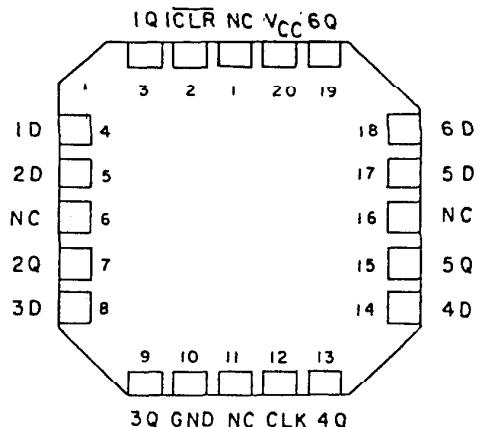
Device type 07

Cases E and F



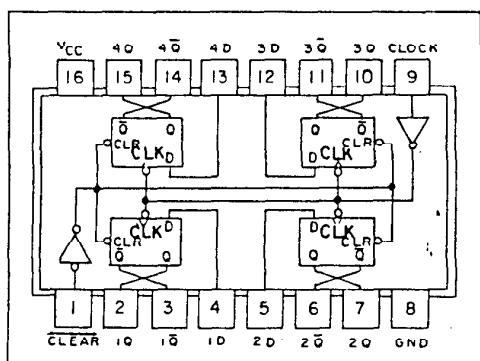
Device type 07

Case 2



Device type 08

Cases E and F



Device type 08

Case 2

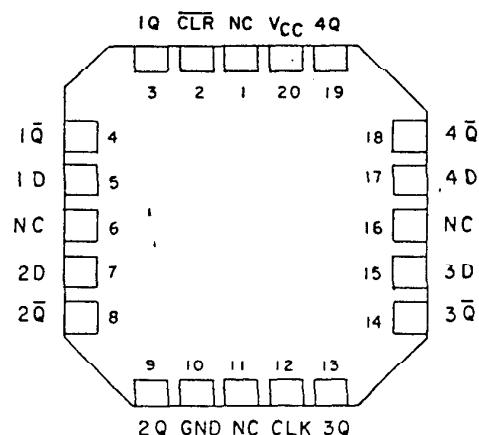


FIGURE 1. Logic diagrams and terminal connections (top view) - Continued.

Device type 01

Inputs				Outputs	
CLR	CLK	J	K	Q	\bar{Q}
L	X	X	X	L	H
H	+	L	L	Q_0	\bar{Q}_0
H	+	H	L	H	L
H	+	L	H	L	H
H	+	H	H	Toggle	
H	H	X	X	Q_0	\bar{Q}_0

X pins have no effect on output.

Device types 02 and 52

Inputs				Outputs	
PRE	CLR	CLK	D	Q	\bar{Q}
L	H	X	X	H	L
H	L	X	X	L	H
L	L	X	X	H*	H*
H	H	+	H	H	I
H	H	+	L	L	H
H	H	X	X	Q_0	\bar{Q}_0

*This configuration is nonstable
X Pins have no effect on output.

Device type 03

Inputs				Outputs	
CLR	CLK	J	K	Q	\bar{Q}
L	X	X	X	L	H
H	+	L	L	Q_0	\bar{Q}_0
H	+	H	L	H	L
H	+	L	H	L	H
H	+	H	H	Toggle	
H	H	X	X	Q_0	\bar{Q}_0

X pins have no effect on output.

Device type 04

Inputs				Outputs		
PRE	CLR	CLK	J	K	Q	\bar{Q}
L	H	X	X	X	H	L
H	L	X	X	X	L	H
L	L	X	X	X	H*	H*
H	H	+	L	L	L	H
H	H	+	H	L	Toggle	
H	H	+	L	H	Q_0	\bar{Q}_0
H	H	+	H	H	H	L
H	H	L	X	X	Q_0	\bar{Q}_0

*This configuration is nonstable
X Pins have no effect on output.

FIGURE 2 Truth tables..

Device type 05

Inputs				Outputs		
PRE	CLR	CLK	J	K	Q	\bar{Q}
L	H	X	X	X	H	L
H	L	X	X	X	L	H
L	L	X	X	X	H*	H*
H	H	+	L	L	Q_0	\bar{Q}_0
H	H	+	H	L	H	L
H	H	+	L	H	L	H
H	H	+	H	H	Toggle	
H	H	H	X	X	Q_0	\bar{Q}_0

*This configuration is nonstable
X Pins have no effect on output.

Device type 06

Data enable				Data D	Output Q
CLR	CLK	E_1	E_2		
H	X	X	X	X	L
L	L	X	X	X	Q_0
L	+	H	X	X	Q_0
L	+	X	H	X	Q_0
L	+	L	L	L	L
L	+	L	L	H	H

When either E_1 or E_2 (or both) is (are) high the output is disable to the high Z state; however, sequential operation of the flip-flops is not affected. X pins have no effect on output.

Device type 07

Inputs			Outputs	
CLR	CLK	D	Q	
L	X	X	L	
H	+	H	H	
H	+	L	L	
H	L	X	Q_0	

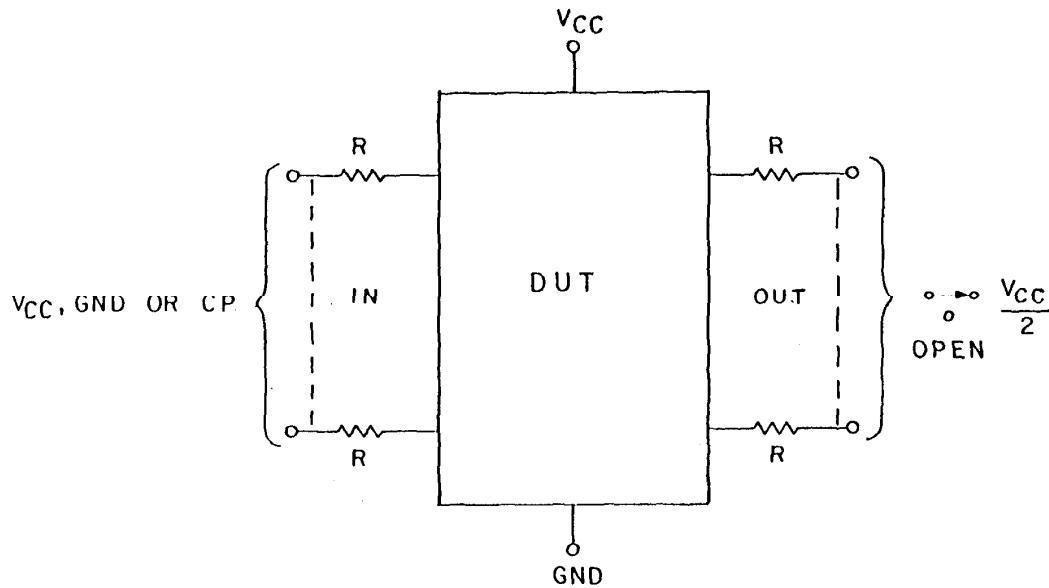
X pins have no effect on output

Device type 08

Inputs			Outputs	
CLR	CLK	D	Q	\bar{Q}
L	X	X	L	H
H	+	H	H	L
H	+	L	L	H
H	L	X	Q_0	\bar{Q}_0

X pins have no effect on output

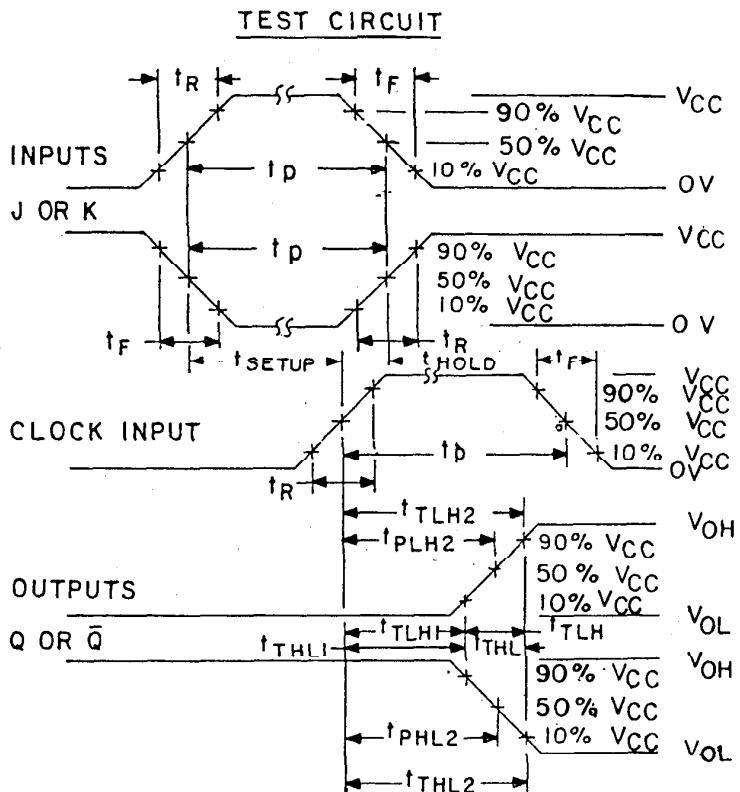
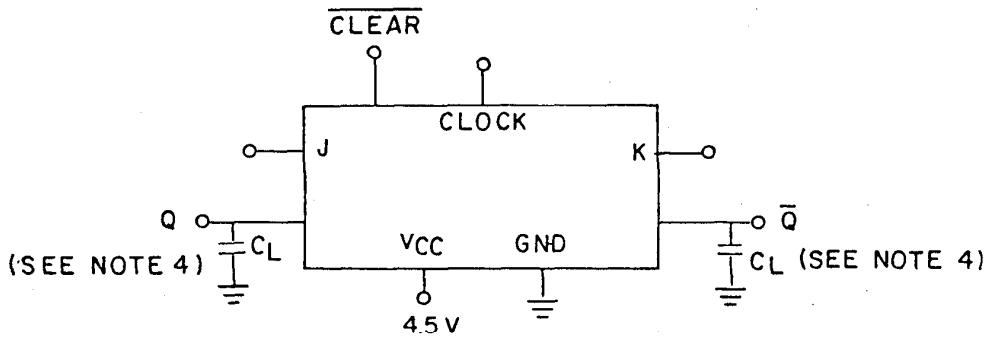
FIGURE 2. Truth tables - Continued.



NOTES:

1. For static burn-in I, all inputs shall be connected to GND. Outputs shall be open or connected to $V_{CC}/2$. Resistors are optional on outputs if open. Resistors are required on inputs and outputs connected to $V_{CC}/2$.
 $R = 470\Omega$ to $47\text{ k}\Omega$.
2. For static burn-in II, all inputs shall be connected through a resistor to V_{CC} . Outputs shall be open or connected to $V_{CC}/2$. Resistors are optional on outputs if open. Resistors are required on inputs and outputs connected to $V_{CC}/2$.
 $R = 470\Omega$ to $47\text{ k}\Omega$.
3. For dynamic burn-in, the generator shall be connected to the clock and J - K inputs and reset shall be connected to V_{CC} . Outputs shall be connected to $V_{CC}/2 + 0.5\text{ V}$ through the resistors. $R = .680\Omega$ to $1\text{ k}\Omega$ for outputs, 470Ω to $47\text{ k}\Omega$ for inputs.
4. CP = 25 kHz to 1 MHz square wave; duty cycle = 50 ± 15 percent; $V_{IH} = 4.5\text{ V}$ to V_{CC}
 $V_{IL} = 0 \pm .5\text{ V}$, transition time $\leq 0.5\text{ }\mu\text{s}$.
5. $V_{CC} = 6.0\text{ V} \pm 0.0\text{ V}$, -0.5 V . $V_{CC} = 5.5\text{ V} \pm 0.0\text{ V}$, -0.5 V for device type 52.

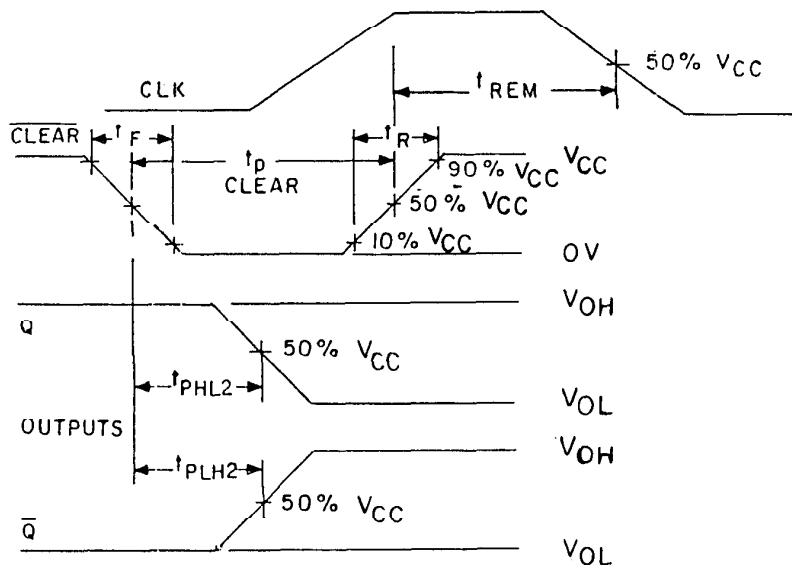
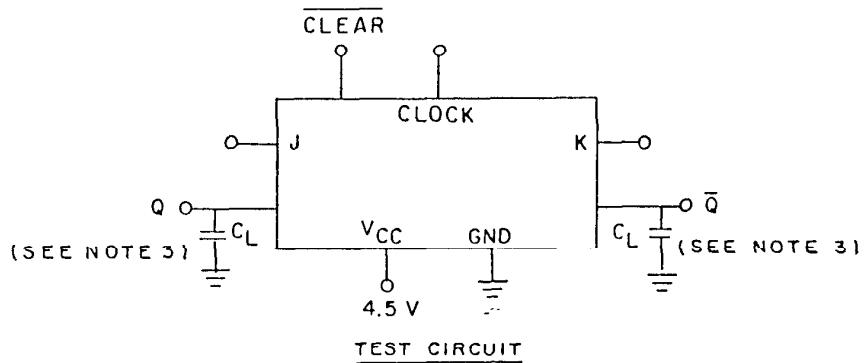
FIGURE 3. Burn in and life test circuit.

VOLTAGE WAVEFORMS

NOTES:

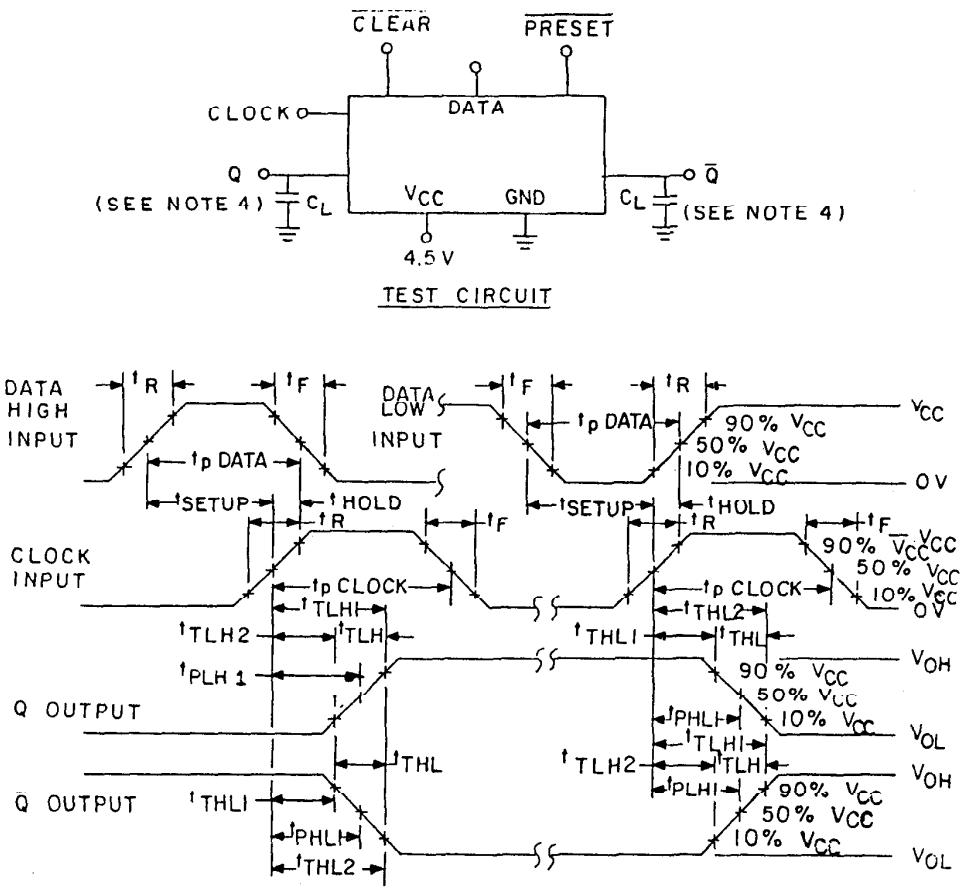
1. Clock input pulse characteristics: $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 27 ns.
2. J or K input pulse characteristics: $t_R = t_F \leq 6$ ns; $t_{SETUP} \leq 30$ ns; $t_{HOLD} \leq 8$ ns; t_p (data) ≤ 38 ns.
3. The clock input characteristics for f_{MAX} are as follows: $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 22 ns; PRR ≥ 23 MHz.
4. $C_L = 50$ pF ± 10 percent (including probe and jig capacitance).
5. Voltage measurements are to be made with respect to network ground terminal.
6. $t_{TLH} = t_{TLH2} - t_{TLH1}$; $t_{THL} = t_{THL2} - t_{THL1}$.

FIGURE 4. Synchronous switching test circuit (device type 01).

NOTES:

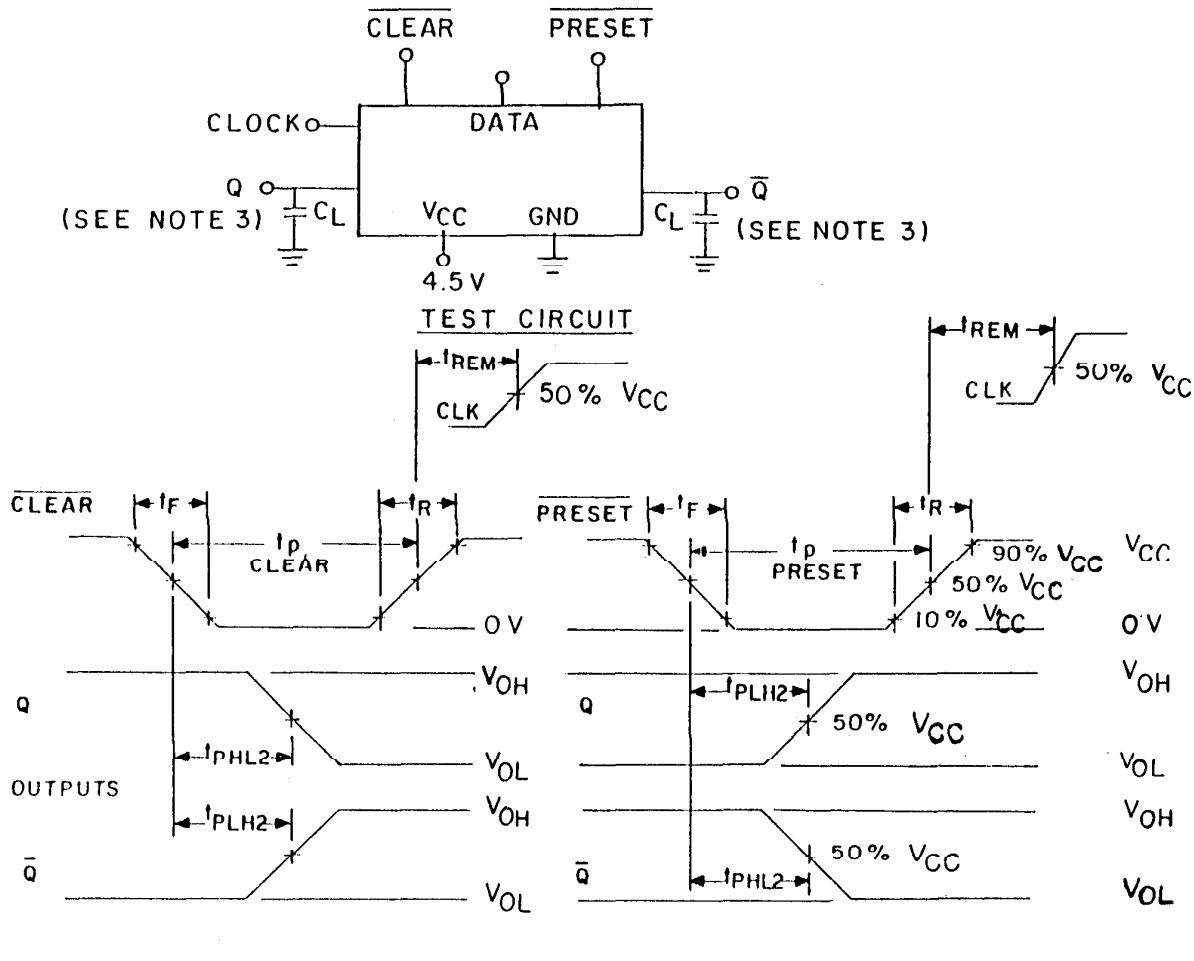
1. Clear pulses dominates regardless of the state of the other inputs.
2. Clear input pulse characteristics are as follows: $t_R = t_F \leq 6$ ns; $t_P(\text{clear}) \leq 30$ ns; $t_{REM} \leq 30$ ns;
3. $C_L = 50 \text{ pF} \pm 10 \text{ percent}$ (including probe and jig capacitance).
4. Clock pulse prior to test with inputs biased to place output at the appropriate level for test.
5. Voltage measurements are to be made with respect to network ground terminal.

FIGURE 4. Clear switching test circuit (devices type 01) - Continued.

**NOTES:**

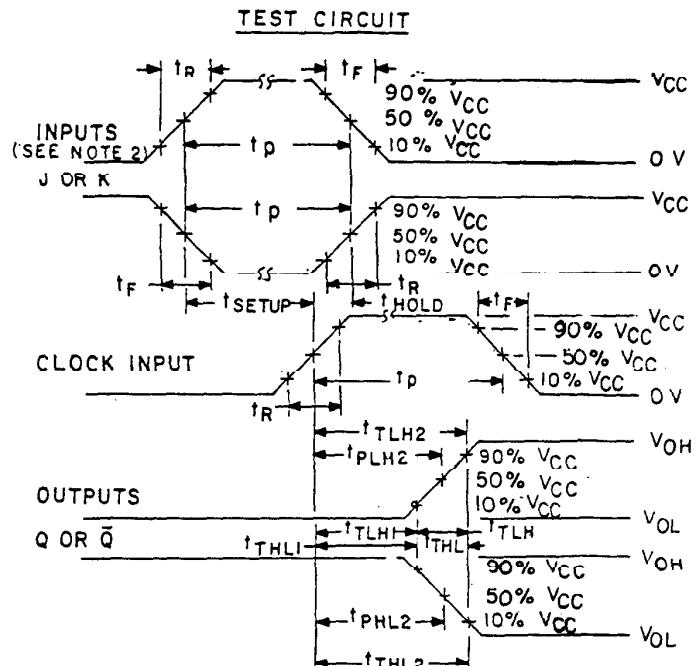
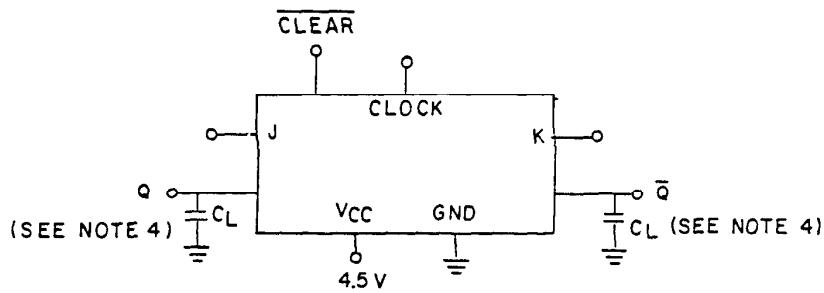
1. Clock input pulse characteristics: $t_R = t_F \leq 6$ ns. t_p (clock) ≤ 30 ns.
2. Data input pulse characteristics: $t_R = t_F \leq 6$ ns. t_p (data) ≤ 38 ns
(for device type 52). t_p (data) ≤ 33 ns; $t_{SETUP} \leq 30$ ns; $t_{HOLD} \leq 8$ ns
(for device type 52). $t_{HOLD} \leq 3$ ns; PRR ≥ 1 MHz.
3. The clock input characteristics for f_{MAX} are as follows: $t_R = t_F \leq 6$ ns;
 t_p (clock) ≤ 24 ns; PRR ≥ 21 MHz.
4. $C_L = 50$ pF ± 10 percent (including probe and jig capacitance).
5. Voltage measurements are to be made with respect to network ground terminal.
The input signal(s) for HCT device type 52 will be 0 to 3 V; however,
the 50 percent V_{CC} measuring point is 1.3 V for input and output.
6. $t_{TLH} = t_{TLH1} - t_{TLH2}$; $t_{THL} = t_{THL2} - t_{THL1}$.

FIGURE 4. Synchronous switching test circuit (device types 02 and 52) - Continued.

**NOTES:**

1. Clear and preset pulses are active low and dominates regardless of the state of the clock and data inputs.
2. Clear input pulse characteristics are as follows: $t_R = t_F \leq 6\text{ ns}$; $t_P(\text{clear}) = t_P(\text{preset}) \leq 30\text{ ns}$; $t_{REM} \leq 38\text{ ns}$;
3. $C_L = 50\text{ pF} \pm 10\text{ percent}$ (including probe and jig capacitance).
4. While testing the Clear input at a logic "0" level, Preset will have a logic "1" level applied. While testing the Preset input at a logic "0" level, Clear will have a logic "1" applied.
5. Voltage measurements are to be made with respect to network ground terminal. The input signal(s) for HCT device type 52 will be 0 - 3 volts; however, the 50 percent measurement point is 1.3 V for inputs and outputs.

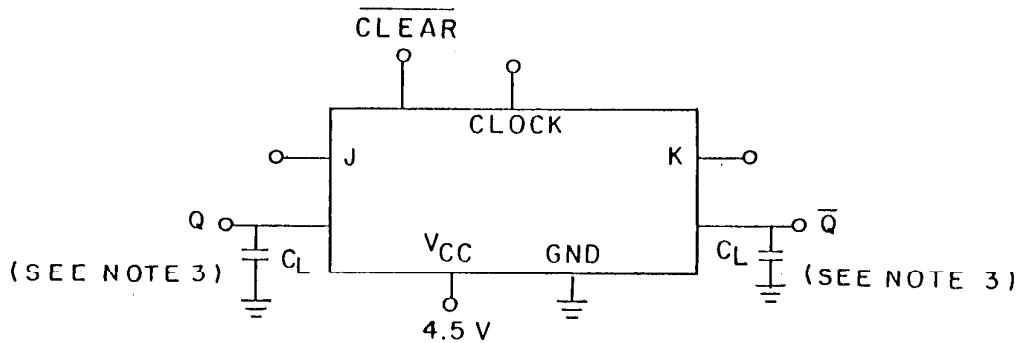
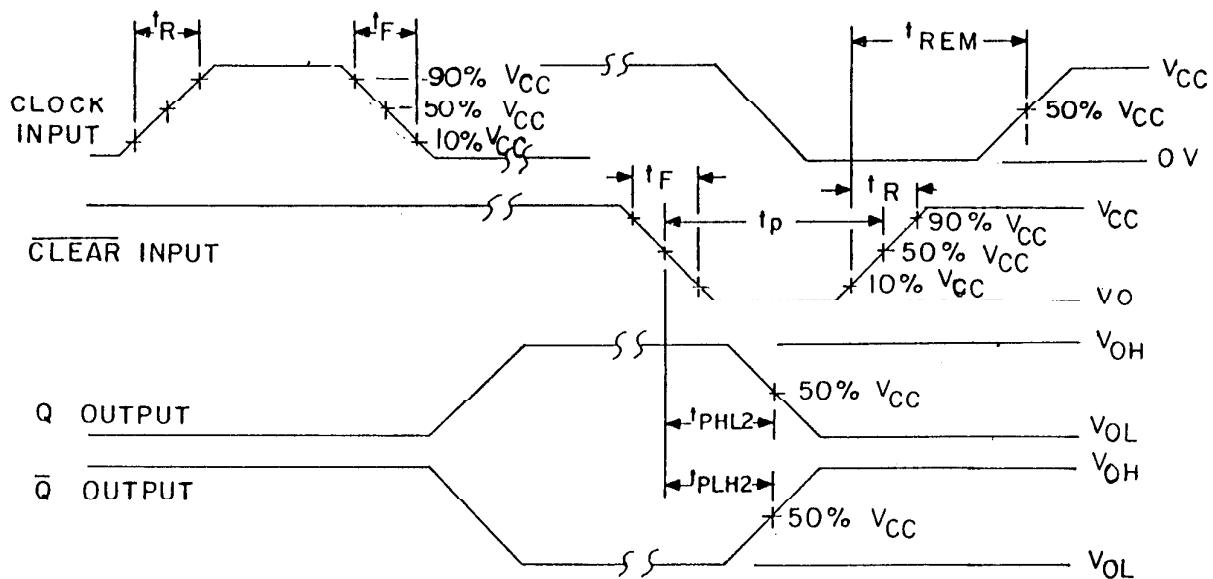
FIGURE 4. Clear switching test circuit (devices types 02 and 52) - Continued.

VOLTAGE WAVEFORMS

NOTES:

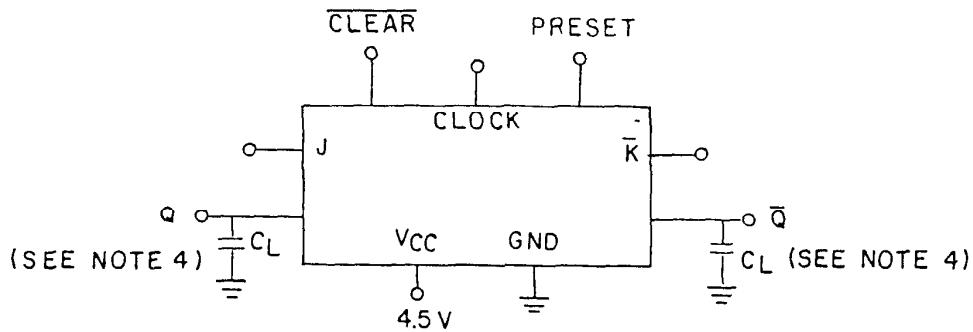
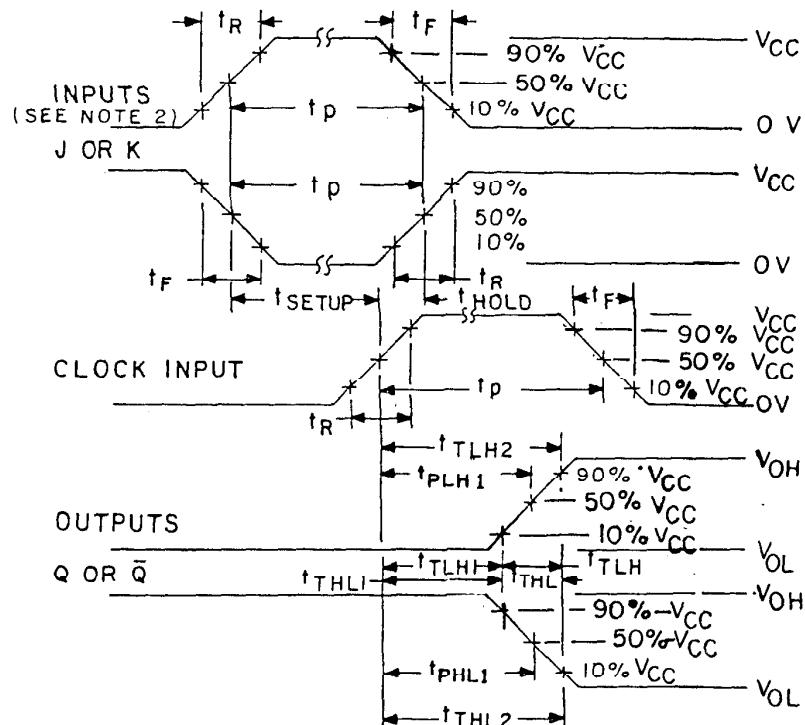
1. Clock input pulse characteristics: $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 27 ns.
2. J or K input pulse characteristics: $t_R = t_F \leq 6$ ns; t_p (J or K) ≤ 38 ns; $t_{HOLD} \leq 8$ ns; $t_{SETUP} \leq 30$ ns; PRR ≤ 1 MHz.
3. The clock input characteristics for f_{MAX} are as follows:
 $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 22 ns; PRR ≥ 23 MHz.
4. $C_L = 50 \text{ pF} \pm 10 \text{ percent}$ (including probe and jig capacitance).
5. Voltage measurements are to be made with respect to network ground terminal.
6. $t_{TLH} = t_{TLH2} - t_{TLH1}$; $t_{THL} = t_{THL2} - t_{THL1}$.

FIGURE 4. Synchronous switching test circuit (device type 03) - Continued.

TEST CIRCUITNOTES:

1. Clear pulses dominates regardless of the state of the other inputs.
2. Clear input pulse characteristics are as follows: $t_R = t_F \leq 6$ ns; t_p (clear) ≤ 30 ns; $t_{REM} \leq 30$ ns;
3. $C_L = 50$ pF ± 10 percent (including probe and jig capacitance).
4. Clock pulse prior to test with inputs biased to place output at the appropriate level for test.
5. Voltage measurements are to be made with respect to network ground terminal.

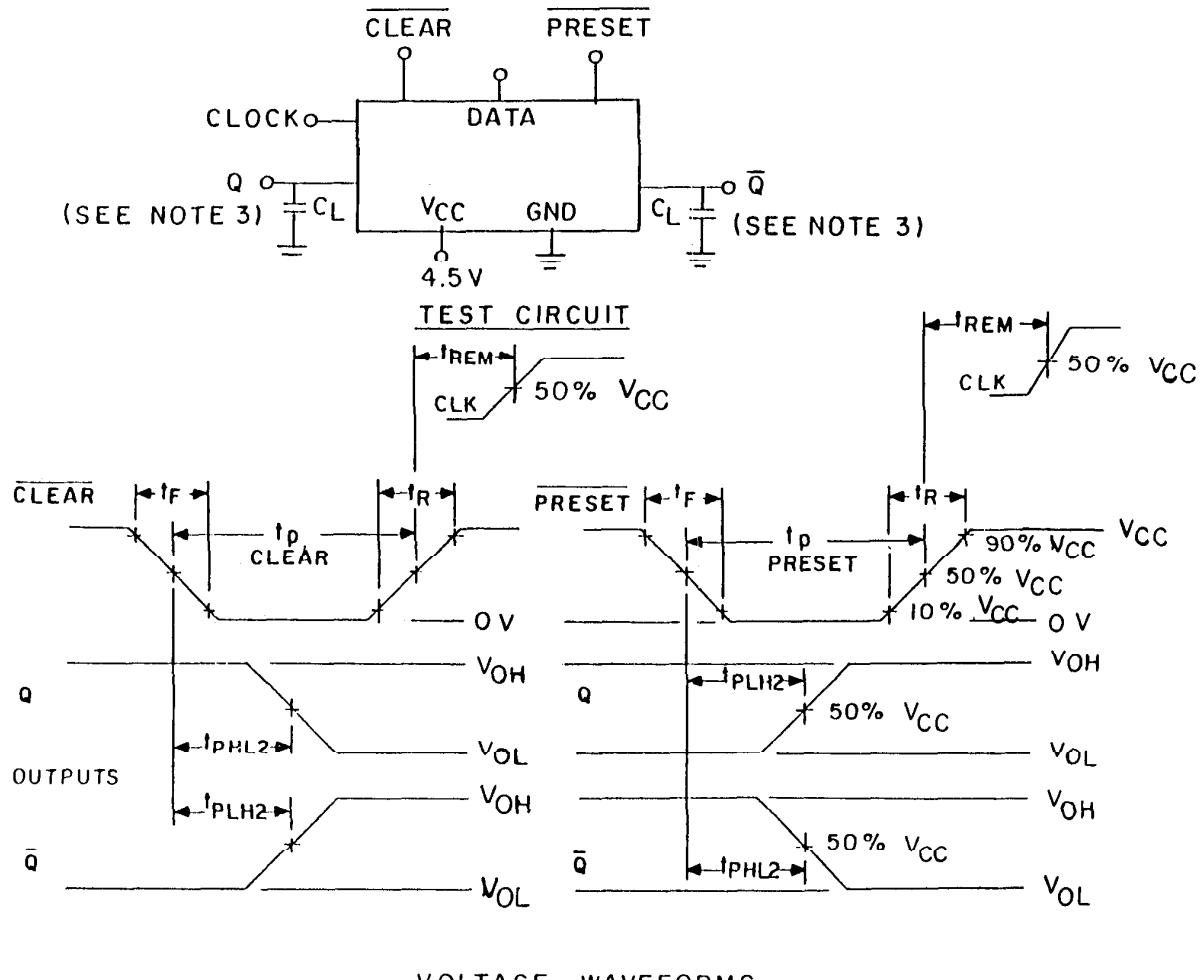
FIGURE 4. Clear switching test circuit (devices type 03) - Continued.

TEST CIRCUITVOLTAGE WAVEFORMS

NOTES:

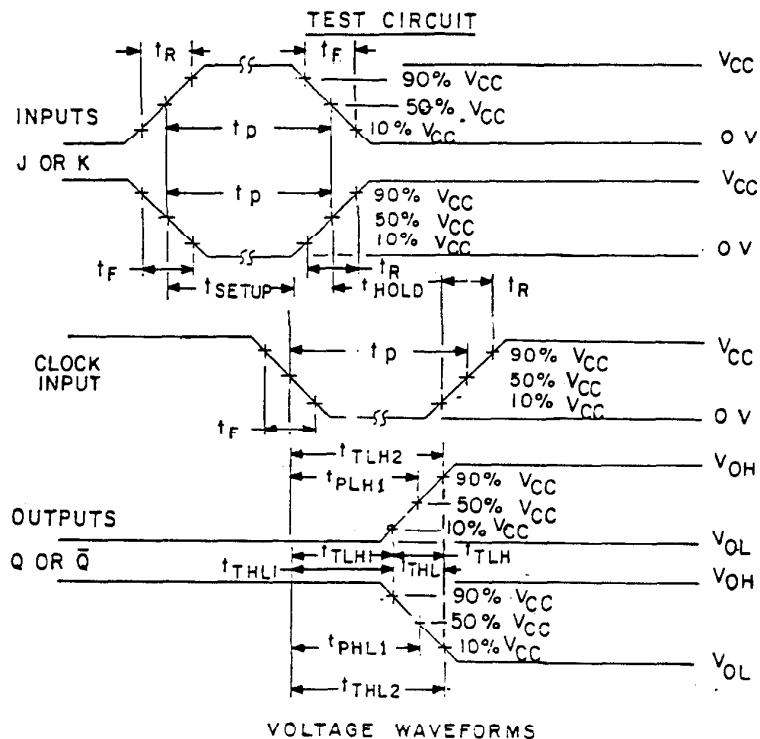
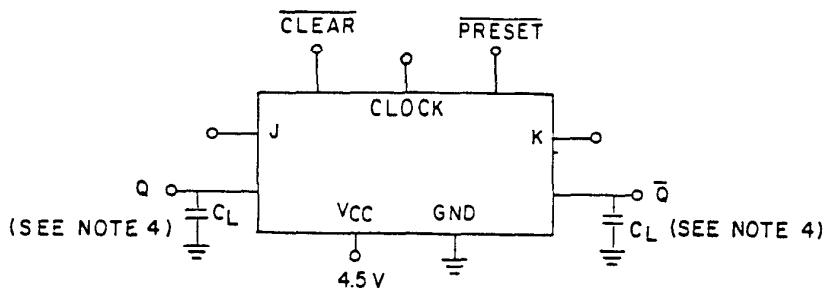
1. Clock input pulse characteristics: $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 27 ns.
2. J or K input pulse characteristics: $t_R = t_F \leq 6$ ns; t_p (J or K) ≤ 38 ns; $t_{HOLD} \leq 8$ ns; $t_{SETUP} \leq 30$ ns.
3. The clock input characteristics for f_{MAX} are as follows: $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 22 ns; PRR ≥ 23 MHz.
4. $C_L = 50$ pF ± 10 percent (including probe and jig capacitance).
5. Voltage measurements are to be made with respect to network ground terminal.
6. $t_{TLH} = t_{TLH2} - t_{TLH1}$; $t_{THL} = t_{THL2} - t_{THL1}$.

FIGURE 4. Synchronous switching test circuit (device type 03) - Continued.

**NOTES:**

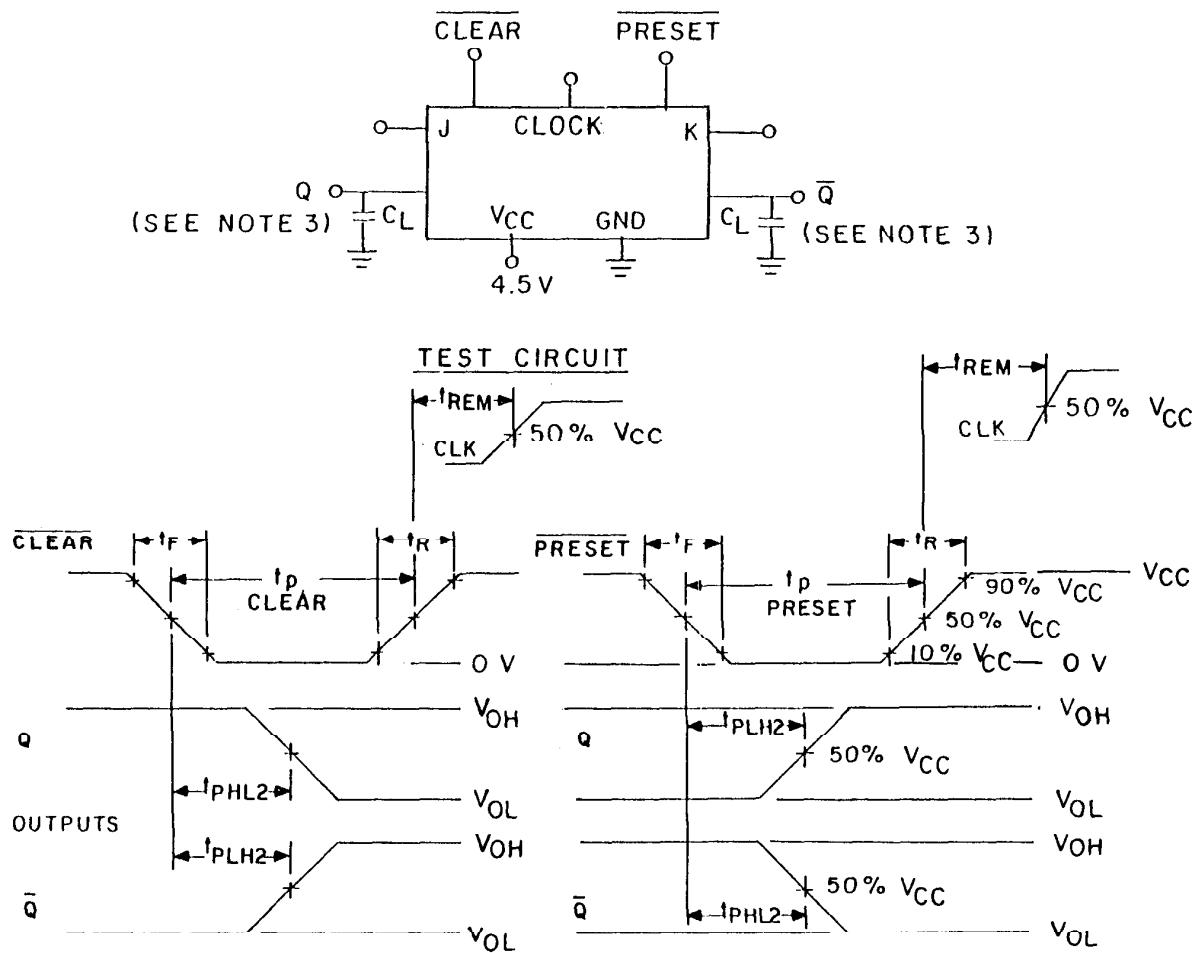
1. Clear and preset pulses are active low and dominates regardless of the state of the other inputs.
2. Clear input pulse characteristics are as follows: $t_R = t_F \leq 6$ ns; t_p (clear or preset) ≤ 30 ns; $t_{REM} \leq 30$ ns;
3. $C_L = 50$ pF ± 10 percent (including probe and jig capacitance).
4. May be pulsed prior to test with inputs biased to place output at the appropriate level for test.
5. Voltage measurements are to be made with respect to network ground terminal.

FIGURE 4. CTear and Preset switching test circuit (devices type 04) - Continued.

**NOTES:**

1. Clock input pulse characteristics: $t_R = t_F \leq 6$ ns;
 t_p (clock) ≤ 30 ns.
2. J or K input pulse characteristics: $t_R = t_F \leq 6$ ns;
 t_p (J or K) ≤ 38 ns; $t_{HOLD} \leq 8$ ns; $t_{SETUP} \leq 30$ ns.
3. The clock input characteristics for f_{MAX} are as follows:
 $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 24 ns; PRR ≥ 21 MHz.
4. $C_L = 50$ pF ± 10 percent (including probe and jig capacitance).
5. Voltage measurements are to be made with respect to network ground terminal.
6. $t_{TLH} = t_{TLH2} - t_{TLH1}$; $t_{\bar{H}L} = t_{THL2} - t_{THL1}$.

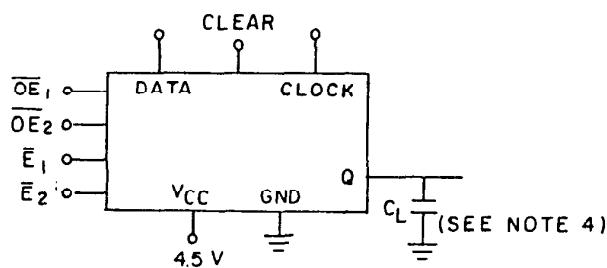
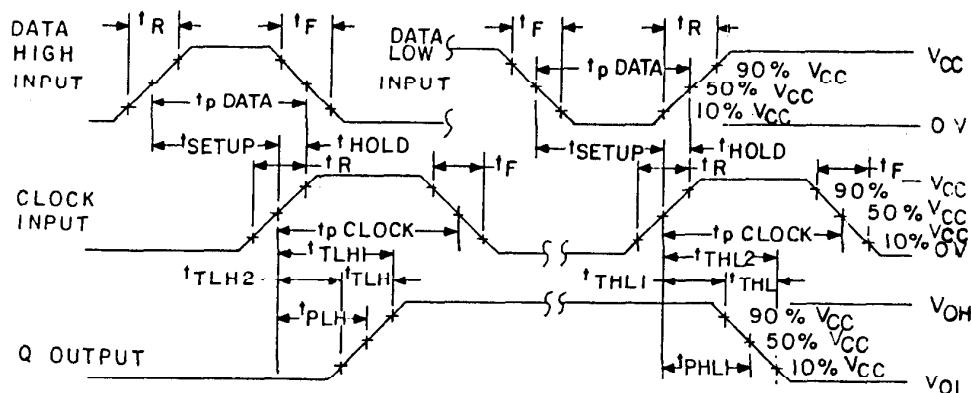
FIGURE 4. Synchronous switching test circuit (device type 05) - Continued.

VOLTAGE WAVEFORMS

NOTES:

1. Clear and preset pulses are active low and dominates regardless of the state of the other inputs.
2. Clear and preset input pulse characteristics are as follows: $t_R = t_F \leq 6$ ns; t_p (clear or preset) ≤ 30 ns; $t_{REM} \leq 30$ ns;
3. $C_L = 50$ pF ± 10 percent (including probe and jig capacitance).
4. May be pulsed prior to test with inputs biased to place output at the appropriate level for test.
5. Voltage measurements are to be made with respect to network ground terminal.

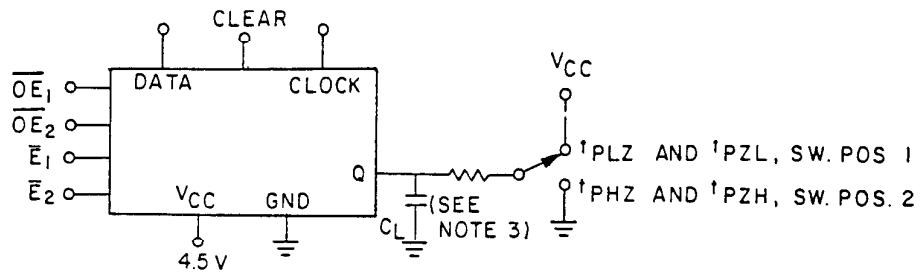
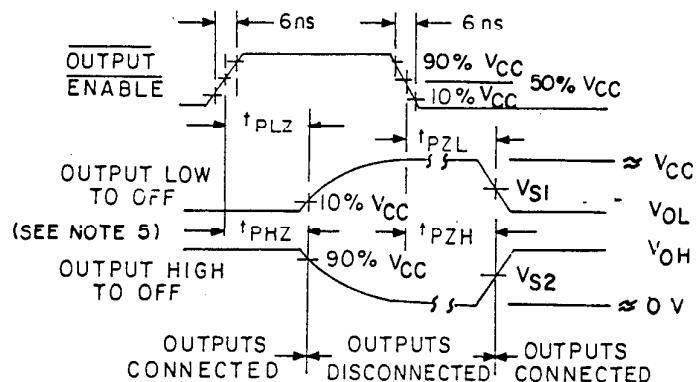
FIGURE 4. Clear and Preset switching test circuit (devices type 05) - Continued.

TEST CIRCUITVOLTAGE WAVEFORMS

NOTES:

1. Clock input pulse characteristics: $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 27 ns.
2. Data input pulse characteristics: $t_R = t_F \leq 6$ ns; t_p (data) ≤ 38 ns; $t_{SETUP} \leq 30$ ns; $t_{HOLD} \leq 8$ ns; PRR ≥ 1 MHz.
3. The clock input characteristics for f_{MAX} are as follows: $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 22 ns; PRR ≥ 23 MHz.
4. $C_L = 50 \text{ pF} \pm 10\%$ percent (including probe and jig capacitance).
5. $t_{TLH} = t_{TLH2} - t_{TLH1}$; $t_{THL} = t_{THL2} - t_{THL1}$.
6. Voltage measurements are to be made with respect to network ground terminal.

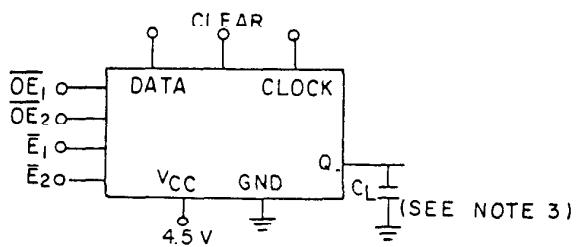
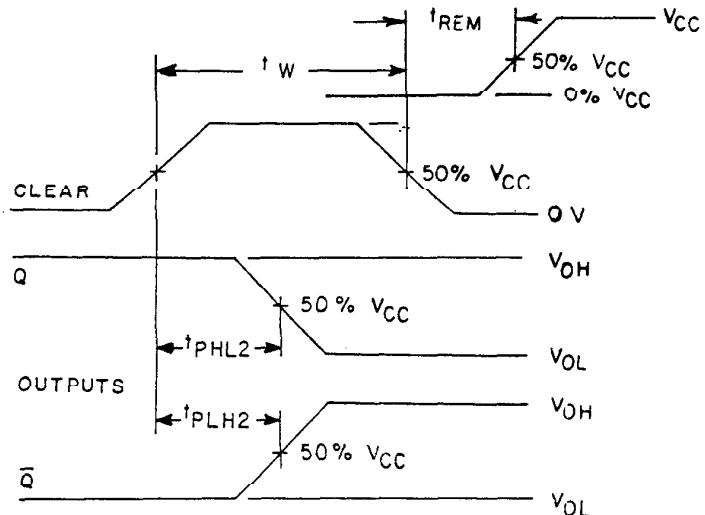
FIGURE 4. Synchronous switching test circuit (device type 06) - Continued.

TEST CIRCUITVOLTAGE WAVEFORMS

NOTES:

1. \overline{OE}_1 and \overline{OE}_2 pulses are active low and one or both must be active to enable the outputs.
2. \overline{OE}_1 and \overline{OE}_2 input pulse characteristics are as follows: $t_R = t_F \leq 6$ ns; $t_{pOE_1} = t_{pOE_2} \geq 200$ ns.
3. $C_L = 50$ pF ± 10 percent (including probe and jig capacitance).
4. OE_1 or OE_2 does not affect the sequential operation of the flip-flop.
5. For t_{PHZ} and t_{PZH} , a 1 k Ω resistor is connected between the output and GND terminal. For t_{PZL} and t_{PLZ} , a 1 k Ω resistor is connected between the output and V_{CC} terminal. $VS_1 = VOL + 0.1$ V ($VOH - VOL$). $VS_2 = VOH - 0.1$ V ($VOH - VOL$).

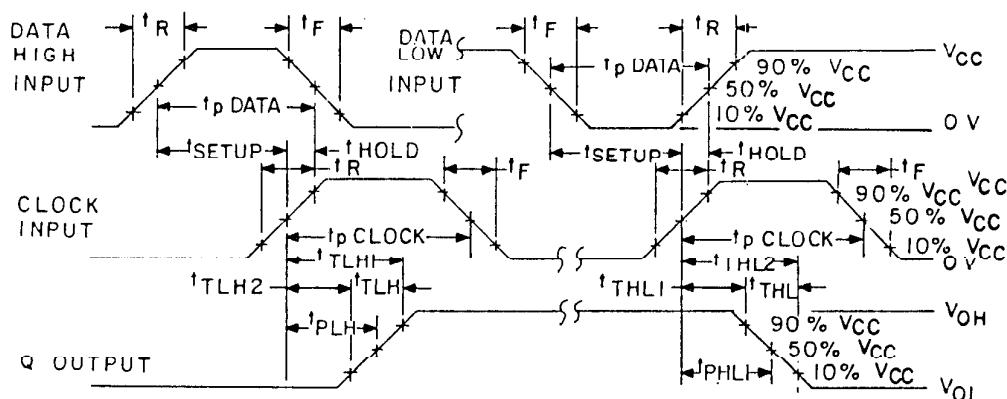
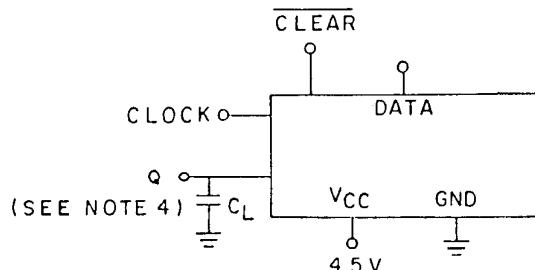
FIGURE 4. Three-state switching test circuit (device type 06) - Continued.

TEST CIRCUITVOLTAGE WAVEFORMS

NOTES:

1. Clear pulses are active high and dominates regardless of the state of the clock and data inputs.
2. Clear input pulse characteristics are as follows: $t_R = t_F \leq 6$ ns; t_p (clear) = t_p (preset) < 30 ns; $t_{REM} \leq 27$ ns.
3. $C_L = 50$ pF ± 10 percent (including probe and jig capacitance).
4. Clock pulse prior to test with inputs biased to place output at the appropriate level for test.
5. Voltage measurements are to be made with respect to network ground terminal.

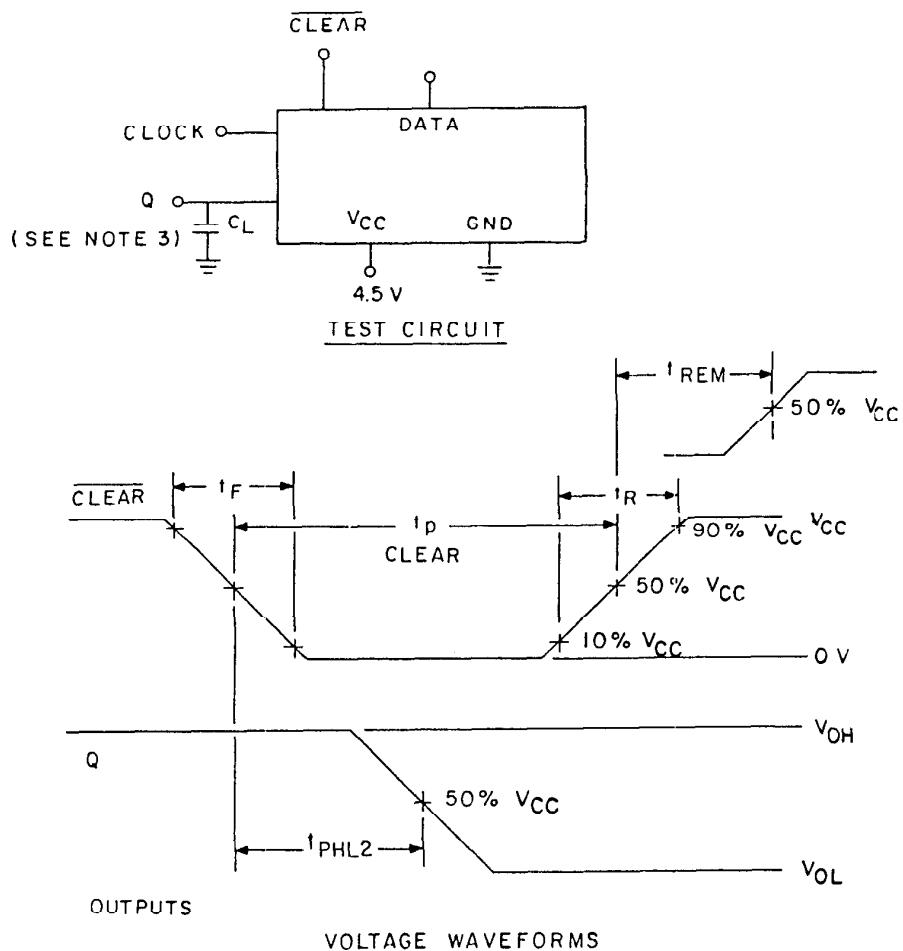
FIGURE 4. Clear switching test circuit (device type 06) - Continued.



NOTES:

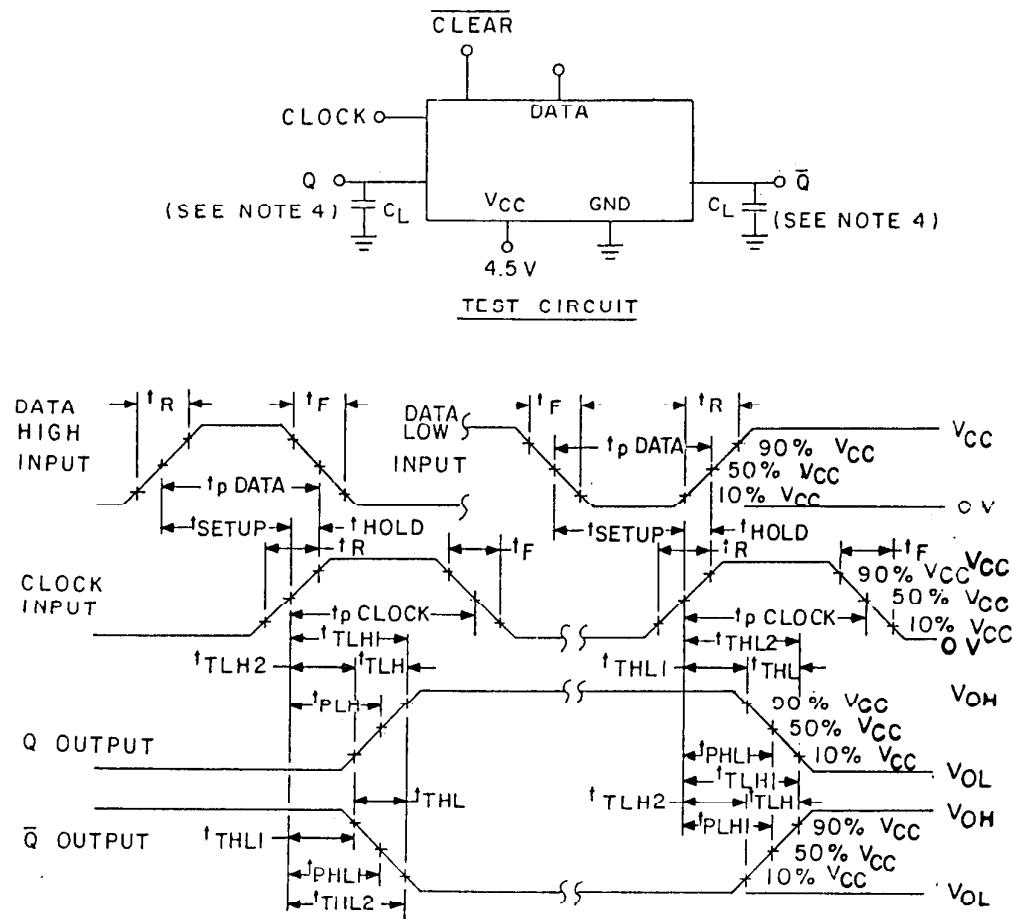
1. Clock input pulse characteristics: $t_R = t_F \leq 6$ ns; $t_p(\text{clock}) \leq 27$ ns.
2. Data input pulse characteristics: $t_R = t_F \leq 6$ ns; $t_p(\text{data}) \leq 38$ ns; $t_{\text{SETUP}} \leq 30$ ns; $t_{\text{HOLD}} \leq 8$ ns.
3. The clock input characteristics for f_{MAX} are as follows:
 $t_R = t_F \leq 6$ ns; $t_p(\text{clock}) \leq 22$ ns; PRR ≥ 23 MHz.
4. $C_L = 50 \text{ pF} \pm 10$ percent (including probe and jig capacitance).
5. Voltage measurements are to be made with respect to network ground terminal.
6. $t_{\text{TLH}} = t_{\text{TLH2}} - t_{\text{TLH1}}$; $t_{\text{THL}} = t_{\text{THL2}} - t_{\text{THL1}}$.

FIGURE 4. Synchronous switching test circuit (device type 07) - Continued.

**NOTES:**

1. Clear pulses are active high and dominates regardless of the state of the clock and data inputs.
2. Clear input pulse characteristics are as follows: $t_R = t_F \leq 6\text{ ns}$; t_p (clear) $\leq 24\text{ ns}$; $t_{REM} \leq 30\text{ ns}$.
3. $C_L = 50\text{ pF} \pm 10\text{ percent}$ (including probe and jig capacitance).
4. Clock pulse prior to test with inputs biased to place output at the appropriate level for test.
5. Voltage measurements are to be made with respect to network ground terminal.

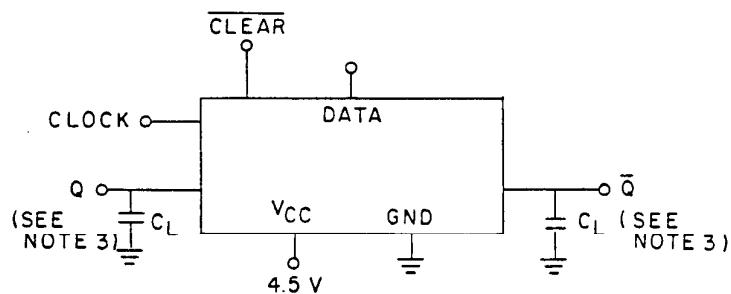
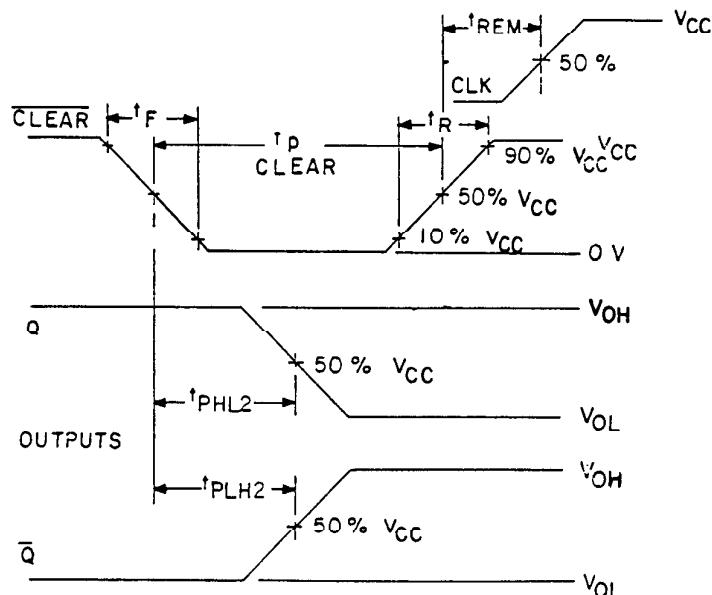
FIGURE 4. Clear switching test circuit (device type 07) - Continued.

VOLTAGE WAVEFORMS

NOTES:

1. Clock input pulse characteristics: $t_R = t_F \leq 6$ ns;
 t_p (clock) ≤ 24 ns.
2. Data input pulse characteristics: $t_R = t_F \leq 6$ ns;
 t_p (data) ≤ 38 ns; $t_{SETUP} \leq 30$ ns; $t_{HOLD} \leq 8$ ns.
3. The clock input characteristics for f_{MAX} are as follows:
 $t_R = t_F \leq 6$ ns; t_p (clock) ≤ 19 ns; PRR ≥ 26 MHz.
4. $C_L = 50 \text{ pF} \pm 10 \text{ percent}$ (including probe and jig capacitance).
5. Voltage measurements are to be made with respect to network ground terminal.
6. $t_{TLH} = t_{TLH2} - t_{TLH1}$; $t_{THL} = t_{THL2} - t_{THL1}$.

FIGURE 4. Synchronous switching test circuit (device type 08) - Continued.

TEST CIRCUITVOLTAGE WAVEFORMS

NOTES:

1. Clear pulses are active high and dominates regardless of the state of the clock and data inputs.
2. Clear input pulse characteristics are as follows: $t_R = t_F \leq 6\text{ ns}$; t_p (clear) $\leq 24\text{ ns}$; $t_{REM} \leq 30\text{ ns}$.
3. $C_L = 50\text{ pF} \pm 10\text{ percent}$ (including probe and jig capacitance).
4. Clock pulse prior to test with inputs biased to place output at the appropriate level for test.
5. Voltage measurements are to be made with respect to network ground terminal.

FIGURE 4. Clear switching test circuit (device type 07) - Continued.

TABLE III. Group A Inspection for device type 01.

Symbol	Case	terminal conditions ΣI														Test limits						Subgroup 1		Subgroup 2		Subgroup 3		Unit
		STD-Tcases		2		3		4		6		8		9		10		12		13		Measured terminal		$T_C = +25^\circ C$		$T_C = +125^\circ C$		
		C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	18	19	20	$T_C = -55^\circ C$	$T_C = -55^\circ C$	$T_C = -55^\circ C$	$T_C = -55^\circ C$			
I_{IC} (pos) $\underline{\underline{I}}$	1	1 mA	2	1 mA	3	1 mA	4	1 mA	5	1 mA	6	1 mA	7	1 mA	8	1 mA	9	1 mA	10	1 mA	11	1 mA	12	1 mA	13	1 mA	V	
I_{IC} (neg) $\underline{\underline{I}}$	9	-1 mA	10	-1 mA	11	-1 mA	12	-1 mA	13	-1 mA	14	-1 mA	15	-1 mA	16	-1 mA	17	-1 mA	18	-1 mA	19	-1 mA	20	-1 mA	21	-1 mA	V	
I_{CC}	17	6.0 V	18	6.0 V	GND	6.0 V	$\underline{\underline{I}}$	6.0 V	GND	6.0 V	$\underline{\underline{I}}$	6.0 V	$\underline{\underline{I}}$	6.0 V	$\underline{\underline{I}}$	6.0 V	$\underline{\underline{I}}$	6.0 V	$\underline{\underline{I}}$	6.0 V	$\underline{\underline{I}}$	6.0 V	$\underline{\underline{I}}$	6.0 V	$\underline{\underline{I}}$	6.0 V	μA	
V_{OH3}	19	1.2 V	20	1.2 V	21	4.2 V	22	4.2 V	23	4.2 V	24	4.2 V	25	4.2 V	26	4.2 V	27	4.2 V	28	4.2 V	29	4.2 V	30	4.2 V	31	4.2 V	V	
V_{OL5}	3006	25-30	Same terminal conditions as specified above for V_{OH3} except $I_{OH} = 5.2 \text{ mA}$.														5.48	5.2	5.48	5.2	5.48	5.2	5.48	5.2	5.48	5.2	V	
V_{OL3}	3007	31	1.2 V	32	1.2 V	33	2/ $\underline{\underline{I}}$	34	4.2 V	35	4.2 V	36	4.2 V	37	4.2 V	38	4.2 V	39	4.2 V	40	4.2 V	41	4.2 V	42	4.2 V	43	4.2 V	μA
V_{OL5}	3007	37-42	Same terminal conditions as specified above for V_{OL3} except $I_{OL} = 5.2 \text{ mA}$.														0.26	0.4	0.26	0.4	0.26	0.4	0.26	0.4	0.26	0.4	V	

See footnotes at end of table.

TABLE II. Group A inspection for device type 01 - continued.

Symbol	Base no.	Test method	terminal conditions $\frac{V}{I}$												Test limits									
			Measured terminal conditions						Subgroup 1 $T_C = +25^\circ C$						Subgroup 2 $T_C = +125^\circ C$				Subgroup 3 $T_C = -55^\circ C$					
			2	3	4	5	6	8	10	12	13	14	16	18	19	20	10	12	13	14	Min	Max	Min	Max
I0S1	3011	43	3/ 4/	4.0 V	GND	4.0 V	4.0 V	4.0 V	4.0 V	4.0 V	4.0 V	4.0 V	GND	GND	4.0 V	GND	10	-10	-10	-10	-120	-120	-120	-120
	44	45	4/	4.0 V	4.0 V	3/ 4/	3/ 4/	3/ 4/	3/ 4/	3/ 4/	3/ 4/	3/ 4/	GND	GND	4.0 V	GND	20	-20	-20	-20	-10	-10	-10	-10
IIH	3010	47	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	GND	GND	6.0 V	GND	10LK	10LK	10LK	10LK	0.1	0.1	0.1	0.1
	48	49	50	51	52	53	54	54	54	54	54	54	GND	GND	6.0 V	GND	1K	1K	1K	1K	20LK	20LK	20LK	20LK
III	3009	55	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	5.0 V	5.0 V	5.0 V	5.0 V	10LK	10LK	10LK	10LK
	56	57	58	59	60	61	62	62	62	62	62	62	GND	GND	GND	GND	20	20	20	20	20LK	20LK	20LK	20LK
C1	3012	63	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	64	65	66	67	68	69	70	70	70	70	70	70	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
Truth table tests G/	71	A	B	B	B	B	B	B	B	B	B	B	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	72	A	B	B	B	B	B	B	B	B	B	B	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
	73	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	74	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
	75	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	76	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
	77	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	78	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
	79	B	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	80	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
	81	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	82	B	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
	83	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	84	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
	85	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	86	B	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
	87	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10	10	10	10	10	10	10	10
	88	B	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10LK	10LK	10LK	10LK	20LK	20LK	20LK	20LK
	89	A	A	A	A	A	A	A	A	A	A	A	GND	GND	GND	GND	10	10	10	10	10	10	10	10

See footnotes at end of table.

TABLE III. Group A inspection for device type 01 - Continued.

Symbol	Case no.	Terminal conditions Σ												Test limits Σ						
		Subgroup 9						Subgroup 10						Subgroup 11						
		Test method	ICLK	ICTR	1K	VCC	2CLK	2CTR	2J	2Q	2R	GND	IQ	IT	IU	Measured terminal	TC = +25°C	TC = +125°C	TC = -55°C	
f _{MAX} (Fig. 4)	90	IN	4.5 V	4.5 V	4.5 V	"	IN	4.5 V	4.5 V	OUT	4.5 V	GND	OUT	4.5 V	10	31	3	NHz		
<u>7/ 9/</u>	91	IN	4.5 V	4.5 V	4.5 V	"	IN	4.5 V	4.5 V	OUT	4.5 V	"	OUT	4.5 V	20	"	"	"		
<u>7/ 9/</u>	92	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
<u>7/ 9/</u>	93	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
tplHL1	3003 (Fig. 4)	94	IN	4.5 V	4.5 V	GND	"	IN	4.5 V	IN	OUT	GND	OUT	IN	1ICLK to 1Q	5	30	5	30	
<u>7/</u>	95	IN	4.5 V	4.5 V	4.5 V	IN	"	IN	4.5 V	GND	OUT	IN	OUT	GND	1ICLK to 1Q	"	"	"	"	
<u>7/</u>	96	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLK to 1Q	"	"	"	"	
<u>7/</u>	97	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLK to 1Q	"	"	"	"	
tplHL1	3003 (Fig. 4)	98	IN	4.5 V	4.5 V	"	"	IN	4.5 V	4.5 V	OUT	4.5 V	IN	OUT	IN	1ICLK to 1Q	"	"	"	"
<u>7/</u>	99	IN	4.5 V	4.5 V	4.5 V	IN	"	IN	4.5 V	4.5 V	OUT	4.5 V	IN	OUT	GND	1ICLK to 1Q	"	"	"	"
<u>7/</u>	100	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLK to 1Q	"	"	"	"	
<u>7/</u>	101	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLK to 1Q	"	"	"	"	
tplHL2	3003 (Fig. 4)	102	10/	IN	GND	"	"	10/	IN	4.5 V	OUT	GND	OUT	IN	1ICLR to 1Q	6	32	6	32	
<u>7/</u>	103	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLR to 1Q	"	"	"	"	
<u>7/</u>	104	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLR to 1Q	"	"	"	"	
tplHL2	3003 (Fig. 4)	105	10/	IN	GND	"	"	10/	IN	4.5 V	OUT	GND	OUT	IN	1ICLR to 1Q	6	32	6	32	
<u>7/</u>	106	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLR to 1Q	"	"	"	"	
tplHL	3004 (Fig. 4)	107	IN	4.5 V	4.5 V	GND	"	IN	4.5 V	4.5 V	OUT	GND	OUT	IN	1ICLR to 2Q	3	15	3	15	
<u>7/</u>	108	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLR to 2Q	"	"	"	"	
<u>7/</u>	109	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLR to 2Q	"	"	"	"	
tplHL	3004 (Fig. 4)	110	IN	4.5 V	4.5 V	GND	"	IN	4.5 V	4.5 V	OUT	GND	OUT	IN	1ICLR to 2Q	3	20	3	20	
<u>7/</u>	111	IN	4.5 V	4.5 V	4.5 V	GND	"	IN	4.5 V	4.5 V	OUT	GND	OUT	IN	1ICLR to 2Q	"	"	"	"	
<u>7/</u>	112	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLR to 2Q	"	"	"	"	
<u>7/</u>	113	"	"	"	"	"	"	"	"	"	"	"	"	"	1ICLR to 2Q	"	"	"	"	

See footnotes at end of table.

TABLE III. Group A inspection for device type 02.

Symbol	Case	Terminal Conditions $\frac{1}{V}$														Test limits					
		Subgroup 1 TC = +25°C			Subgroup 2 TC = +125°C			Subgroup 3 TC = -55°C			Subgroup 1 TC = +25°C			Subgroup 2 TC = +125°C			Subgroup 3 TC = -55°C				
MIL-STO-953-001	2	2	3	4	6	8	9	10	12	13	14	16	18	19	20	GND	ICTR	$\frac{1}{I_D}$	1.5		
Tcases	3															ID	ICLX				
C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14		JPRE					
Test	4															2PRE					
TC(LX)	5															2CLK					
I(DS)	6															2CLR					
Test	7																				
TD	8																				
V _{CC}	9	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	GND	ICTR	$\frac{1}{I_D}$	1.5		
V _{DS}	10	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	ID	ICLX				
V _{DS}	11															JPRE					
V _{DS}	12															2PRE					
V _{DS}	13															2CLK					
V _{DS}	14															2CLR					
V _{DS}	15																				
V _{DS}	16																				
V _{CC}	17	6.0 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	6.0 V	$\frac{6.0}{V_{CC}}$	0.15	15	$\frac{\mu A}{\mu A}$	
V _{CC}	18	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	6.0 V	$\frac{6.0}{V_{CC}}$	0.15	15	$\frac{\mu A}{\mu A}$	
V _{OH3}	19	4.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	4.2 V	1.2 V	1.2 V	1.2 V	1.2 V	
V _{OH3}	20	1.2 V														-20 μA	-20 μA				
V _{OH3}	21															-20 μA	-20 μA				
V _{OH3}	22															-20 μA	-20 μA				
V _{OH3}	23															-20 μA	-20 μA				
V _{OH3}	24															-20 μA	-20 μA				
V _{OH3}	25															-20 μA	-20 μA				
V _{OH3}	26															-20 μA	-20 μA				
V _{OH5}	3006	27-34 Same terminal conditions as specified above for V _{OH3} except I _{OH} = -5.2 mA.														5.48		5.2	5.48		
V _{OL3}	35	4.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	6.0 V	10	0.05	0.05	$\frac{C_0}{C_u}$	
V _{OL3}	36	1.2 V														20 μA	4.2 V	1.2 V	1.2 V	1.2 V	
V _{OL3}	37															20 μA	4.2 V	1.2 V	1.2 V	1.2 V	
V _{OL3}	38															20 μA	4.2 V	1.2 V	1.2 V	1.2 V	
V _{OL3}	39															20 μA	4.2 V	1.2 V	1.2 V	1.2 V	
V _{OL3}	40															20 μA	4.2 V	1.2 V	1.2 V	1.2 V	
V _{OL3}	41															20 μA	4.2 V	1.2 V	1.2 V	1.2 V	
V _{OL3}	42															20 μA	4.2 V	1.2 V	1.2 V	1.2 V	

See footnotes at end of table.

TABLE III. Group A inspection for device type 02 - Continued.

Symbol	MIL-STD-883C,D	Test no.	Terminal conditions 1/												Test limits														
			Case 2	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Subgroup 1	Subgroup 2	Subgroup 3	Unit	$T_C = +25^\circ C$	$T_C = +125^\circ C$	$T_C = -55^\circ C$				
Y0L5	3007	143-5/ Same terminal conditions as specified above for Y0L3 except $I_{OL} = 5.2 \text{ mA}$.																											
10S4	3011	51	4.0 V	GND	GND	GND	4.0 V											4.0 V	12	-10	-120	-10	-120	-10	-120	-10	-120	-10	
		52	GND	GND	GND	GND													15	*	*	*	*	*	*	*	*	*	*
		53																	25	*	*	*	*	*	*	*	*	*	*
		54																	20	*	*	*	*	*	*	*	*	*	*
11W	3010	55	6.0 V	6.0 V														6.0 V	ICLR	-0.5	0.1								
		57																	ICLK	*	*	*	*	*	*	*	*	*	*
		58																	PTE	*	*	*	*	*	*	*	*	*	*
		59																	2PTE	*	*	*	*	*	*	*	*	*	*
		60																	2CLK	*	*	*	*	*	*	*	*	*	*
		61																	2D	*	*	*	*	*	*	*	*	*	*
		62																	2CR	*	*	*	*	*	*	*	*	*	*
11L	3009	63	GND	GND	GND	GND												6.0 V	ICLR	-0.5	-0.1								
		64																	ICLK	*	*	*	*	*	*	*	*	*	*
		65																	PTE	*	*	*	*	*	*	*	*	*	*
		66																	2PTE	*	*	*	*	*	*	*	*	*	*
		67																	2CLK	*	*	*	*	*	*	*	*	*	*
		68																	2D	*	*	*	*	*	*	*	*	*	*
		69																	2CR	*	*	*	*	*	*	*	*	*	*
		70																											
C1	3012	71	4/		4/													GND	ICLR	10									
		72																	ICLK	*	*	*	*	*	*	*	*	*	*
		73																PTE	*	*	*	*	*	*	*	*	*	*	
		74																2PTE	*	*	*	*	*	*	*	*	*	*	
		75																2CLK	*	*	*	*	*	*	*	*	*	*	
		76																2D	*	*	*	*	*	*	*	*	*	*	
		77																2CR	*	*	*	*	*	*	*	*	*	*	
		78																											
Truth table tests 6/7/	3014	79	B	A	B	A	A	L	H	H	L	A	A	A	B	A	4.5 V	outputs	5/	5/	5/	5/	5/	5/	5/	5/	5/	5/	
		80	B	A	B	A	B	A	L	H	H	L	A	A	B	A													
		81	A	A	B	A	B	A	L	H	H	L	A	A	B	A													
		82	A	A	B	A	B	A	L	H	H	L	A	A	B	A													
		83	A	A	B	A	B	A	L	H	H	L	A	A	B	A													
		84	A	B	B	B	B	B	L	H	H	L	A	A	B	B													
		85	A	B	B	B	B	B	L	H	H	L	A	A	B	B													
		86	A	B	B	B	B	B	L	H	H	L	A	A	B	B													
		87	A	B	B	B	B	B	L	H	H	L	A	A	B	B													
		88	A	B	B	B	B	B	L	H	H	L	A	A	B	B													
		89	A	B	B	B	B	B	L	H	H	L	A	A	B	B													
		90	A	A	A	A	A	A	L	H	H	L	A	A	B	B													
		91	A	A	A	A	A	A	L	H	H	L	A	A	B	B													

See footnotes at end of table.

TABLE III. Group A inspection for device type 02 - Continued.

Symbol	Case no.	Terminal conditions 1/												Test limits 8/						
		MIL-STD-883 C.D	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Subgroup 9	Subgroup 10	Subgroup 11
method	Test no.	TCCR	10	ICLK	IPRE	10	1Q	GND	2Q	20	2PRE	2CLK	2D	2CCR	VCC	T _C = +25°C	T _C = +125°C	T _C = -55°C		
t _{MAX} 7/ 9/	(Fig. 4)	92	4.5 V	IN	IN	4.5 V	OUT	OUT	GND	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	28	21	28	MHz
		93	4.5 V	IN	IN	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	"	"	"	
		94														20	"	"	"	
		95															"	"	"	
t _{PHL1} 7/ 9/	(Fig. 4)	96	4.5 V	IN	IN	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	31	5	41	5
		97	4.5 V	IN	IN	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	"	"	"	
		98															"	"	"	
		99															"	"	"	
t _{PHL1} 7/ 9/	(Fig. 4)	100	4.5 V	IN	IN	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	"	"	"	
		101															"	"	"	
		102	4.5 V	IN	IN	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	"	"	"	
		103															"	"	"	
t _{PHL2} 7/ 9/	(Fig. 4)	104	IN	GND	GND	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	"	"	"	
		105	4.5 V	GND	GND	4.5 V	IN	OUT	"	OUT	OUT	4.5 V	GND	GND	4.5 V	10 V	"	"	"	
		106															"	"	"	
		107															"	"	"	
t _{PHL2} 7/ 9/	(Fig. 4)	108	IN	GND	GND	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	GND	GND	4.5 V	10 V	"	"	"	
		109	4.5 V	GND	GND	4.5 V	IN	OUT	"	OUT	OUT	4.5 V	GND	GND	4.5 V	10 V	"	"	"	
		110															"	"	"	
		111															"	"	"	
t _{THL} 7/ 9/	(Fig. 4)	112	4.5 V	IN	IN	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	3	15	3	15
		113															"	"	"	
		114	4.5 V	IN	IN	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	"	"	"	
		115															"	"	"	
t _{THL} 7/ 9/	(Fig. 4)	116	4.5 V	IN	IN	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	3	20	3	15
		117															"	"	"	
		118	4.5 V	IN	IN	4.5 V	OUT	OUT	"	OUT	OUT	4.5 V	IN	IN	4.5 V	10 V	"	"	"	
		119															"	"	"	

See footnotes at end of table.

TABLE III. Group A inspection for device type 03.

Symbol	Case	Terminal conditions \underline{I}												Test limits					
		2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Subgroup 1	Subgroup 2	Subgroup 3
MIL-STD-883 method	Cases	1	2	3	4	5	6	7	8	9	10	11	12	13	:4	TC = 25°C	TC = +125°C	TC = -55°C	
Test no.	13	1Q	1Q	1K	2Q	2Q	GND	2J	2CLK	2CTR	2K	1CLK	1CTR	V _{CC}		Min	Max	Min	Max
V _{IC} (pos) 1/	1	1 mA						1/ -						GND	1J	1/ -	1.5		V
	2															2J	2/ -		
	3															2CLK	2/ -		
	4															2CR	2/ -		
	5															1CLK	1/ -		
	6															1CTR	1/ -		
	7																		
	8																		
V _{IC} (neg) 1/	9	-1 mA													1/ -	1J	1/ -	-1.5	
	10															2J	2/ -		
	11															2CLK	2/ -		
	12															2CR	2/ -		
	13															1CLK	1/ -		
	14															1CTR	1/ -		
	15																		
	16																		
I _{CC}	3005	17	6.0 V		6.0 V	GND										6.0 V	6.0 V	6.0 V	6.0 V
	18	6.0 V														6.0 V	6.0 V	6.0 V	6.0 V
V _{OH3}	3006	19	1.2 V	-20 μ A	1.2 V											1.2 V	1.2 V	1.2 V	1.2 V
	20															1.2 V	1.2 V	1.2 V	1.2 V
	21	4.2 V														4.2 V	4.2 V	4.2 V	4.2 V
	22	1.2 V	-20 μ A	-20 μ A	1.2 V	4.2 V										4.2 V	4.2 V	4.2 V	4.2 V
	23															4.2 V	4.2 V	4.2 V	4.2 V
	24																		
V _{OL5}	3006	25-30	Same terminal conditions as specified above for V _{OH3} except I _{OH} = -5.2 mA.													5.48	5.2	5.48	
V _{OL3}	3007	31	1.2 V		2C μ A	1.2 V	20 μ A		GND	1.2 V	10	0.05	0.05	0.05					
	32															20	10	10	10
	33	4.2 V	20 μ A	1.2 V		1.2 V										2/ -	4.2 V	4.2 V	4.2 V
	34	1.2 V			2C μ A	4.2 V										2/ -	4.2 V	4.2 V	4.2 V
	35															20	10	10	10
	36															2/ -	4.2 V	4.2 V	4.2 V
V _{OL5}	3007	37-42	Same terminal conditions as specified above for V _{OL3} except I _{OL} = 5.2 mA.													0.26	0.4	0.26	

See footnotes at end of table.

TABLE III. Group A Inspection for device type 03 - Continued.

Symbol	Case no.	Terminal conditions												Test limits								
		MIL-STD-883 C/D						MIL-STD-883 C/D						Measured terminal		Subgroup 1		Subgroup 2				
		2	3	4	6	8	9	10	12	13	14	16	19	TC = +5°C	TC = +125°C	TC = -55°C	Unit					
OS4	3011	43	4.0 V	GND	GND	4.0 V	GND	GND	4.0 V	GND	4.0 V	4.0 V	10	11	13	14	10	-120	-10	-120	-10	
	44	45	46																			
I _H	3010	47	6.0 V											6.0 V	1V	4.0 V	1V	10	-120	-10	-120	-10
	48	49	50	51	52	53	54															
I _L	3009	55	GND											6.0 V	6.0 V	6.0 V	6.0 V	1V	10	-0.5	0.1	
	56	57	58	59	60	61	62															
I ₁	3012	63	4/ ₁											GND	GND	GND	GND	1V	10	-0.5	0.1	
	64	65	66	67	68	69	70															
Truth table tests	6/ ₁																					

See footnotes at end of table.

TABLE III. Group A inspection for device type 03 - Continued.

Symbol	Case no.	terminal conditions <u>I</u>												Test limits <u>II</u>													
		MIL-STD-883 C,D	2	3	4	6	8	9	10	12	13	14	16	18	19	20	TC = +25°C	TC = +125°C	TC = -55°C	TC = -55°C	Unit						
f _{MAX} 7/ _	(Fig. 4) 91 92 93	90	4.5 V	DUT	OUT	4.5 V		GND					IN	4.5 V	4.5 V	1Q	31	23	31	31	NH ₂						
t _{PHL1} 7/ _	(Fig. 4) 95 96 97	3003	94	IN GND	JUT	OUT	4.5 V	OUT	4.5 V	OUT	4.5 V	IN	IN	4.5 V	4.5 V	10	10	10	10	10							
t _{PHL1} 7/ _	(Fig. 4) 99 100 101	3003	98	IN 4.5 V	JUT	OUT	4.5 V	IN	OUT	4.5 V	OUT	4.5 V	IN	IN	4.5 V	4.5 V	10	10	10	10	10						
t _{PHL2} 7/ _	(Fig. 4) 102 103	3003	102	4.5 V	OUT	GND	OUT					4.5 V	IN	4.5 V	4.5 V	10	10	10	10	10							
t _{PHL2} 7/ _	(Fig. 4) 104 105	3003	104	4.5 V	JUT	GND	OUT					4.5 V	10/	IN	10/	IN	10/	10	10	10	10						
t _{THL} 7/ _	(Fig. 4) 106 107 108 109	3003	106	4.5 V	DUT	OUT	4.5 V	OUT	4.5 V	OUT	4.5 V	GND	IN	4.5 V	4.5 V	10	10	10	10	10							
t _{TLH} 7/ _	(Fig. 4) 110 111 112 113	3003	110	GND	OUT	4.5 V	GND	OUT	4.5 V	OUT	4.5 V	GND	IN	4.5 V	4.5 V	10	10	10	10	10							

See footnotes at end of table.

TABLE III. Group A inspection for device type 04.

Symbol	MIL-STD-883 Cases method E, F	Test no.	Test ICLR	terminal conditions \underline{V}												Test limits					
				Case 2				Case 3				Case 4				Case 5					
				1K	10K	100K	1M	1K	10K	100K	1M	1K	10K	100K	1M	1K	10K	100K	1M		
V_{IC} (pos)	1	1	1mA	1mA	1mA	1mA	1mA	1mA	1mA	1mA	1mA	1mA	1mA	1mA	1mA	GND	1.5	1.5	1.5	1.5	
V_{IC} (neg)	2	1	-1mA	-1mA	-1mA	-1mA	-1mA	-1mA	-1mA	-1mA	-1mA	-1mA	-1mA	-1mA	-1mA	GND	1.5	1.5	1.5	1.5	
V_{CC}	3005	21	GND	6.0V	6.0V	GND	6.0V	GND	6.0V	6.0V	GND	6.0V	6.0V	6.0V	GND	6.0V	6.0V	6.0V	6.0V	6.0V	
V_{OH3}	3006	23	4.2V	1.2V	1.2V	1.2V	1.2V	4.2V	4.2V	1.2V	1.2V	4.2V	4.2V	1.2V	1.2V	4.2V	4.2V	1.2V	1.2V	4.2V	
V_{OL3}	3007	39	1.2V	4.2V	4.2V	4.2V	4.2V	1.2V	1.2V	4.2V	4.2V	1.2V	1.2V	4.2V	4.2V	1.2V	1.2V	4.2V	4.2V	1.2V	1.2V
		40	4.2V	1.2V	1.2V	1.2V	1.2V	4.2V	4.2V	1.2V	1.2V	4.2V	4.2V	1.2V	1.2V	4.2V	4.2V	1.2V	1.2V	4.2V	4.2V
		41																			
		42																			

TABLE III. Group A inspection for device type D4 - Continued.

Symbol	MIL-Case STD-883 Laser Method E, F	Test no.	Terminal conditions \underline{V}												Test limits							
			Subgroup 1				Subgroup 2				Subgroup 3				Measured terminal		TC = +25°C		TC = +125°C		TC = -55°C	
			TC	TC	TC	TC	TC	TC	TC	TC	TC	TC	TC	TC	Min	Max	Min	Max	Min	Max	Min	Max
V _{O,L3}	43 44 45 46	3007	4.2 V 4.2 V 4.2 V 4.2 V	1.2 V 1.2 V 1.2 V 1.2 V	4.2 V 4.2 V 4.2 V 4.2 V	20 μ A 20 μ A 20 μ A 20 μ A	GND GND GND GND	2Q 2Q 2Q 2Q	2PTE 2PTE 2PTE 2PTE	2CLK 2CLK 2CLK 2CLK	2J 2J 2J 2J	2CCR 2CCR 2CCR 2CCR	6.0 V 6.0 V 6.0 V 6.0 V	1.5 1.5 1.5 1.5	0.05 0.05 0.05 0.05	1.05 1.05 1.05 1.05	0.05 0.05 0.05 0.05	V V V V				
V _{O,L5}	47 48 49 50 51 52 53 54 55 56 57 58	3007	4.2 V 4.2 V 4.2 V 4.2 V 4.2 V 4.2 V 4.2 V 4.2 V 4.0 V 4.0 V 4.0 V 4.0 V	1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V GND GND GND GND	4.2 V 4.2 V 4.2 V 4.2 V 4.2 V 4.2 V 4.2 V 4.2 V 4.0 V 4.0 V 4.0 V 4.0 V	20 μ A 20 μ A GND GND GND GND	GND GND GND GND GND GND GND GND GND GND GND GND	5.2 mA 5.2 mA	5.2 mA 5.2 mA	1.2 V 1.2 V	1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 4.2 V 4.2 V 4.2 V 4.2 V	4.2 V 4.2 V	4.2 V 4.2 V	4.2 V 4.2 V	1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 4.2 V 4.2 V 4.2 V 4.2 V	1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 1.2 V 4.2 V 4.2 V 4.2 V 4.2 V	1.26 1.26 1.26 1.26 1.26 1.26 1.26 1.26 1.26 1.26 1.26 1.26	0.4 0.4 0.4 0.4 0.4 0.4 0.4 0.4 0.4 0.4 0.4 0.4				
I _{O,S4}	59 60 61 62 63 64 65 66 67 68	3011	6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V	GND GND GND GND GND GND GND GND GND GND	4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V	10 10 10 10 10 10 10 10 10 10	-10 -10 -10 -10 -10 -10 -10 -10 -10 -10	-120 -120 -120 -120 -120 -120 -120 -120 -120 -120	-10 -10 -10 -10 -10 -10 -10 -10 -10 -10	-120 -120 -120 -120 -120 -120 -120 -120 -120 -120	mA mA mA mA mA mA mA mA mA mA					
I _{H,H}	69 70 71 72 73 74 75 76 77 78	3010	6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V	GND GND GND GND GND GND GND GND GND GND	6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V 6.0 V	10 10 10 10 10 10 10 10 10 10	10 10 10 10 10 10 10 10 10 10	ICTR ICTR ICTR ICTR ICTR ICTR ICTR ICTR ICTR ICTR	0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00	mA mA mA mA mA mA mA mA mA mA						
I _{H,L}	79 80 81 82 83 84 85 86 87 88	3009	4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V	GND GND GND GND GND GND GND GND GND GND	4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	10 10 10 10 10 10 10 10 10 10	50 50 50 50 50 50 50 50 50 50	-0.00 -0.00 -0.00 -0.00 -0.00 -0.00 -0.00 -0.00 -0.00 -0.00	pF pF pF pF pF pF pF pF pF pF							
C ₁	89 90 91 92 93 94 95 96 97 98	3012	4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V	GND GND GND GND GND GND GND GND GND GND	4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V 4.0 V	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	GND GND GND GND GND GND GND GND GND GND	10 10 10 10 10 10 10 10 10 10	10 10 10 10 10 10 10 10 10 10	ICTR ICTR ICTR ICTR ICTR ICTR ICTR ICTR ICTR ICTR	0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00	pF pF pF pF pF pF pF pF pF pF							

See footnotes at end of table.

TABLE III. Group A inspection for device type 0x - Continued.

Symbol	MIL- STD- 803 Cases method E, F	Case no.	Terminal conditions 1/																Test limits				
			Measured terminal								Measured Subgroup 7 TC = +25°C								Subgroup 8 TC = +125°C TC = -55°C				
			Test	ICR	X	ICLK	1REF	10	1Q	2Q	2PRE	2CLK	2K	2TR	VCC	Min	Max	Min	Max	All outputs	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}
Truth table tests	6/ 7/	3014	B	A	A	A	A	L	H	H	L	A	A	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		90	B	A	A	A	A	L	H	H	L	A	A	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		91	B	A	A	A	A	L	H	H	L	A	A	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		92	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		93	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		94	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		95	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		96	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		97	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		98	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		99	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		100	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		101	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		102	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		103	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		104	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		105	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		106	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		107	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		108	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		109	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		110	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		111	A	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
		112	B	A	A	A	A	B	B	B	B	B	B	B	4.5 V	Min	Max	Min	Max	5/ ^{ns}	5/ ^{ns}	5/ ^{ns}	
Test limits 2/																							
Subgroup 9 3/ Subgroup 10 4/ Subgroup 11 5/																							
TC = +25°C TC = +125°C TC = -55°C																							
f _{MAX} 17/ 9/	113	4.5 V	4.5 V	GND	IN	4.5 V	OUT	GND	OUT	4.5 V	IN	GND	4.5 V	IN	4.5 V	IN	4.5 V	IN	10	31	23	31	NHz
46	114	4.5 V	4.5 V	GND	IN	4.5 V	OUT	GND	OUT	4.5 V	IN	GND	4.5 V	IN	4.5 V	IN	4.5 V	IN	20	*	*	*	*
tPHL1 7/	3003	117	4.5 V	1V	GND	IN	4.5 V	OUT	OUT	4.5 V	IN	GND	4.5 V	IN	4.5 V	IN	4.5 V	IN	5	31	5	31	ns
	118	4.5 V	4.5 V	IN	IN	4.5 V	OUT	OUT	OUT	4.5 V	IN	IN	4.5 V	IN	4.5 V	IN	4.5 V	IN	20	*	*	*	*
	119	119	120																				

See footnotes at end of table.

TABLE III. Group A inspection for device type 04 - Continued.

Symbol	MIL-STD-883 Cases method E, F	terminal conditions $\underline{I_7}$												Test limits \underline{g}							
		Subgroup 11 Unit				Subgroup 101 Unit				Subgroup 9				Subgroup 10				Measured terminal			
		TC = +25°C	TC = +125°C	TC = +25°C	TC = -55°C	TC = +25°C	TC = +125°C	TC = +25°C	TC = -55°C	TC = +25°C	TC = +125°C	TC = +25°C	TC = -55°C	TC = +25°C	TC = +125°C	TC = +25°C	TC = -55°C	Min	Max		
Test no.	1CCR	1J	1K	1CLK	1PRE	1Q	1T	GND	2Q	2PRE	2CLK	2J	2CCR	2CC	4.5 V	ICLK to 10	5	31	5	31	
tpLH1	3003 (Fig. 4)	121	4.5 V	IN	4.5 V	OUT	OUT	GND	"	"	"	"	GND	4.5 V	4.5 V	4.5 V	ICLK to 10	5	31	5	31
<u>7/</u>	122	4.5 V	GND	IN	4.5 V	"	"	"	"	"	"	"	GND	4.5 V	4.5 V	4.5 V	ICLK to 10	5	31	5	31
tpLH2	3003 (Fig. 4)	125	1N	4.5 V	GND	GND	4.5 V	IN	OUT	OUT	OUT	OUT	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41
<u>7/</u>	126	4.5 V	4.5 V	GND	GND	GND	GND	"	"	"	"	"	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41
tpLH2	3003 (Fig. 4)	129	4.5 V	4.5 V	GND	GND	4.5 V	IN	OUT	OUT	OUT	OUT	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41
<u>7/</u>	130	4.5 V	4.5 V	GND	GND	GND	GND	"	"	"	"	"	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41
tpLH	3004 (Fig. 4)	133	4.5 V	4.5 V	GND	GND	4.5 V	IN	4.5 V	OUT	OUT	OUT	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41
<u>7/</u>	134	4.5 V	4.5 V	GND	GND	GND	GND	"	"	"	"	"	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41
tpLH	3004 (Fig. 4)	137	4.5 V	IN	4.5 V	OUT	OUT	"	"	"	"	"	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41
<u>7/</u>	138	4.5 V	GND	IN	4.5 V	OUT	OUT	"	"	"	"	"	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41
tpLH	3004 (Fig. 4)	139	4.5 V	IN	4.5 V	IN	4.5 V	OUT	OUT	OUT	OUT	OUT	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41
<u>7/</u>	140	"	"	"	"	"	"	"	"	"	"	"	GND	4.5 V	4.5 V	4.5 V	ICLR to 10	6	41	6	41

See footnotes at end of table.

TABLE III Group A inspection for device type 05.

Symbol	MIL-STD-853 Case Cases	Test method E, F	Terminal conditions $\frac{V}{I}$												Test limits							
			2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured Subgroup 1 terminal TC = +25°C	Subgroup 2 TC = +125°C	Subgroup 3 TC = -55°C	Unit
V _{IC} (pos) $\frac{V}{I}$	1	Test no.	1CLK	1K	1J	1PTE	1Q	1T	2T	GND	20	2PTE	2Q	2K	2CLK	ICR	VCC	GND	1CLK	1/	1.5	
	2		1 mA	1 mA	1 mA	1 mA													1K	"	"	
	3		4	3	4	5	6	7	8	9	10	11	12	13	14	15	16		1J	"	"	
	4		5	5															IPRE	"	"	
	6		7	7															2PTE	"	"	
	8		9	8															2J	"	"	
	9		10	9															2K	"	"	
	10																		2CLK	"	"	
																			2CLR	"	"	
																			ICR	"	"	
V _{IC} (neg) $\frac{V}{I}$	11	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA		1/	1CLK	-1.5	
	12																		1K	"	"	
	13																		1J	"	"	
	14																		IPRE	"	"	
	15																		2PTE	"	"	
	16																		2J	"	"	
	17																		2K	"	"	
	18																		2CLK	"	"	
	19																		2CLR	"	"	
	20																		ICR	"	"	
I _{CC}	21	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	GND	6.0 V	6.0 V	6.0 V	
	22	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V		V _{CC}	0.15	15	mA
V _{OH3}	23	1.2 V	1.2 V	4.2 V	4.2 V	4.2 V	4.2 V	4.2 V	4.2 V	-20 uA	-20 uA	-20 uA		4.2 V	"	"						
	24	1.2 V	1.2 V	4.2 V	4.2 V	4.2 V	4.2 V	4.2 V	4.2 V	-20 uA	-20 uA	-20 uA		4.2 V	"	"						
	25																		1.2 V	"	"	
	26																		20	"	"	
	27																		1.2 V	"	"	
	28																		1.2 V	"	"	
	29																		20	"	"	
	30																		20	"	"	
V _{OHS}	31	1.2 V	1.2 V	4.2 V	4.2 V	4.2 V	4.2 V	4.2 V	4.2 V	-5.2 mA	-5.2 mA	-5.2 mA		4.2 V	"	"						
	32	1.2 V	1.2 V	4.2 V	4.2 V	4.2 V	4.2 V	4.2 V	4.2 V	-5.2 mA	-5.2 mA	-5.2 mA		1.2 V	"	"						
	33																		20	"	"	
	34																		20	"	"	

See footnotes at end of table.

TABLE III. Group A inspection for device type 05 - Continued.

Symbol	MIL-STD-883 Cases method F	Case no.	Terminal conditions 1/												Test limits							
			Subgroup 1						Subgroup 2						Subgroup 3							
			TC = +25°C			TC = +125°C			TC = -55°C			TC = +25°C			TC = +125°C			TC = -55°C				
			Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max		
Y0H5	3006	35	2/	1.2 V	4.2 V	4.2 V	-5.2 mA	-5.2 mA	GND	"	-5.2 mA	4.2 V	4.2 V	2/	4.2 V	4.2 V	10	5.48	5.2	5.48		
	36	37	Z/	4.2 V	1.2 V	4.2 V	"	"	"	"	20	4.2 V	4.2 V	2/	4.2 V	4.2 V	20	"	"	"		
	38						-5.2 mA	"														
Y0L3	3007	39	1.2 V	4.2 V	1.2 V	1.2 V	20 μ A	20 μ A	"	"	20 μ A	4.2 V	4.2 V	1.2 V	1.2 V	1.2 V	10	0.05	0.05	0.05		
	40	41	1.2 V	4.2 V	1.2 V	1.2 V	20 μ A	20 μ A	"	"	20 μ A	1.2 V	4.2 V	1.2 V	1.2 V	4.2 V	20	"	"	"	"	
	42	43	Z/	4.2 V	1.2 V	4.2 V	20 μ A	20 μ A	"	"	"											
	44	45	Z/	1.2 V	4.2 V	4.2 V	20 μ A	20 μ A	"	"	20 μ A	4.2 V	4.2 V	1.2 V	1.2 V	4.2 V	10	"	"	"	"	
	46																					
Y0L5	3007	47	1.2 V	4.2 V	1.2 V	4.2 V	5.2 mA	5.2 mA	"	"	"	5.2 mA	4.2 V	1.2 V	4.2 V	1.2 V	4.2 V	10	0.26	0.4	0.26	
	48	49	1.2 V	4.2 V	1.2 V	1.2 V	5.2 mA	5.2 mA	"	"	"	5.2 mA	1.2 V	4.2 V	1.2 V	4.2 V	4.2 V	20	"	"	"	
	50	51	Z/	4.2 V	1.2 V	4.2 V	4.2 V	5.2 mA	5.2 mA	"	"	"	5.2 mA	1.2 V	4.2 V	1.2 V	4.2 V	4.2 V	10	"	"	"
	52	53	Z/	1.2 V	4.2 V	4.2 V	4.2 V	5.2 mA	5.2 mA	"	"	"	5.2 mA	4.2 V	1.2 V	4.2 V	1.2 V	4.2 V	20	"	"	"
	54																					
I0S4	3011	55	GND	GND	3ND	GND	4.0 V	GND	GND	GND	GND	GND	GND	GND	4.0 V	GND	10	-10	-120	-10	-120 mA	
	56	57	GND	GND	3ND	GND	"	GND	GND	GND	GND	GND	GND	GND	4.0 V	GND	10	10	120	10	120 mA	
	58																					
ITI	3010	59	6.0 V	6.0 V	6.0 V	6.0 V	"	"	"	"	"	"	"	"	6.0 V	ICL1	50	100	100 nA	"	"	
	60	61	"	"	"	"	"	"	"	"	"	"	"	"	6.0 V	ICL1	1K	"	"	"	"	
	62	63	"	"	"	"	"	"	"	"	"	"	"	"	6.0 V	1PRT	"	"	"	"	"	
	64	65	"	"	"	"	"	"	"	"	"	"	"	"	6.0 V	2PRT	"	"	"	"	"	
	66	67	"	"	"	"	"	"	"	"	"	"	"	"	6.0 V	2CL	"	"	"	"	"	
	68														6.0 V	1CTC	"	"	"	"	"	

See footnotes at end of table.

TABLE III. Group A inspection for device type 05 - Continued.

Symbol	MIL-STD-883C Test no.	Case no.	Terminal conditions V_T												Test limits											
			12	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal value	Subgroup 1 $T_C = +25^\circ C$	Subgroup 2 $T_C = +125^\circ C$	Subgroup 3 $T_C = -55^\circ C$	Unit		
I1L	3009	65	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	nA	*	*	*	*	*		
I1L	71	7C	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	6.0 V	1CLK	-50	-100	nA	*		
I1L	72	7J	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	10K	1J	1PRE	2PRE	2J	*		
I1L	74	74	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	2J	2K	2CLK	2CTR	2K	*		
I1L	75	75	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1CTR	1J	1PRE	2PRE	2J	*		
I1L	76	76	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	10K	1J	1PRE	2PRE	2J	*		
I1L	77	77	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	2K	2CLK	2CTR	2J	2K	*		
I1L	78	78	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	1CTR	1J	1PRE	2PRE	2J	*		
C1	3012	75	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	6.0 V	1CLK	10	nA	*	*		
C1	80	80	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	10K	1J	1PRE	2PRE	2J	*		
C1	82	82	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	2J	2K	2CLK	2CTR	2K	*		
C1	84	84	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	1CTR	1J	1PRE	2PRE	2J	*		
C1	86	86	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	10K	1J	1PRE	2PRE	2J	*		
C1	87	87	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	2J	2K	2CLK	2CTR	2K	*		
C1	88	88	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	4/ 4/	1CTR	1J	1PRE	2PRE	2J	*		
Subgroup 4 $T_C = +25^\circ C$																										
Subgroup 7 $T_C = +125^\circ C$																										
Subgroup 8 $T_C = -55^\circ C$																										
Truth table tests 6/7/ _/_	85	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	E	6.0 V	All outputs	5/ E	5/ E	5/ E	5/ E	5/ E	
	90	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	E	E	A	A	A	A	A	A	
	92	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	93	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	94	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	96	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	97	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	98	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	99	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	100	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	101	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	102	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	103	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	104	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	105	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	106	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	107	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	108	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	109	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	110	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	
	111	A	B	A	A	A	A	A	B	A	B	B	B	B	B	B	B	H	H	A	A	A	A	A	A	

TABLE III. Group A inspection for device type 05 - Continued.

See footnotes at end of table.

TABLE III. Group A inspection for device type 06.

Symbol	MIL-STD-883 Cases Method F	Test No.	Terminal conditions $\frac{V}{I}$												Test limits					
			Subgroup 1						Subgroup 2			Subgroup 3			Init					
			$T_C = +25^\circ\text{C}$	$T_C = +125^\circ\text{C}$	$T_C = -55^\circ\text{C}$	terminal	$T_C = +25^\circ\text{C}$	$T_C = +125^\circ\text{C}$	$T_C = -55^\circ\text{C}$	terminal	$T_C = +25^\circ\text{C}$	$T_C = +125^\circ\text{C}$	$T_C = -55^\circ\text{C}$	terminal	$T_C = +25^\circ\text{C}$	$T_C = +125^\circ\text{C}$	$T_C = -55^\circ\text{C}$	terminal		
V_{IC} (pos)	1 2 3 4 5 6 7 8 9 10	OE1 OE2	1 mA	1 mA	1 mA	GND	E_1	E_2	40	30	20	10	CLR	V_{CC}	Min	Max	Min	Max		
V_{IC} (neg)	11 12 13 14 15 16 17 18 19 20		-1 mA	-1 mA	-1 mA	GND							GND	OE1	1V	1.5V	1V	1.5V		
I_{CC}	21 22 23	IND GND GND	5.0 V	5.0 V	5.0 V	GND	GND	GND	6.0 V	6.0 V	6.0 V	6.0 V	GND	V_{CC}	0.2	20 uA	0.2	20 uA		
V_{DH3}	24 25 26 27	1.2 V 1.2 V 1.2 V 1.2 V	-20 uA	-20 uA	-20 uA	2V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	GND	OE2	-	-	OE2	-		
V_{DH5}	28 29 30 31	" " " "	-7.8 mA	-7.8 mA	-7.8 mA	"	"	"	1.2 V	1.2 V	1.2 V	1.2 V	GND	OE1	-	-	OE1	-		
V_{U3}	32 33 34 35	" " " "	20 uA	20 uA	20 uA	20 uA	20 uA	20 uA	1.2 V	1.2 V	1.2 V	1.2 V	GND	V_{CC}	5.95	5.95	5.95	5.95		

See 5.5 notes at end of table.

TABLE III. Group A inspection for device type 06 - Continued.

MIL-Symbol	Case no.	Test no.	Terminal conditions V_T												Test limits							
			Subgroup 1						Subgroup 2						Subgroup 3							
			TC = +25°C			TC = +125°C			TC = -55°C			TC = +25°C			TC = +125°C			TC = -55°C				
MIL-Symbol	Case no.	Test no.	E ₁	E ₂	E ₃	E ₄	E ₅	E ₆	E ₇	E ₈	E ₉	E ₁₀	E ₁₁	E ₁₂	E ₁₃	E ₁₄	E ₁₅	E ₁₆	E ₁₇	E ₁₈		
S10-883	2	36	1.2 V	1.2 V	20 μ A	20 μ A	20 μ A	20 μ A	20 μ A	20 μ A	20 μ A	20 μ A	20 μ A									
Method E, F	1	37	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	
Test no.	38	38	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	
V _{OL3}	3007	39	0E2	1Q	2Q	3Q	4Q	CLK	GND	E ₁	E ₂	4D	30	2D	1D	C/LR	V _{CC}	N	M	N	M	
V _{OL5}	3007	40	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.05	0.05	0.05	V
T _{OS}	3011	41	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	42	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	20	0.26	0.4	0.26	"
	43	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	30	0.26	0.4	0.26	"
	44	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	40	0.26	0.4	0.26	"
	45	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	46	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	20	0.26	0.4	0.26	"
	47	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	30	0.26	0.4	0.26	"
T _{OZL}	3011	48	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	4.0 V	4.0 V	4.0 V	4.0 V	GND
	49	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	50	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	20	0.26	0.4	0.26	"
	51	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	30	0.26	0.4	0.26	"
T _{OZH}	3011	52	1.2 V	1.2 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	1.2 V	6.0 V	1.2 V	6.0 V	GND							
	53	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	54	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	20	0.26	0.4	0.26	"
	55	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	30	0.26	0.4	0.26	"
	56	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	40	0.26	0.4	0.26	"
	57	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	58	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	20	0.26	0.4	0.26	"
	59	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	30	0.26	0.4	0.26	"
T _{TH}	3010	60	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	1.2 V	6.0 V	1.2 V	6.0 V	GND
	61	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	40	0.26	0.4	0.26	"
	62	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	50	0.26	0.4	0.26	"
	63	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	100	0.26	0.4	0.26	"
	64	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	100	0.26	0.4	0.26	"
	65	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	100	0.26	0.4	0.26	"
	66	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	100	0.26	0.4	0.26	"
	67	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	100	0.26	0.4	0.26	"
	68	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	100	0.26	0.4	0.26	"
	69	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	100	0.26	0.4	0.26	"
I _{L1}	3039	70	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	0E1	0E2	0E3	0F	Subgroup 4
	71	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	72	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	73	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	74	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	75	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	76	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	77	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	78	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	79	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	80	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	81	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	82	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	83	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	84	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	85	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	86	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	87	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	88	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"
	89	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	10	0.26	0.4	0.26	"

See footnotes at end of table.

TABLE III. Group A inspection for device type 06 - Continued.

CONCENTRATES AT END OF TABLE

TABLE III. Group A Inspection for device type 06 - Continued.

Symbol	MLT- 883 method, no.	Case	terminal conditions 17												Test limits					
			2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured time final	Subgroup 9 TC = 25°C
t _{PH2}	3003 (Fig. 4)	139 140 141 142 143 144 145 146	IN GND IN GND IN GND IN GND	OUT OUT OUT OUT OUT OUT OUT OUT	10/ CLK GND GND GND GND GND GND	E ₁ GND GND GND GND GND GND GND	E ₂ GND GND GND GND GND GND GND GND	0 30 40 0 0 0 0 0	10 0 0 0 0 0 0 0	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	GND 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	10 20 30 40 10 20 30 40	5 20 30 40 10 20 30 40	35 35 35 35 35 35 35 35	5 20 30 40 10 20 30 40	26 ns ns ns ns ns ns ns			
t _{DL2}	3003 (Fig. 4)	147 148 149 150 151 152 153 154	IN GND IN GND IN GND IN GND	OUT OUT OUT OUT OUT OUT OUT OUT	10/ CLK GND GND GND GND GND GND	E ₁ GND GND GND GND GND GND GND	E ₂ GND GND GND GND GND GND GND GND	0 30 40 0 0 0 0 0	10 0 0 0 0 0 0 0	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	GND 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	10 20 30 40 10 20 30 40	35 35 35 35 35 35 35 35	26 ns ns ns ns ns ns ns					
t _{DH}	3003 (Fig. 4)	155 156 157 158 159 160 161 162	IN GND IN GND IN GND IN GND	OUT OUT OUT OUT OUT OUT OUT OUT	10/ CLK GND GND GND GND GND GND GND	E ₁ GND GND GND GND GND GND GND	E ₂ GND GND GND GND GND GND GND GND	0 30 40 0 0 0 0 0	10 0 0 0 0 0 0 0	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	GND 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	10 20 30 40 10 20 30 40	35 35 35 35 35 35 35 35	26 ns ns ns ns ns ns ns					
t _{DL}	3003 (Fig. 4)	163 164 165 166 167 168 169 170	IN GND IN GND IN GND IN GND	OUT OUT OUT OUT OUT OUT OUT OUT	10/ CLK GND GND GND GND GND GND GND	E ₁ GND GND GND GND GND GND GND	E ₂ GND GND GND GND GND GND GND GND	0 30 40 0 0 0 0 0	10 0 0 0 0 0 0 0	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	GND 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	10 20 30 40 10 20 30 40	35 35 35 35 35 35 35 35	26 ns ns ns ns ns ns ns					
t _{THL}	3004 (Fig. 4)	171 172 173 174 175 176 177 178	IN GND IN GND IN GND IN GND	OUT OUT OUT OUT OUT OUT OUT OUT	10/ CLK GND GND GND GND GND GND GND	E ₁ GND GND GND GND GND GND GND	E ₂ GND GND GND GND GND GND GND GND	0 30 40 0 0 0 0 0	10 0 0 0 0 0 0 0	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	GND 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V 4.5 V	10 20 30 40 10 20 30 40	35 35 35 35 35 35 35 35	26 ns ns ns ns ns ns ns					
t _{TLH}	3004 (Fig. 4)	175 176 177 178	IN GND IN GND	OUT OUT OUT OUT	10/ CLK GND GND GND	E ₁ GND GND GND	E ₂ GND GND GND	0 30 40 0	10 0 0 0	4.5 V 4.5 V 4.5 V 4.5 V	GND 4.5 V 4.5 V 4.5 V 4.5 V	4.5 V 4.5 V 4.5 V 4.5 V	10 20 30 40	35 35 35 35	26 ns ns ns ns					

See footnotes at end of table.

TABLE III. Group A inspection for device type 07.

Symbol	MIL-STD-883 Testes method E, F	Terminal conditions $\frac{V}{I}$												Test limits					
		Subgroup 1						Subgroup 2						Subgroup 3					
		TC = +25°C			TC = +125°C			TC = -55°C			TC = +25°C			TC = +125°C			TC = -55°C		
MIL-STD-883 Test no.	Test	CLR	10	10	20	20	30	30	40	40	50	50	60	60	60	60	60	60	60
V_{IC} (pos)	1	1 mA																	
	2		1 mA																
	3			1 mA															
	4				1 mA														
	5					1 mA													
	6						1 mA												
	7							1 mA											
	8								1 mA										
V_{IC} (neg)	9	-1 mA																	
	10		-1 mA																
	11			-1 mA															
	12				-1 mA														
	13					1 mA													
	14						1 mA												
	15							1 mA											
	15								1 mA										
V_{CC}	17	GLD	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V
	18	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V
I_{DL13}	2006	10	4.2 V	-20 uA	4.2 V	1.2 V	-20 uA	4.2 V	-20 uA	4.2 V	-20 uA	4.2 V	-20 uA	4.2 V	-20 uA	4.2 V	-20 uA	4.2 V	-20 uA
	20																		
	21																		
	22																		
	23																		
	24																		
V_{DL15}	1006	25	*	-5.2 mA	4.2 V	4.2 V	-5.2 mA	4.2 V	-5.2 mA	4.2 V	-5.2 mA	4.2 V	-5.2 mA	4.2 V	-5.2 mA	4.2 V	-5.2 mA	4.2 V	-5.2 mA
	26	*																	
	27	*																	
	28	*																	
	29	*																	
	30	*																	
V_{OL3}	3037	31	1.2 V	20 uA															
	32																		
	33																		
	34																		
	35																		
	36																		
	37																		
	38																		
	39																		
	40																		
	41																		
	42																		

See footnotes at end of table.

TABLE III. Group A inspection for device type 07 - Continued.

Symbol	MIL-STD-883C Case Method IC, F	Terminal conditions 1Y												Test limits						
		Measured terminal						Subgroup 1			Subgroup 2			Subgroup 3			Unit			
		Test No.	CLR	10	10	20	20	30	30	GND	CLK	40	40	50	50	60	V _{CC}	Hn	Max	Min
YOL5	3007	43	1.2 V	5.2 mA		5.2 mA		5.2 mA		GND						6.0 V	10	0.26	0.4	0.261 V
	44	-	-	-	-	-	-	-	-	5.2 mA						6.0 V	20	-	-	-
	45	-	-	-	-	-	-	-	-	5.2 mA						6.0 V	30	-	-	-
	46	-	-	-	-	-	-	-	-	5.2 mA						6.0 V	40	-	-	-
	47	-	-	-	-	-	-	-	-	5.2 mA						6.0 V	50	-	-	-
	48	-	-	-	-	-	-	-	-	5.2 mA						6.0 V	60	-	-	-
	49	4.2 V	5.2 mA	1.2 V	5.2 mA	4.2 V	5.2 mA	4.2 V	5.2 mA	2/						5.2 mA	10	-	-	-
	50	-	-	-	-	-	-	-	-	5.2 mA						5.2 mA	20	-	-	-
	51	-	-	-	-	-	-	-	-	5.2 mA						5.2 mA	30	-	-	-
	52	-	-	-	-	-	-	-	-	5.2 mA						5.2 mA	40	-	-	-
	53	-	-	-	-	-	-	-	-	5.2 mA						5.2 mA	50	-	-	-
	54	-	-	-	-	-	-	-	-	5.2 mA						5.2 mA	60	-	-	-
J054	3011	55	4.0 V	GND	4.0 V	4.0 V	GND	4.0 V	GND						4.0 V	10	10	120	-10	-120 mA
	56	-	-	-	-	-	-	-	-	GND						4.0 V	20	-	-	-
	57	-	-	-	-	-	-	-	-	GND						4.0 V	30	-	-	-
	58	-	-	-	-	-	-	-	-	GND						4.0 V	40	-	-	-
	59	-	-	-	-	-	-	-	-	GND						4.0 V	50	-	-	-
	60	-	-	-	-	-	-	-	-	GND						4.0 V	60	-	-	-
I1H	3010	61	6.0 V		6.0 V										6.0 V	10	CLK	50	100 mA	
	62	-	-	-	-	-	-	-	-						6.0 V	20	-	-	-	
	63	-	-	-	-	-	-	-	-						6.0 V	30	-	-	-	
	64	-	-	-	-	-	-	-	-						6.0 V	40	-	-	-	
	65	-	-	-	-	-	-	-	-						6.0 V	50	-	-	-	
	66	-	-	-	-	-	-	-	-						6.0 V	60	-	-	-	
	67	-	-	-	-	-	-	-	-						6.0 V					
	68	-	-	-	-	-	-	-	-						6.0 V					
I1L	3009	69	GND		GND										GND	10	CLK	50	100 mA	
	70	-	-	-	-	-	-	-	-						GND	20	-	-	-	
	71	-	-	-	-	-	-	-	-						GND	30	-	-	-	
	72	-	-	-	-	-	-	-	-						GND	40	-	-	-	
	73	-	-	-	-	-	-	-	-						GND	50	-	-	-	
	74	-	-	-	-	-	-	-	-						GND	60	-	-	-	
	75	-	-	-	-	-	-	-	-						GND					
	76	-	-	-	-	-	-	-	-						GND					
C1	5012	77	4/		4/					GND					GND	10	AT1 outputs	5/	PF	
	78	-	-	-	-	-	-	-	-						GND	20	-	-	-	
	79	-	-	-	-	-	-	-	-						GND	30	-	-	-	
	80	-	-	-	-	-	-	-	-						GND	40	-	-	-	
	81	-	-	-	-	-	-	-	-						GND	50	-	-	-	
	82	-	-	-	-	-	-	-	-						GND	60	-	-	-	
	83	-	-	-	-	-	-	-	-						GND					
	84	-	-	-	-	-	-	-	-						GND					

See comments at end of table.

TABLE III. Group A inspection for device type 07 - Continued

Symbol	MIL-STD-883 Test no.	Test method E, F	Test no.	Terminal conditions <i>T</i>												Test limits <i>8/</i>							
				1	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Subgroup 10 ISU	Subgroup 11 Junit
<i>t_{MAX}</i> <i>T</i>	3003 (Fig. 4)	91	4.5 V	OUT	IN	IN	OUT	IN	OUT	IN	GND	IN	CLK	4Q	4D	5Q	5D	6Q	6D	V _{CC}	4.5 V	10	31
<i>t_{PHL1}</i> <i>T</i>	3003 (Fig. 4)	97	*	OUT	IN	IN	OUT	IN	OUT	IN	*	*	*	*	*	*	*	*	*	*	20	36	31
<i>t_{PLH1}</i> <i>T</i>	3003 (Fig. 4)	98	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	50	50	51
<i>t_{HLR2}</i> <i>T</i>	3004 (Fig. 4)	103	*	OUT	IN	IN	OUT	IN	OUT	IN	*	*	*	*	*	*	*	*	*	*	10	31	31
<i>t_{HLH}</i> <i>T</i>	3004 (Fig. 4)	110	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	20	30	31
<i>t_{HLH}</i> <i>T</i>	3004 (Fig. 4)	111	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	30	40	41
<i>t_{HLH}</i> <i>T</i>	3004 (Fig. 4)	112	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	40	50	51
<i>t_{HLH}</i> <i>T</i>	3004 (Fig. 4)	113	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	50	60	61
<i>t_{HLH}</i> <i>T</i>	3004 (Fig. 4)	114	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	60	60	61

See footnotes at end of table.

TABLE III. Group A inspection for device type 08.

See footnotes at end of table.

TABLE III. Group A inspection for device type 08 - Continued.

Symbol	MIL-Case STD- 883 Cases Method E, F	Terminal conditions <u>T₁</u>												Test limits													
		Measured terminal						Subgroup 1 TC = +25°C						Subgroup 2 TC = +125°C TC = -55°C						Subgroup 3 Unit							
		no.	Test	U _{TR}	I _Q	I _U	I _D	GND	CLK	3C	JT	30	40	4Q	V _{CC}	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max		
Y ₀₁ 3	3007	39	1.2 V	20 μ A	-	-	-	20 μ A	-	-	-	-	-	-	6.0 V	10	0.05	0.05	0.05	0.05	0.05	0.05	0.05	0.05	0.05		
		40	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		41	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		42	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		43	4.2 V	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		44	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		45	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		46	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		47	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		48	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		49	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
		50	-	-	-	-	-	-	-	-	-	-	-	-	20 μ A	20	40	40	40	40	40	40	40	40	40		
Y ₀₁ 5	3007	51	1.2 V	5.2 mA	-	-	-	5.2 mA	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		52	-	-	-	-	-	-	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		53	-	-	-	-	-	-	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		54	4.2 V	-	-	-	-	5.2 mA	4.2 V	5.2 mA	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		55	-	-	-	-	-	5.2 mA	4.2 V	5.2 mA	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		56	-	-	-	-	-	-	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		57	-	-	-	-	-	-	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		58	-	-	-	-	-	-	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		59	-	-	-	-	-	-	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		60	-	-	-	-	-	-	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		61	-	-	-	-	-	-	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
		62	-	-	-	-	-	-	-	-	-	-	-	-	5.2 mA	10	20	20	20	20	20	20	20	20	20		
I ₀₃ 4	3011	63	4.0 V	GND	4.0 V	GND	4.0 V	GND	GND	GND	GND	GND	GND	GND	4.0 V	10	20	20	20	20	20	20	20	20	20		
		64	-	-	-	-	-	-	-	-	-	-	-	-	4.0 V	10	20	20	20	20	20	20	20	20	20		
		65	-	-	-	-	-	-	-	-	-	-	-	-	4.0 V	10	20	20	20	20	20	20	20	20	20		
		66	-	-	-	-	-	-	-	-	-	-	-	-	4.0 V	10	20	20	20	20	20	20	20	20	20		
		67	-	-	-	-	-	-	-	-	-	-	-	-	4.0 V	10	20	20	20	20	20	20	20	20	20		
		68	-	-	-	-	-	-	-	-	-	-	-	-	4.0 V	10	20	20	20	20	20	20	20	20	20		
		69	-	-	-	-	-	-	-	-	-	-	-	-	4.0 V	10	20	20	20	20	20	20	20	20	20		
		70	-	-	-	-	-	-	-	-	-	-	-	-	4.0 V	10	20	20	20	20	20	20	20	20	20		
I ₁₁ H	3010	71	6.0 V	-	-	-	-	-	-	-	-	-	-	-	6.0 V	10	20	20	20	20	20	20	20	20	20		
		72	-	-	-	-	-	-	-	-	-	-	-	-	6.0 V	10	20	20	20	20	20	20	20	20	20		
		73	-	-	-	-	-	-	-	-	-	-	-	-	6.0 V	10	20	20	20	20	20	20	20	20	20		
		74	-	-	-	-	-	-	-	-	-	-	-	-	6.0 V	10	20	20	20	20	20	20	20	20	20		
		75	-	-	-	-	-	-	-	-	-	-	-	-	6.0 V	10	20	20	20	20	20	20	20	20	20		
		76	-	-	-	-	-	-	-	-	-	-	-	-	6.0 V	10	20	20	20	20	20	20	20	20	20		
I ₁₁ L	3009	77	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	10	20	20	20	20	20	20	20	20	20	
		78	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20	
		79	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20	
		80	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20	
		81	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20	
		82	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20	
C ₁	3011	83	4/ ₁	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20	
		84	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20
		85	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20
		86	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20
		87	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20
		88	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	10	20	20	20	20	20	20	20	20	20

See footnotes at end of table.

TABLE III. Group A inspection for device type 08 - Continued.

Symbol	MIL-Case	STD-883 Class	Test no.	Terminal conditions 1/												Test limits									
				2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20	Measured terminal	Subgroup 7	Subgroup 8	Subgroup 8	Unit	
Method F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	TC = +25°C	TC = +125°C	TC = -55°C		
Truth tests	67	7/	91	A	H	L	A	H	L	A	H	L	A	H	L	A	H	L	A	A	A	A			
Table	3014	89	B	L	H	A	A	A	A	GND	B	L	H	A	A	A	H	L	A	A	A	A			
Table	90	B	L	H	A	A	A	A	A	L	H	A	A	A	A	A	H	L	A	A	A	A			
Table	91	A	L	H	C	A	A	A	A	L	H	A	A	A	A	A	H	L	A	A	A	A			
Table	91	A	H	L	A	A	A	A	A	L	H	A	A	A	A	A	H	L	A	A	A	A			
Table	95	B	L	H	A	A	A	A	A	L	H	A	A	A	A	A	H	L	A	A	A	A			
Table	96	A	L	H	A	A	A	A	A	L	H	A	A	A	A	A	H	L	A	A	A	A			
Table	97	A	H	L	A	A	A	A	A	L	H	A	A	A	A	A	H	L	A	A	A	A			
Table	98	A	L	H	A	A	A	A	A	L	H	A	A	A	A	A	H	L	A	A	A	A			
Table	99	A	H	L	A	A	A	A	A	L	H	A	A	A	A	A	H	L	A	A	A	A			
Table	100	A	L	H	B	B	B	B	B	L	H	A	A	A	A	A	H	L	B	B	B	B			
Test limits 1/																									
Test limits 2/																									
Test limits 3/																									
f _{MAX}	(F19, 4)	10	4.5 V	OUT	IN	IN	OUT	IN	IN	GND	IN	IN	OUT	IN	IN	IN	OUT	OUT	4.5 V	0	35	26	35	MHz	
f _{MAX}	(F19, 4)	101																							
f _{MAX}	(F19, 4)	104																							
f _{MAX}	(F19, 4)	105																							
f _{MAX}	(F19, 4)	106																							
f _{PHL1}	3003 (F19, 4)	108		OUT	IN	IN	OUT	IN	IN	GND	IN	IN	OUT	IN	IN	IN	OUT	OUT	4.5 V	0	35	26	35	MHz	
f _{PHL1}	3003 (F19, 4)	110																							
f _{PHL1}	3003 (F19, 4)	111																							
f _{PHL1}	3003 (F19, 4)	112																							
f _{PHL1}	3003 (F19, 4)	113																							
f _{PHL1}	3003 (F19, 4)	114																							
f _{PHL1}	3003 (F19, 4)	115																							
f _{PHL1}	3003 (F19, 4)	116																							
f _{PHL1}	3003 (F19, 4)	117		DUT	IN	IN	OUT	IN	IN	GND	IN	IN	OUT	IN	IN	IN	OUT	OUT	4.5 V	0	35	26	35	MHz	
f _{PHL1}	3003 (F19, 4)	118																							
f _{PHL1}	3003 (F19, 4)	119																							
f _{PHL1}	3003 (F19, 4)	120																							
f _{PHL1}	3003 (F19, 4)	121																							
f _{PHL1}	3003 (F19, 4)	122																							
f _{PHL1}	3003 (F19, 4)	123																							
f _{PHL1}	3003 (F19, 4)	124																							
f _{PHL2}	3003 (F19, 4)	125	IN	DUT	4.5 V	4.5 V	OUT	IN	IN	10/ 10/	OUT	4.5 V	4.5 V	OUT	4.5 V	4.5 V	OUT	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	4.5 V	ns	
f _{PHL2}	3003 (F19, 4)	126																							
f _{PHL2}	3003 (F19, 4)	127																							
f _{PHL2}	3003 (F19, 4)	128																							
f _{PHL2}	3003 (F19, 4)	129																							
f _{PHL2}	3003 (F19, 4)	130																							
f _{PHL2}	3003 (F19, 4)	131																							
f _{PHL2}	3003 (F19, 4)	132																							

See footnotes at end of table.

TABLE III. Group A inspection for device type 08 - Continued.

Symbol	MIL-STD-883 Cases method E, F	Test no.	Terminal conditions 1)												Test limits					
			Subgroup 1				Subgroup 2				Subgroup 3				Measured terminal IC = +25°C		Measured terminal IC = +125°C		Measured terminal IC = -55°C	
			IC	IC	IC	IC	IC	IC	IC	IC	IC	IC	IC	IC	IC	IC	IC	IC	IC	
t _{THL} 7/ _	3004 (Fig. 4)	133	4.5 V	0T	IN	IN	OUT	GND	IN	OUT	IN	IN	IN	OUT	4.5 V	10	3	15	3	15 ns
	134	*	*	*	*	*	*	*	*	*	*	*	*	*	*	20	*	*	*	*
	135	*	*	*	*	*	*	*	*	*	*	*	*	*	*	30	*	*	*	*
	136	*	*	*	*	*	*	*	*	*	*	*	*	*	*	40	*	*	*	*
	137	*	*	*	*	*	*	*	*	*	*	*	*	*	*	17	*	*	*	*
	138	*	*	*	*	*	*	*	*	*	*	*	*	*	*	25	*	*	*	*
	139	*	*	*	*	*	*	*	*	*	*	*	*	*	*	37	*	*	*	*
	140	*	*	*	*	*	*	*	*	*	*	*	*	*	*	40	*	*	*	*
t _{T LH} 7/ _	3004 (Fig. 4)	141	*	0T	IN	IN	OUT	*	*	*	*	*	*	*	*	10	*	*	*	*
	142	*	*	*	*	*	*	*	*	*	*	*	*	*	*	20	*	*	*	*
	143	*	*	*	*	*	*	*	*	*	*	*	*	*	*	30	*	*	*	*
	144	*	*	*	*	*	*	*	*	*	*	*	*	*	*	40	*	*	*	*
	145	*	*	*	*	*	*	*	*	*	*	*	*	*	*	17	*	*	*	*
	146	*	*	*	*	*	*	*	*	*	*	*	*	*	*	25	*	*	*	*
	147	*	*	*	*	*	*	*	*	*	*	*	*	*	*	37	*	*	*	*
	148	*	*	*	*	*	*	*	*	*	*	*	*	*	*	40	*	*	*	*

See footnotes at end of table.

TABLE III. Group A inspection for device type 52.

Symbol	Case no.	Terminal conditions $\frac{V}{I}$												Test limits					
		Case 2				Case 3				Case 4				Measured terminal 3		Subgroup 1		Subgroup 2	
		MIL-STD-883 Cases C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14	TC = +25°C	TC = +125°C	TC = -55°C
V _{IC} (pos)	1	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	GND	I _{CTR} 1/	I _D 1/	I _{CLK} 1/
	2	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	"	"	"	"
	3	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	"	"	"	"
	4	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	"	"	"	"
	5	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	"	"	"	"
	6	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	"	"	"	"
	7	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	"	"	"	"
	8	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	1 mA	"	"	"	"
V _{IC} (neg)	9	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	GND	I _{CTR} 1/	I _D 1/	I _{CLK} 1/
	10	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	"	"	"	"
	11	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	"	"	"	"
	12	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	"	"	"	"
	13	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	"	"	"	"
	14	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	"	"	"	"
	15	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	"	"	"	"
	16	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	-1 mA	"	"	"	"
I _{CC}	17	5.5 V	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	5.5 V	GND	GND	GND
	18	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"	"	"	"
I _{CA}	19	5.5 V	2.4 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	5.5 V	2.4 V	0.8 V	0.8 V
	20	2.4 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	"	"	"	"
	21															"	"	"	"
	22															"	"	"	"
V _{OH6}	23	2.0 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	2.0 V	0.8 V	0.8 V	0.8 V
	24	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	"	"	"	"
	25															"	"	"	"
	26	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	"	"	"	"
	27	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	"	"	"	"
	28	2.0 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	"	"	"	"
	29															"	"	"	"
	30															"	"	"	"
V _{OH7}	31-38	Same terminal conditions as specified above for V _{OH6} except I _{OL} = -5.2 mA, V _{CC} = 5.5 V.												3.98	3.70	3.98	3.98	3.98	3.98
V _{OL6}	39	2.0 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	2.0 V	0.8 V	0.8 V	0.8 V
	40	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	"	"	"	"
	41															"	"	"	"
	42															"	"	"	"
	43	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	2.0 V	"	"	"	"
	44	2.0 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	0.8 V	"	"	"	"
	45															"	"	"	"
	46															"	"	"	"
V _{OL7}	47-54	Same terminal conditions as specified above for V _{OL6} except I _{OL} = 5.2 mA, V _{CC} = 5.5 V.												0.26	0.4	0.26	0.26	0.26	0.26

See footnotes at end of table.

TABLE III. Group A inspection for device type 52 - Continued.

Symbol	Case no.	Terminal conditions T_c												Test limits							
		MIL-STD-883 C.0	2	3	4	6	8	9	10	12	13	14	16	18	19	20	Measured terminal	Subgroup 1	Subgroup 2	Subgroup 3	Unit
Method	Test no.	JCTR	ID	IOLK	IPRE	IQ	IC	GND	20	20	2PTE	2CLK	20	2CTR	V _{CC}	$T_c = 25^\circ\text{C}$	$T_c = +125^\circ\text{C}$	$T_c = -55^\circ\text{C}$			
11054	3011	55	4.0 V	GND	GND	GND	GND	GND	*	*	GND	GND	GND	GND	4.0 V	10	-10	-10	-10	A	
		56	GND	GND	GND	GND	GND	GND	*	*	GND	GND	GND	GND	4.0 V	11	*	*	*	*	
		57	GND	GND	GND	GND	GND	GND	*	*	GND	GND	GND	GND	4.0 V	12	*	*	*	*	
		58	GND	GND	GND	GND	GND	GND	*	*	GND	GND	GND	GND	4.0 V	13	*	*	*	*	
1111n	3010	59	6.0 V	*	*	*	*	*	*	5.5 V	1CTR	.05	0.1	0.1	A						
		60	6.0 V	*	*	*	*	*	*	1CTR	1D	*	*	*	*						
		61	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	1CTR	1CK	*	*	*	*	
		62	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	1CTR	1PRE	*	*	*	*	
		63	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	1CTR	2PRE	*	*	*	*	
		64	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	1CTR	2CLK	*	*	*	*	
		65	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	1CTR	20	*	*	*	*	
		66	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	1CTR	2CLR	*	*	*	*	
1111l	3009	67	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	6.0 V	6.0 V	6.0 V	6.0 V	6.0 V	P	
		68	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		69	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		70	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		71	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		72	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		73	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		74	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
Subgroup 4 $T_c = 25^\circ\text{C}$																					
1111r	3012	75	4/	4/	4/	4/	4/	4/	4/	4/	4/	4/	4/	4/	4/	4/	4/	4/	4/	4/	P
		76	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		77	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		78	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		79	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		80	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		81	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
		82	GND	GND	GND	GND	GND	GND	*	*	*	*	*	*	GND	GND	*	*	*	*	
Subgroup 7 $T_c = +25^\circ\text{C}$																					
Truth table	3014	83	B	A	B	A	B	A	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		84	B	A	B	A	B	A	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		85	B	A	B	A	B	A	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		86	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		87	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		88	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		89	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		90	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		91	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		92	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		93	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		94	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
		95	A	B	A	B	A	B	L	H	CND	H	L	A	B	4.5 V	11 outputs	5/	5/	5/	S
Subgroup 8 $T_c = +25^\circ\text{C}$																					

See continuation at end of table.

TABLE III. Group A inspection for device type S2 - Continued.

Symbol	Case	Terminal conditions $\underline{I_1}$												Test limits					
		MIL-SID-Cases	2	3	4	6	8	9	10	12	13	.4	16	18	19	20	Measured terminal $T_C = +25^\circ C$	Subgroup 1 $T_C = +25^\circ C$	Subgroup 2 $T_C = +125^\circ C$
tMAX (Fig. 4)	96 97 98 99	4.5 V 4.5 V IN IN	IN IN 4.5 V 4.5 V	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	4.5 V 4.5 V IN IN	ICLK to IQ 2CLK to 2Q ICLK to 2Q	1Q 2Q 2Q	21 28	28 MHz				
tPHL1 (Fig. 4)	100 101 102 103	4.5 V 4.5 V 4.5 V IN	IN IN IN IN	4.5 V 4.5 V 4.5 V 4.5 V	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	4.5 V 4.5 V 4.5 V 4.5 V	ICLK to IQ 2CLK to 2Q ICLK to 2Q	5 20 20	31 21	31 ns				
tPLH1 (Fig. 4)	104 105 106 107	4.5 V 4.5 V 4.5 V IN	IN IN IN IN	4.5 V 4.5 V 4.5 V 4.5 V	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	4.5 V 4.5 V 4.5 V 4.5 V	ICLK to IQ 2CLK to 2Q ICLK to 2Q	5 20 20	31 21	31 ns				
tPHL2 (Fig. 4)	108 109 110 111	4.5 V 4.5 V 4.5 V IN	GND GND GND IN	4.5 V 4.5 V 4.5 V OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	4.5 V 4.5 V 4.5 V 4.5 V	GND GND GND IN	GND GND GND IN	GND GND GND IN	GND GND GND IN	ICLR to IQ 1PRE to IQ 2CLR to 2Q 2CLR to 2Q	1 1 1 1	47 35 35 35	47 35 35 35	
tPLH2 (Fig. 4)	112 113 114 115	4.5 V 4.5 V 4.5 V IN	GND GND GND IN	4.5 V 4.5 V 4.5 V OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	4.5 V 4.5 V 4.5 V 4.5 V	GND GND GND IN	GND GND GND IN	GND GND GND IN	GND GND GND IN	ICLR to IQ 1PRE to IQ 2CLR to 2Q 2CLR to 2Q	1 1 1 1	47 35 35 35	47 35 35 35	
tTHL (Fig. 4)	116 117 118 119	4.5 V 4.5 V 4.5 V IN	GND GND GND IN	4.5 V 4.5 V 4.5 V OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	4.5 V 4.5 V 4.5 V 4.5 V	GND GND GND IN	GND GND GND IN	GND GND GND IN	GND GND GND IN	ICLR to IQ 1PRE to IQ 2CLR to 2Q 2CLR to 2Q	1 1 1 1	47 35 35 35	47 35 35 35	
tTLH (Fig. 4)	120 121 122 123	4.5 V 4.5 V 4.5 V IN	GND GND GND IN	4.5 V 4.5 V 4.5 V OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	OUT OUT OUT OUT	4.5 V 4.5 V 4.5 V 4.5 V	GND GND GND IN	GND GND GND IN	GND GND GND IN	GND GND GND IN	ICLR to IQ 1PRE to IQ 2CLR to 2Q 2CLR to 2Q	1 1 1 1	47 35 35 35	47 35 35 35	

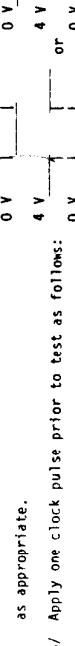
1/ Pins not designated may be high level logic, low level logic, or open.

2/ Exceptions are as follows:

a. V_C (pos) tests, the GND terminal shall be open. For test equipment that does not apply input test parameters such that t_{SV}, t_{SP}, t_{PM}, and t_W values are not greater than the recommended operating minimums (see 1). Preset outputs to required state if necessary, prior to test.b. V_C (neg) tests, the VCC terminal shall be open.c. I_C tests, the output terminals shall be open.

See 4.4.1d.

2/ Apply one clock pulse prior to test as follows:



4/ See 4.4.1c.

5/ A = 3.7 V, B = 0.4 V for all device types (except S2 where A = 2.4 V); H > 2.5 V, L < 2.5 V.

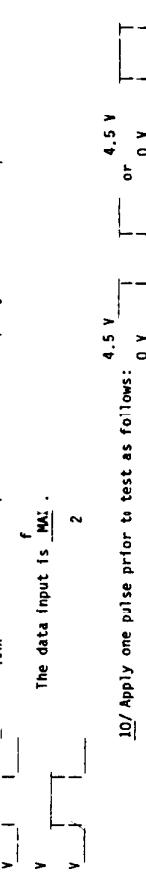
6/ Only a summary of attributes data is required.

7/ Apply input test parameters such that t_{SV}, t_{SP}, t_{PM}, and t_W values are not greater than the recommended operating minimums (see 1). Preset outputs to required state if necessary, prior to test.

8/ See 4.4.1d.

9/ The MAX limit specified is the frequency of the clock input.

10/ Apply one pulse prior to test as follows:



as appropriate.

11/ Three-state output conditions are required.

4.4.3 Group C inspection. Group C inspection shall be in accordance with table III of method 5005 of MIL-STD-883 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein. Delta limits shall apply only to subgroup 1 of group C inspection and shall consist of tests specified in table IV herein.
- b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
 - (1) Test condition D and as specified in 4.5.2 herein and as shown on figure 3 (note 3), or equivalent.
 - (2) $T_A = +125^\circ\text{C}$ minimum.
 - (3) Test duration, 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.4.4 Group D inspection. Group D inspection shall be in accordance with table IV of method 5005 of MIL-STD-883. End-point electrical parameters shall be as specified in table II herein.

4.4.5 Group E inspection. Group E inspection is required only for device types intended to be marked as radiation hardened (see 3.6.1). When group E testing is performed, it shall be in accordance with table V of method 5005 of MIL-STD-883.

4.5 Methods of inspection. Methods of inspection shall be specified as follows:

4.5.1 Voltage and current. Unless otherwise specified, all voltages given are referenced to the microcircuit GND terminal. Currents given are conventional current and positive when flowing into the referenced terminal.

4.5.2 Burn-in and life test cool down procedures. When the burn-in and life tests are completed and prior to removal of bias voltages, the devices under test (DUT) shall be cooled to within 10°C of their power stable condition at room temperature; then, electrical parameter end-point measurements shall be performed.

TABLE IV. Delta limits at 25°C .

Parameter 1/	Device types	
	All	
I_{CC}		$\pm 30 \text{ nA}$

1/ The above parameter shall be recorded before and after the required burn-in and life tests to determine deltas (Δ).

4.5.3 Quiescent supply current (I_{CC} test). When performing quiescent supply current measurements (I_{CC}), the meter shall be placed so that all currents flow through the meter.

4.6 Data reporting. When specified in the purchase order or contract, a copy of the following data, as applicable, shall be supplied.

- a. Attributes data for all screening tests (see 4.4) and variables data for all static burn-in, dynamic burn-in, and steady-state life tests (see 3.5).
- b. A copy of each radiograph.
- c. The quality conformance inspection data (see 4.4).
- d. Parameter distribution data on parameters evaluated during burn-in (see 3.5).
- e. Final electrical parameters data (see 4.2c).

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this specification are intended for original equipment design application and logistic support of existing equipment.

6.2 Ordering data. The acquisition document should specify the following:

- a. Complete part number (see 1.2).
- b. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
- c. Requirements for certificate of compliance, if applicable.
- d. Requirements for notification of change of product or process to the contracting activity in addition to notification to the qualifying activity, if applicable.
- e. Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action and reporting of results, if applicable.
- f. Requirements for product assurance options.
- g. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements shall not affect the part number. Unless otherwise specified, these requirements shall not apply to direct purchase by, or direct shipment to the Government.
- h. Requirements for "JAN" marking.
- i. Requirements for total dose radiation testing (see 3.6.1), if applicable.

6.3 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-M-38510, MIL-STD-883, and as follows:

C _T - - - - - - - - - - - - - - -	Input terminal-to-GND capacitance.
GND - - - - - - - - - - - - - - -	Ground zero voltage potential.
I _{CC} - - - - - - - - - - - - - - -	Quiescent supply current.
T _A - - - - - - - - - - - - - - -	Free air temperature.
V _{CC} - - - - - - - - - - - - - - -	Positive supply voltage.

6.4 Logistic support. Lead materials and finishes (see 3.3) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class S for National Aeronautics and Space Administration or class B for Department of Defense (see 1.2.2), lead finish C (see 3.3). Longer length leads and lead forming shall not affect the part number.

6.5 Substitutability. The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information shall not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-M-38510.

Military device type	Generic-industry type
01	54HC73
02	54HC74
03	54HC107
04	54HC109
05	54HC112
06	54HC173
07	54HC174
08	54HC175
52	54HCT74

6.6 Handling. MOS devices must be handled with certain precautions to avoid damage due to accumulation of static charge. Input protective devices have been designed in the chip to minimize the effect of this static build up. However, the following handling practices are recommended:

- a. Devices should be handled on benches with conductive and grounded surface.
- b. Ground test equipment and tools.
- c. Do not handle devices by the leads.
- d. Store devices in conductive foam or carriers.
- e. Avoid use of plastic, rubber, or silk in MOS areas.
- f. Maintain relative humidity above 50 percent, if practical.

Custodians:

Army - ER
Navy - EC
Air Force - 17
NASA - NA

Review activities:

Army - AR, MI
Air Force - 11, 19, 85, 99
DLA - ES

User activities:

Army - SM
Navy - AS, CG, OC, MC, SH

Preparing activity:
Air Force - 17

Agent:
DLA - ES

(Project 5962-1038)