

MILITARY SPECIFICATION  
MICROCIRCUITS, DIGITAL, HIGH SPEED, CMOS,  
DECODERS, MONOLITHIC SILICON, POSITIVE LOGIC

Inactive for new design after 9 August 1996

This amendment forms a part of MIL-M-38510/658, dated 19 December 1986,  
and is approved for use by all Departments and Agencies of the Department of Defense

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TABLE I, Additional supply current quiescent per input pin: delete in its entirety and substitute as follows:

Additional supply current quiescent per input pin (one unit load)	$I_{CC\Delta}$	$V_{IL} = 0.8 \text{ V}$ , $V_{IH} = 2.4 \text{ V}$ , Test pin at $V_{IN} = 2.4 \text{ V}$ , Other pins at 0.8 V or 5.5 V, $I_O = 0 \text{ V}$	52	5.5 V		3.0	mA
	4/		53			1.5	

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TABLE III, device type 52,  $I_{CC\Delta}$ , test numbers 14, 15, and 16: delete in its entirety and substitute as follows:

$I_{CC\Delta}$	14	0.8 V	2.4 V	GND							5.5 V	V <sub>CC</sub>	3.0	3.0	3.0	mA					
	15	2.4 V	0.8 V	0.8 V	0.8 V	"	5.5 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"
	16				2.4 V	"	0.8 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"

MIL-M-38510/658  
AMENDMENT 1

CONCLUDING MATERIAL

Custodians:

Army - CR  
Navy - EC  
Air Force - 17  
NASA - NA

Preparing activity:  
DLA - CC

Review activities:

Army - AR, MI, SM  
Air Force - 11, 19, 85, 99  
Navy - AS, CG, MC, OS, SH, TD

(Project 5962-1818)

Civil Agency Coordinating Activity:

NASA - NA