

MILITARY SPECIFICATION

TRANSISTOR, NPN, SILICON, POWER  
TYPES 2N1714 THROUGH 2N1717

This amendment forms a part of Military Specification MIL-S-19500/263A(EL), dated 15 May 1963.

PAGE 1

- \* Title: Delete in its entirety and substitute the following:

"SEMICONDUCTOR DEVICE, TRANSISTOR, NPN, SILICON, POWER  
TYPES 2N1714, 2N1714A, 2N1715, 2N1715A,  
2N1716, 2N1716A, 2N1717, AND 2N1717A"

- \* Delete "type numbers" shown in types column of electrical characteristics table and substitute the following:

2N1714, 2N1714A 1/  
2N1715, 2N1715A 1/  
2N1716, 2N1716A 1/  
2N1717, 2N1717A 1/

- 1/ Make these same changes wherever these type numbers appear in the specification."

- \* Bottom of page: Delete "FSC 5960" and substitute "FSC 5961".

PAGE 2

Add the following paragraph:

"3.3.3 Terminal lead length. Terminal-lead length(s) other than specified on figure 1 may be furnished when specified in the order."

3.5, delete and substitute the following:

"3.5 Marking. Except as otherwise specified herein, marking shall be in accordance with MIL-S-19500. If any specification-requirements waiver has been granted, the product-identification marking shall consist of the "classification" type designation only. The "manufacturer's identification" and "country of origin" may, at option of the manufacturer, be omitted from being marked on the semiconductor device covered herein."

3.5.1: Delete in its entirety.

PAGE 4

TABLE I, Method 3036, under Symbol column: Delete "ICRO" and substitute "ICBO".

PAGE 5

TABLE I, Method 3236, under Conditions column: Delete " $V_{BC} = 10 \text{ Vdc}$ " and substitute " $V_{CB} = 10 \text{ Vdc}$ ".

PAGE 7

- \* TABLE III, Method 3136: Delete "3136" and substitute "3131"; under Conditions column, delete  $T_2 = T = +165^{\circ} \pm 10^{\circ}C$   
 $T_1 = T_C = +110 \pm 10^{\circ}C$   
 $I_{BC} = 50 \text{ mAdc}$

and substitute the following:

$I_C$  (during heating) = 750 mAdc;  
 $V_{CE} = 10 \text{ Vdc}$ ;  
 $I_M = 1 \text{ mAdc}$ ;  
 $T_R$  point shall be the case  $30^{\circ}C \leq T_R \leq 70^{\circ}C$ ;  
mounting arrangement shall be to heat sink."

PAGE 9

- \* Add new note 6 as follows:

"NOTE 6: For transistor types 2N17148, 2N17158, 2N17168, and 2N17178, lead length shall be 0.5 inch (12.70 mm) minimum and 0.75 inch (19.05 mm) maximum."

- \* NOTE: The margins of this amendment are marked with an asterisk to indicate where changes (additions, modifications, corrections, deletions) from previous amendments were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous amendment.

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