

The documentation and process conversion measures necessary to comply with this revision shall be completed by 22 February 2003.

INCH-POUND

MIL-PRF-19500/528A
 AMENDMENT 1
 14 November 2002

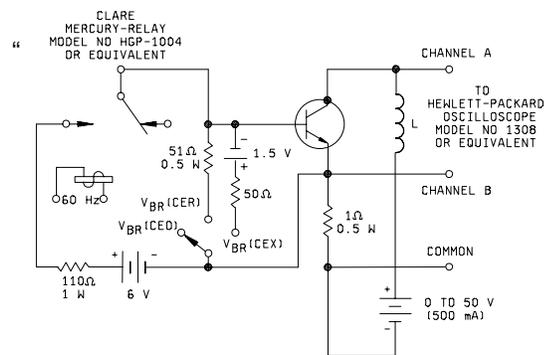
PERFORMANCE SPECIFICATION SHEET

SEMICONDUCTOR DEVICE, TRANSISTOR, NPN,
 SILICON, POWER, TYPES 2N6032 AND 2N6033
 JAN, JANTX, AND JANTXV,

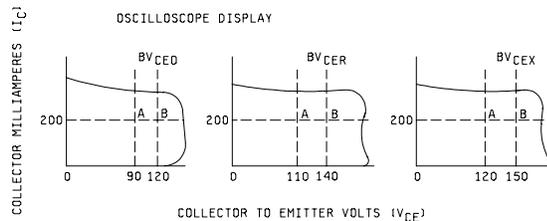
This amendment forms a part of MIL-PRF-19500/528A, dated 23 July 1999, and is approved for use by all Departments and Agencies of the Department of Defense.

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FIGURE 2, delete and substitute:



L=15 mH FOR $V_{BR(CEO)}$, $V_{BR(CER)}$ MEASUREMENTS
 L=2 mH FOR $V_{BR(CEX)}$ MEASUREMENTS



NOTE:

$V_{(BR)CEO}$, $V_{(BR)CER}$, $V_{(BR)CEX}$ is acceptable when the trace falls to the right and above point "A" for type 2N6032. The trace shall fall to the right and above point "B" for type 2N6033

FIGURE 2. $V_{(BR)CEO}$, $V_{(BR)CER}$, $V_{(BR)CEX}$ measurement circuit."

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Custodians:
Air Force - 11
DLA - CC

Preparing activity:
DLA - CC

(Project 5961-2677)

Review activities:
Air Force - 19