

REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Add one vendor, CAGE U4637. Make changes to 1.2.2, 3.3, 4.2, 4.3, and editorial changes throughout. Make changes to table I and table II.	89-12-28	M. A. FRYE
B	Changes in accordance with N.O.R. 5962-R035-93.	93-04-06	M. A. FRYE
C	Changes in accordance with N.O.R. 5962-R176-96.	96-07-10	M. A. FRYE
D	Drawing updated to reflect current requirements.	01-11-01	R. MONNIN

THE ORIGINAL FIRST SHEET OF THIS DRAWING HAS BEEN REPLACED.

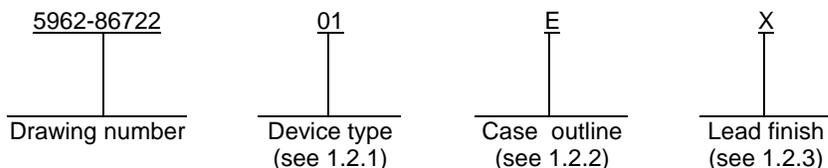
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PMIC N/A	PREPARED BY DONALD R. OSBORNE	DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216 http://www.dsccl.dla.mil																	
STANDARD MICROCIRCUIT DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE AMSC N/A	CHECKED BY D. A. DICENZO																		
	APPROVED BY N. A. HAUCK	MICROCIRCUIT, LINEAR, SWITCHED-MODE POWER SUPPLY CONTROL CIRCUIT, MONOLITHIC SILICON																	
	DRAWING APPROVAL DATE 87-08-18																		
	REVISION LEVEL D	SIZE A	CAGE CODE 67268	5962-86722															
		SHEET 1 OF 11																	

1. SCOPE

1.1 Scope. This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.

1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:



1.2.1 Device type(s). The device type(s) identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	5560	Switched-mode power supply control circuit

1.2.2 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
E	GDIP1-T16 or CDIP2-T16	16	Dual-in-line

1.2.3 Lead finish. The lead finish is as specified in MIL-PRF-38535, appendix A.

1.3 Absolute maximum ratings.

Supply voltage forced mode (V_S)	+18 V dc
Supply current fed mode (I_S)	30 mA
Output transistor current (I_C)	40 mA
Output transistor collector voltage (OUTPUT COLLECTOR pin)	$V_{CC} + 1.4$ V
Output transistor maximum emitter voltage (OUTPUT EMITTER pin)	+5 V
Maximum power dissipation (P_D)	1 W
Storage temperature range (T_{STG})	-65°C to +150°C
Lead temperature (soldering, 60 seconds)	+300°C
Junction temperature (T_J)	+150°C
Thermal resistance, junction-to-case (θ_{JC})	See MIL-STD-1835

1.4 Recommended operating conditions.

Operating ambient temperature range (T_A)	-55°C to +125°C
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2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DoDISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-38535 -- Integrated Circuits, Manufacturing, General Specification for.

STANDARDS

DEPARTMENT OF DEFENSE

MIL-STD-883 - Test Method Standard Microcircuits.
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

HANDBOOKS

DEPARTMENT OF DEFENSE

MIL-HDBK-103 -- List of Standard Microcircuit Drawings.
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Unless otherwise indicated, copies of the specification, standards, and handbooks are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535, appendix A and herein.

3.2.1 Case outline. The case outline shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.3 Logic diagram. The logic diagram shall be as specified on figure 2.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions ^{1/} -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Reference section							
Internal reference voltage	V _{REF}	I _L = -7 mA	1	01	3.69	3.81	V
			2,3		3.65	3.85	
Internal zener reference	V _Z	I _L = -7 mA, T _A = +25°C	1	01	7.8	8.8	V
Oscillator section							
Frequency range	f _R	R = 5 kΩ, f _O = 20 kHz	4,5,6	01	50	100 k	Hz
Duty cycle range ^{2/}	D.C.	R = 5 kΩ, f _O = 20 kHz, T _A = +25°C	4	01	0	98	%
MODULATOR section							
Modulation input current	I _I	MODULATOR pin = 2 V	1,2,3	01		20	μA
Protection function section							
Input current at DUTY CYCLE CONTROL pin	I _I	DUTY CYCLE CONTROL pin = 2 V	1,2,3	01		20	μA
Duty cycle limit control at DUTY CYCLE CONTROL pin	D.C.	(For 50 % maximum duty cycle) 15 kHz to 50 kHz / (41 % of V _Z), T _A = +25°C	4	01	40	60	% of duty cycle
Low supply voltage protection threshold at V _{CC} pin	V _{CC(TH)}	T _A = +25°C	1	01	8	10.5	V
Feedback loop protection trip threshold at FEEDBACK pin	V _{TH}	T _A = +25°C	1	01	400	720	mV
Pull up current at FEEDBACK pin	I _{fb}	FEEDBACK pin = 2 V	1,2,3	01	-7	-35	μA

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions ^{1/} -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Protection function section - continued							
DEMAGNETIZATION / OVERVOLTAGE pin protection trip on threshold	V _{TH}	T _A = +25°C	1	01	470	720	mV
Input current at DEMAGNETIZATION / OVERVOLTAGE pin	I _I	DEMAGNETIZATION / OVERVOLTAGE pin = 0.25 V	1	01		-10	μA
			2,3			-20	
Duty cycle control at FEED FORWARD pin	DC _{ff}	FEED FORWARD pin = 2 V _Z , T _A = +25°C	4	01	30	50	<u>3/</u>
Input current at FEED ^{4/} FORWARD pin	I _I	FEED FORWARD pin = 16 V, V _{CC} = 18 V	1	01		5	μA
			2,3			10	

External synchronization section

Voltage off at EXTERNAL SYNCHRONIZATION pin	V _{OFF}	T _A = +25°C	1	01	0	0.8	V
Voltage on at EXTERNAL SYNCHRONIZATION pin	V _{ON}	T _A = +25°C	1	01	2	V _Z	V
Sink current at EXTERNAL SYNCHRONIZATION pin	I _I	EXTERNAL SYNCHRONIZATION pin = 0 V	1	01		-100	μA
			2,3			-125	

Remote section

Voltage off at REMOTE pin	V _{OFF}	T _A = +25°C	1	01	0	0.8	V
Voltage on at REMOTE pin	V _{ON}	T _A = +25°C	1	01	2	V _Z	V

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions ^{1/} -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Remote section - continued							
Sink current at REMOTE ON/OFF pin	I _I	REMOTE ON/OFF pin = 0 V	1	01		-100	μA
			2,3			-125	
Current limiting section							
Current at CURRENT LIMITING pin	I	CURRENT LIMITING pin = 250 mV	1	01		-20	μA
			2,3			-40	
Single pulse inhibit ^{2/} delay at CURRENT LIMITING pin	T _D	Inhibit delay time for 20 percent override at 40 mA I _{OUT} , T _A = +25°C	9	01		0.8	μs
Trip levels: shut down, slow start	V _{THSD}	T _A = +25°C	1	01	0.500	0.700	V
Trip levels: current limit	V _{THCL}	T _A = +25°C	1	01	0.400	0.560	V
Error amplifier section							
Output voltage swing, high	V _{OH}		1,2,3	01	6.2	9.5	V
Output voltage swing, low	V _{OL}		1	01		0.7	V
			2,3			0.85	
Open loop gain	A _o		4	01	54		dB
			5,6		50		
Feedback resistor	R _f	T _A = +25°C	1	01	10		kΩ
Output stage section							
Voltage at OUTPUT COLLECTOR pin	V _{CE(SAT)}	I _C = 40 mA, V _E = 0 V, T _A = +25°C	1	01		0.5	V

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions ^{1/} -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Output stage section - continued							
Maximum voltage at ^{2/} OUTPUT EMITTER pin	V _{E(MAX)}	I _E = -200 mA, V _C = 12 V, T _A = +25°C	1	01	5		V
Supply current at V _{CC} pin	I _{CC}	I _Z = 0 V, voltage forced	1	01		10	mA
			2,3			15	
Supply voltage at V _{CC} pin	V _{CC}	I _{CC} = 10 mA current fed, T _A = +25°C	1	01	20	23	V
		I _{CC} = 30 mA current fed, T _A = +25°C			20	30	

^{1/} Unless otherwise specified, V_{CC} = 12 V.

^{2/} If not tested, shall be guaranteed to the limits specified in table I herein.

^{3/} Percentage of original duty cycle

^{4/} Does not include current for timing resistor or capacitor.

3.5 Marking. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in MIL-HDBK-103 (see 6.6 herein). For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device.

3.5.1 Certification/compliance mark. A compliance indicator "C" shall be marked on all non-JAN devices built in compliance to MIL-PRF-38535, appendix A. The compliance indicator "C" shall be replaced with a "Q" or "QML" certification mark in accordance with MIL-PRF-38535 to identify when the QML flow option is used.

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DSCC-VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DSCC-VA shall be required in accordance with MIL-PRF-38535, appendix A.

3.9 Verification and review. DSCC, DSCC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

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Device type	01
Case outline	E
Terminal number	Terminal symbol
1	V _{CC}
2	V _Z
3	FEEDBACK
4	GAIN
5	MODULATOR
6	DUTY CYCLE CONTROL
7	R _T
8	C _T
9	EXTERNAL SYNCHRONIZATION
10	REMOTE ON / OFF
11	CURRENT LIMITING
12	GND
13	DEMAGNETIZATION / OVERVOLTAGE
14	OUTPUT (EMITTER)
15	OUTPUT (COLLECTOR)
16	FEED FORWARD

FIGURE 1. Terminal connections.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-86722
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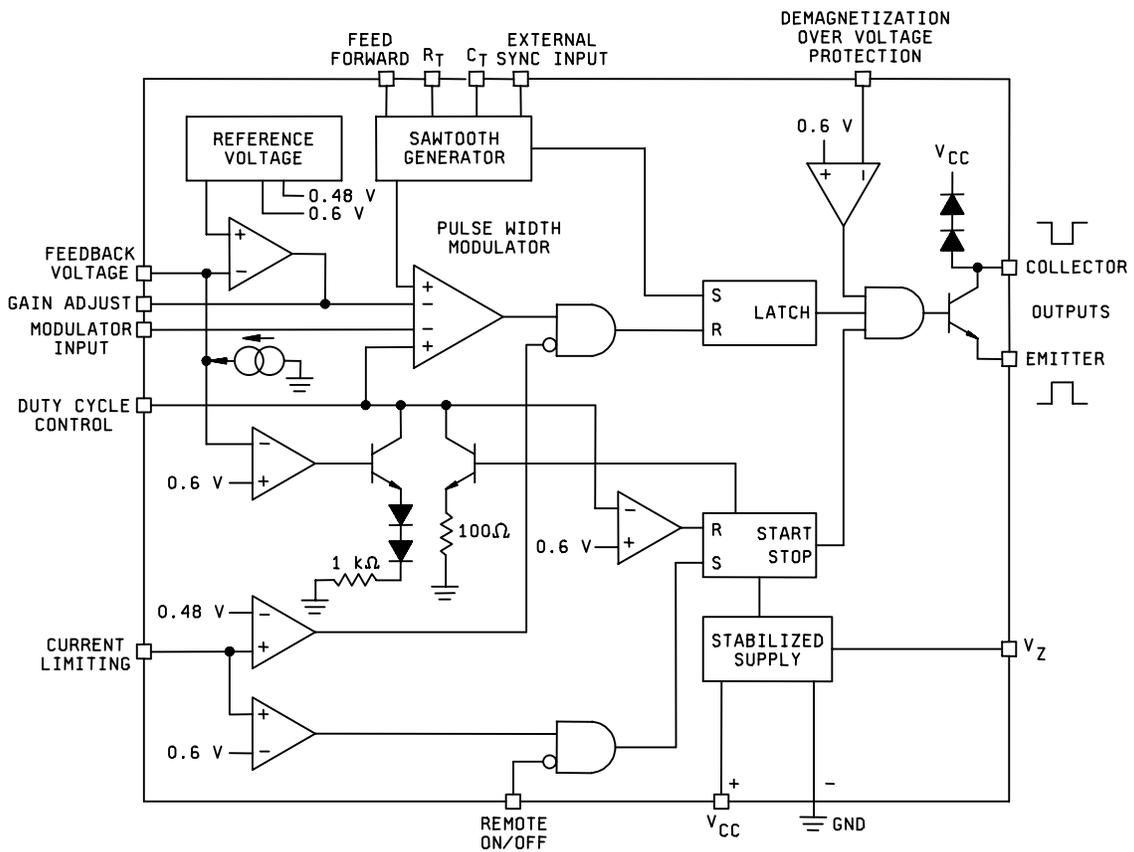


FIGURE 2. Logic diagram.

**STANDARD
MICROCIRCUIT DRAWING**
DEFENSE SUPPLY CENTER COLUMBUS
COLUMBUS, OHIO 43216-5000

SIZE
A

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4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.

(2) $T_A = +125^{\circ}\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 7, 8, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test conditions, method 1005 of MIL-STD-883.

(1) Test condition A or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.

(2) $T_A = +125^{\circ}\text{C}$, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*,2,3,4,5,6
Group A test requirements (method 5005)	1,2,3,4,5,6,9
Groups C and D end-point electrical parameters (method 5005)	1

* PDA applies to subgroup 1.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.4 Record of users. Military and industrial users shall inform Defense Supply Center Columbus when a system application requires configuration control and the applicable SMD. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0544.

6.5 Comments. Comments on this drawing should be directed to DSCC-VA, Columbus, Ohio 43216-5000, or telephone (614) 692-0547.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DSCC-VA.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 01-11-01

Approved sources of supply for SMD 5962-86722 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 during the next revision. MIL-HDBK-103 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This bulletin is superseded by the next dated revision of MIL-HDBK-103.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-8672201EA	U3158	IP5560J/883
	<u>3/</u>	5560/BEA
	<u>3/</u>	IP5560J/883B

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ Not available from an approved source of supply.

Vendor CAGE
number

U3158

Vendor name
and address

Semelab PLC
Coventry Road, Lutterworth
Leicestershire LE174JB
United Kingdom
Point of contact: Martinez & Associates
19 Palham Island Road
Wayland, MA 01778

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