REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Add vendor CAGE 01295. Add case outline 2. Make changes to 1.2.2, 1.3, FIGURE 1, TABLE I, and 6.4. In accordance with NOR 5962-R302-92.	92-12-02	M. A. FRYE
B	Add generic part number LT1007 as device type 02. Make changes to 1.2.1, FIGURE 1, and TABLE I. Redrawn.	95-05-04	M. A. FRYE

THE ORIGINAL FIRST PAGE OF THIS DRAWING HAS BEEN REPLACED.

REV																			
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REV STATUS OF SHEETS															
REV	B	B	B	B	B	B	B	B							
SHEET	1	2	3	4	5	6	7	8							

PMIC N/A

STANDARD MICROCIRCUIT DRAWING

THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE

AMSC N/A

PREPARED BY
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MICHAEL FRYE

DRAWING APPROVAL DATE
87-08-21

REVISION LEVEL
B

DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO 45444

MICROCIRCUIT, LINEAR, LOW-NOISE OPERATIONAL AMPLIFIER, MONOLITHIC SILICON

SIZE	CAGE CODE	5962-87578
A	67268	

SHEET 1 OF 8

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part or Identifying Number (PIN). The complete PIN shall be as shown in the following example:

1.2.1 Device type(s). The device type(s) shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	LT1007A	Low noise, high speed precision operational amplifier
02	LT1007	Low noise, high speed precision operational amplifier

1.2.2 Case outline(s). The case outline(s) shall be as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	Terminals	Package style
G	MACY1-X8	8	Can
P	GDIP1-T8 or CDIP2-T8	8	Dual-in-line
2	CQCC1-N20	20	Square leadless chip carrier

1.2.3 Lead finish. The lead finish shall be as specified in MIL-STD-883 (see 3.1 herein). Finish letter "X" shall not be marked on the microcircuit or its packaging. The "X" designation is for use in specifications when lead finishes A, B, and C are considered acceptable and interchangeable without preference.

1.3 Absolute maximum ratings.

Supply voltage . . . . . ±22.0 V dc
Input voltage . . . . . ±22 V dc
Differential input . . . . . ±25 mA
Junction temperature (T_J) . . . . . +150°C
Lead temperature (soldering, 10 seconds) . . . . . +300°C
Storage temperature range . . . . . -65°C to +125°C
Thermal resistance, junction-to-case (Θ_{JC}) . . . . . See MIL-STD-1835
Thermal resistance, junction-to-ambient (Θ_{JA}):
  Case G . . . . . 150°C/W
  Case P . . . . . 110°C/W
  Case 2 . . . . . 65°C/W

1.4 Recommended operating conditions.

Supply voltage . . . . . ±15.0 V dc
Ambient operating temperature range (T_A) . . . . . -55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and bulletin. Unless otherwise specified, the following specification, standards, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-I-38535 - Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

STANDARDS

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.
MIL-STD-1835 - Microcircuit Case Outlines.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standardized Military Drawings (SMD's).

(Copies of the specification, standards, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-I-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-I-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-I-38535 is required to identify when the QML flow option is used.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-STD-883 (see 3.1 herein) and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-55°C \le T_A \le +125°C$ $\pm V_S = \pm 15$ V unless otherwise specified	Group A subgroups	Device type	Limits 1/ Min	Limits 1/ Max	Unit
Input offset voltage	V_{OS}	2/	4	01		25	µV
			2,3			60	
			4	02		60	
			2,3			160	
Long term input offset voltage stability	V_{OS}/time	3/ 4/	1,2,3	All		1.0	µV/Mo
Average input offset drift	V_{OS}/temp	4/	1,2,3	01		0.6	µV/°C
				02		1.0	
Input offset current	I_{OS}		1	01		30	nA
			2,3			50	
			1	02		50	
			2,3			85	
Input bias current	I_{IB}		1	01	-35	+35	nA
			2,3		-60	+60	
			1	02	-55	+55	
			2,3		-95	+95	
Power dissipation	P_D		1	01		120	mW
			2,3			150	
			1	02		140	
			2,3			170	
Input voltage range	V_{IN}	4/	1	All	-11	+11	V
			2,3		-10.3	+10.3	
Maximum output voltage swing	V_{OUT}	$R_L \ge 2$ kΩ	1,2,3	01	-12.5	+12.5	V
				02	-12.0	+12.0	

See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions $-55°C \le T_A \le +125°C$ $\pm V_S = \pm 15$ V unless otherwise specified	Group A subgroups	Device type	Limits 1/ Min	Limits 1/ Max	Unit
Common mode rejection ratio	CMRR	$V_{CM} = \pm 11$ V	1	01	117		dB
				02	110		
		$V_{CM} = \pm 10.3$ V	2,3	01	112		
				02	104		
Power supply rejection ratio	PSRR	$V_S = \pm 4.5$ V to ± 18 V	1,2,3	01	104		dB
				02	100		
Input noise voltage	e_N	0.1 Hz to 10 Hz, 4/ $T_A = +25°C$	7	All		0.13	μV_{PP}
Input noise voltage density	e_{ND}	$f_O = 10$ Hz, 4/ $T_A = +25°C$	7	All		4.5	$nV/\sqrt{Hz}$
Input noise current density	I_{ND}	$f_O = 10$ Hz, 4/ $T_A = +25°C$	7	All		4.0	$pA/\sqrt{Hz}$
Slew rate	SR	$R_L = 2$ kΩ, AVCL ≥ 1, $T_A = +25°C$	7	All	1.7		V/μs
Gain bandwidth products	GBWP	$f_O = 100$ kHz, 4/ $T_A = +25°C$	7	All	5.0		MHz
Open loop output resistance	Z_O	$V_O = 0$ V, $I_O = 0$ mA, 4/ $T_A = +25°C$	7			2000	Ω
Large signal voltage gain	A_{VOL}	$V_{OUT} = \pm 10$ V, $R_L \ge 1$ kΩ	4,5,6	01	2.0		V/μV
				02	1.5		

1/ The algebraic convention, whereby the most negative value is a minimum and the most positive is a maximum, is used in this table. Negative current shall be defined as conventional current flow out of a device terminal.
2/ Input offset voltage measurements are performed by automatic test equipment approximately 0.5 second after application of power.
3/ Long term input offset stability refers to the average trend line of offset voltage v.s. time over extended periods after the first 30 days of operations. Excluding the first hour of operation, changes in V_{OS} during the first 30 days are typically 2.5 μV.
4/ If not tested, shall be guaranteed to the limits specified in table I herein.

Device types	01 and 02	
Case outlines	G and P	2
Terminal number	Terminal symbol	
1	V_{OS} TRIM	NC
2	-INPUT	V_{OS} TRIM
3	+INPUT	NC
4	$-V_S$	NC
5	NC	-INPUT
6	OUTPUT	NC
7	$+V_S$	+INPUT
8	V_{OS} TRIM	NC
9	---	NC
10	---	$-V_S$
11	---	NC
12	---	NC
13	---	NC
14	---	NC
15	---	OUTPUT
16	---	NC
17	---	$+V_S$
18	---	NC
19	---	NC
20	---	V_{OS} TRIM

NC = No connection

FIGURE 1. Terminal connections.

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-EC prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DESC-EC shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.

(2) T_A = +125°C, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test conditions, method 1005 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.

(2) T_A = +125°C, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*, 2, 3, 4, 5, 6
Group A test requirements (method 5005)	1, 2, 3, 4, 5, 6, 7
Groups C and D end-point electrical parameters (method 5005)	1

* PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-STD-883 (see 3.1 herein).

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-973 using DD Form 1692, Engineering Change Proposal .

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-EC, telephone (513) 296-6047.

6.5 Comments. Comments on this drawing should be directed to DESC-EC, Dayton, Ohio 45444-5270, or telephone (513) 296-5377.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-BUL-103. The vendors listed in MIL-BUL-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-EC.

STANDARD MICROCIRCUIT DRAWING SOURCE APPROVAL BULLETIN

DATE: 95-05-04

Approved sources of supply for SMD 5962-87578 are listed below for immediate acquisition only and shall be added to MIL-BUL-103 during the next revision. MIL-BUL-103 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DESC-EC. This bulletin is superseded by the next dated revision of MIL-BUL-103.

Standard microcircuit drawing PIN	Vendor CAGE number	Vendor similar PIN 1/
5962-8757801GX	64155	LT1007AMH/883B
	2/	LT1007AMLB
5962-8757801PX	01295	LT1007AMJGB
	64155	LT1007AMJ8/883B
5962-87578012X	01295	LT1007AMFKB
5962-8757802GX	64155	LT1007MH
5962-8757802PX	01295	LT1007MJGB
	64155	LT1007MJ8
5962-87578022X	01295	LT1007MFKB

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

2/ No longer available from an approved source of supply.

Vendor CAGE number	Vendor name and address
01295	Texas Instruments, Incorporated 13500 N. Central Expressway P.O. Box 655303 Dallas, TX 75265 Point of contact: I-20 at FM 1788 Midland, TX 79711-0448
64155	Linear Technology 1630 McCarthy Boulevard Milpitas, CA 95035-7487

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