

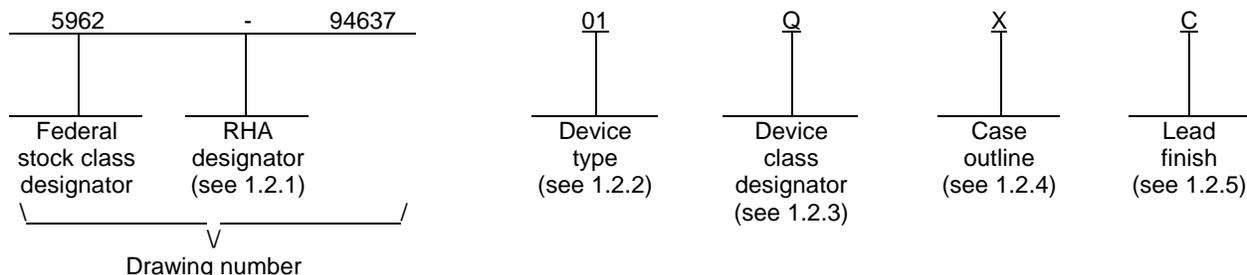
REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Changes to table I. Editorial changes throughout. -drw	00-09-26	Raymond Monnin
B	Replaced reference to MIL-STD-973 with reference to MIL-PRF-38535. Drawing updated to reflect current requirements. -rrp	04-05-25	Raymond Monnin

REV																				
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REV STATUS	REV	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B
OF SHEETS	SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14					
PMIC N/A	PREPARED BY Dan Wonnell				<p align="center">DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216 http://www.dscc.dla.mil</p>															
<p align="center">STANDARD MICROCIRCUIT DRAWING</p> <p align="center">THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS</p> <p align="center">AND AGENCIES OF THE DEPARTMENT OF DEFENSE</p> <p align="center">AMSC N/A</p>	CHECKED BY Sandra Rooney																			
	APPROVED BY Michael A. Frye				<p align="center">MICROCIRCUIT, LINEAR, ANALOG-TO-DIGITAL CONVERTER, 10-BIT, 60 MHz, MONOLITHIC SILICON</p>															
	DRAWING APPROVAL DATE 94-09-07																			
	REVISION LEVEL B																			
				SHEET 1 OF 14																

1. SCOPE

1.1 Scope. This drawing documents two product assurance class levels consisting of high reliability (device classes Q and M) and space application (device class V). A choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of Radiation Hardness Assurance (RHA) levels are reflected in the PIN.

1.2 PIN. The PIN is as shown in the following example:



1.2.1 RHA designator. Device classes Q and V RHA marked devices meet the MIL-PRF-38535 specified RHA levels and are marked with the appropriate RHA designator. Device class M RHA marked devices meet the MIL-PRF-38535, appendix A specified RHA levels and are marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>	<u>Differential nonlinearity</u>
01	AD9020S	A/D converter, 10-bit, 60 MSPS	1.5 LSB
02	AD9020T	A/D converter, 10-bit, 60 MSPS	1.25 LSB

1.2.3 Device class designator. The device class designator is a single letter identifying the product assurance level as follows:

<u>Device class</u>	<u>Device requirements documentation</u>
M	Vendor self-certification to the requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A
Q or V	Certification and qualification to MIL-PRF-38535

1.2.4 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
X	See figure 1	68	Square gullwing lead chip carrier
Y	See figure 1	68	Square leadless chip carrier

1.2.5 Lead finish. The lead finish is as specified in MIL-PRF-38535 for device classes Q and V or MIL-PRF-38535, appendix A for device class M.

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1.3 Absolute maximum ratings. 1/

Positive supply voltage (+V _S).....	+6 V dc
Negative supply voltage (-V _S).....	-6 V dc
Analog IN.....	-2 V dc to +2 V dc
+V _{REF} , -V _{REF} , 3/4 _{REF} , 1/2 _{REF} , 1/4 _{REF}	-2 V dc to +2 V dc
Digital inputs.....	-0.5 V dc to +V _S
3/4 _{REF} , 1/2 _{REF} , 1/4 _{REF} current.....	±10 mA
Digital output current.....	20 mA
Junction temperature (T _J).....	+175°C
Thermal resistance, junction-to-case(θ _{JC}):	
Case X.....	2.6°C/W
Case Y.....	2.6°C/W
Thermal resistance, junction-to-ambient(θ _{JA}):	
Case X.....	20°C/W
Case Y.....	20°C/W
Storage temperature range.....	-65°C to +150°C
Lead soldering temperature (10 seconds).....	+300°C

1.4 Recommended operating conditions.

Positive supply voltage (+V _S).....	+5 V dc
Negative supply voltage (-V _S).....	-5 V dc
Storage temperature range.....	-55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <http://assist.daps.dla.mil/quicksearch/> or www.dodssp.daps.mil or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

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3. REQUIREMENTS

3.1 Item requirements. The individual item requirements for device classes Q and V shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein. The individual item requirements for device class M shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein for device classes Q and V or MIL-PRF-38535, appendix A and herein for device class M.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.3 Electrical performance characteristics and postirradiation parameter limits. Unless otherwise specified herein, the electrical performance characteristics and postirradiation parameter limits are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device. For RHA product using this option, the RHA designator shall still be marked. Marking for device classes Q and V shall be in accordance with MIL-PRF-38535. Marking for device class M shall be in accordance with MIL-PRF-38535, appendix A.

3.5.1 Certification/compliance mark. The certification mark for device classes Q and V shall be a "QML" or "Q" as required in MIL-PRF-38535. The compliance mark for device class M shall be a "C" as required in MIL-PRF-38535, appendix A.

3.6 Certificate of compliance. For device classes Q and V, a certificate of compliance shall be required from a QML-38535 listed manufacturer in order to supply to the requirements of this drawing (see 6.6.1 herein). For device class M, a certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6.2 herein). The certificate of compliance submitted to DSCC-VA prior to listing as an approved source of supply for this drawing shall affirm that the manufacturer's product meets, for device classes Q and V, the requirements of MIL-PRF-38535 and herein or for device class M, the requirements of MIL-PRF-38535, appendix A and herein.

3.7 Certificate of conformance. A certificate of conformance as required for device classes Q and V in MIL-PRF-38535 or for device class M in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change for device class M. For device class M, notification to DSCC-VA of change of product (see 6.2 herein) involving devices acquired to this drawing is required for any change that affects this drawing.

3.9 Verification and review for device class M. For device class M, DSCC, DSCC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

3.10 Microcircuit group assignment for device class M. Device class M devices covered by this drawing shall be in microcircuit group number 57 (see MIL-PRF-38535, appendix A).

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C +V _S = +5 V, -V _S = -5 V, ±V _{SENSE} = ±1.75 V 1/ unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Resolution	RES	2/	1, 2, 3	All	10		Bits
Differential nonlinearity	DNL		1	01		1.25	LSB
				02		1.0	
			2, 3	01		1.5	
				02		1.25	
Integral nonlinearity	INL		1	01		2.75	LSB
				02		2.25	
			2, 3	01		3.0	
				02		2.5	
Input bias current	I _B	3/	1	All		1	mA
			2, 3			2	
Input resistance	R _{IN}		1	All	2		kΩ
Reference ladder resistance	R _{RL}		1	All	22	56	Ω
			2, 3		14	66	
Reference ladder offset voltage	V _{ORL}		1, 2, 3	All		90	mV
Input voltage logic high	V _{IH}		1, 2, 3	All	2.0		V
Input voltage logic low	V _{IL}		1, 2, 3	All		0.8	V
Input current logic high	I _{IH}		1, 2, 3	All		500	μA
Input current logic low	I _{IL}		1, 2, 3	All		800	μA
Output voltage logic high	V _{OH}	I _{OH} = 2 mA	1, 2, 3	All	2.4		V
Output voltage logic low	V _{OL}	I _{OH} = 10 mA	1, 2, 3	All		0.4	V

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C +V _S = +5 V, -V _S = -5 V, ±V _{SENSE} = ±1.75 V 1/ unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Positive supply current	+I _S		1	All		530	mA
			2, 3			542	
Negative supply current	-I _S		1	All		170	mA
			2, 3			177	
Power dissipation	P _D		1	All		3.3	W
			2, 3			3.4	
Power supply rejection ratio	PSRR	4/	1, 2, 3	All		10	mV/V
Effective number of bits	ENOB	f _{IN} = 2.3 MHz 5/	4	All	7.9		Bits
Signal-to-noise ratio	SNR ₁	f _{IN} = 2.3 MHz 5/ 6/	4	All	49.5		dB
		f _{IN} = 10.3 MHz 5/ 6/			47.5		
		f _{IN} = 15.3 MHz 5/ 6/			45.5		
Signal-to-noise ratio (without harmonics)	SNR ₂	f _{IN} = 2.3 MHz 5/ 6/	4	All	49.5		dB
		f _{IN} = 10.3 MHz 5/ 6/			49.5		
		f _{IN} = 15.3 MHz 5/ 6/			48		
Harmonic distortion	HD	f _{IN} = 2.3 MHz 5/	4	All	54.5		dB
		f _{IN} = 10.3 MHz 5/			48.5		
		f _{IN} = 15.3 MHz 5/			46.5		

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C +V _S = +5 V, -V _S = -5 V, ±V _{SENSE} = ±1.75 V 1/ unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Conversion rate	F _S		9	All	60		MSPS
Output delay time	t _{OD}	7/	9	All	6	13	ns
Output time skew	t _{OS}	7/	9	All		5	ns
Pulse width (high)	t _{PWH}		9	All	6		ns
Pulse width (low)	t _{PWL}		9	All	6		ns

- 1/ 3/4_{REF}, 1/2_{REF}, and 1/4_{REF} reference ladder taps are driven from dc sources at +0.875 V, 0 V, and -0.875 V, respectively.
 2/ No missing codes guaranteed over the full operating ambient temperature range.
 3/ Measured with ANALOG IN = +V_{SENSE}.
 4/ Measured as the ratio of the worst-case change in transition voltage of a single comparator for a 5% change in +V_S or -V_S.
 5/ ENCODE = 40 MHz.
 6/ RMS signal to rms noise with analog input signal 1 dB below full scale at specified frequency.
 7/ Output delay measured at worst-case time from 50% point of the rising edge of ENCODE to 50% point of the slowest rising or falling edge of D₀-D₉. Output skew measured as worst-case difference in output delay among D₀-D₉.

4. VERIFICATION

4.1 Sampling and inspection. For device classes Q and V, sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein. For device class M, sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 Screening. For device classes Q and V, screening shall be in accordance with MIL-PRF-38535, and shall be conducted on all devices prior to qualification and technology conformance inspection. For device class M, screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection.

4.2.1 Additional criteria for device class M.

a. Burn-in test, method 1015 of MIL-STD-883.

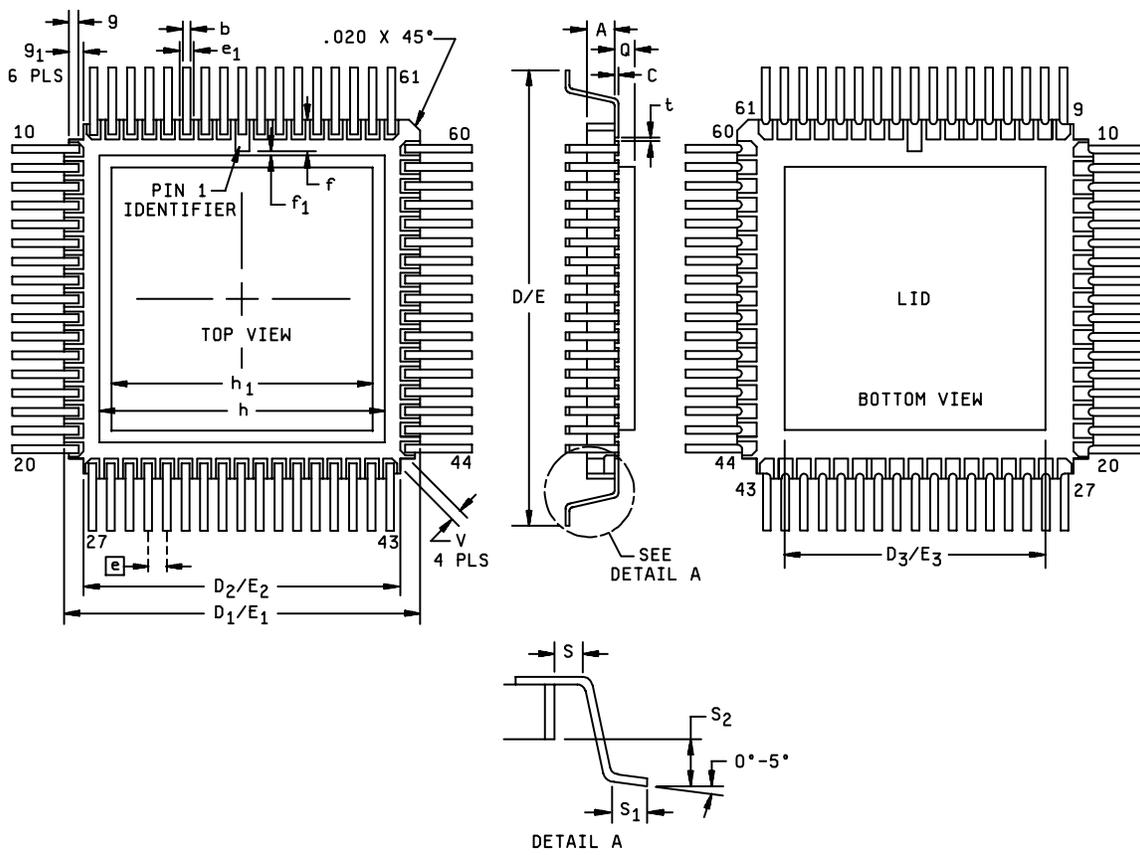
(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015.

(2) T_A = +125°C, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein.

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Case X



Symbol	Inches		Millimeters	
	Min	Max	Min	Max
A	.067	.083	1.70	2.11
b	.016	.020	.41	.51
c	.008	.012	.20	.30
D/E	1.200	1.220	30.48	30.99
D ₁ /E ₁	.940	.960	23.88	24.38
D ₂ /E ₂	.841	.859	21.36	21.82
D ₃ /E ₃	.616	.632	15.65	16.05
e	.050 BSC		1.27 BSC	
e ₁	.033	.039	.84	.99
f	.072	.082	1.83	2.08
f ₁	.015	----	.38	----

Symbol	Inches		Millimeters	
	Min	Max	Min	Max
g	.040 REF		1.02 REF	
g ₁	.042	.058	1.07	1.47
h	.750	.770	19.05	16.56
h ₁	----	.700	----	17.78
Q	----	.030	----	.76
S	.025	----	.64	----
S ₁	.040	----	1.02	----
S ₂	.025	----	.64	----
t	.010 REF		.25 REF	
v	.015	----	.38	----

FIGURE 1. Case outline.

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Device type	01 and 02
Case outline	X and Y
Terminal number	Terminal symbol
1	1/2 _{REF}
2	-V _S
3	+V _S
4	GROUND
5	GROUND
6	+V _S
7	3/4 _{REF}
8	ANALOG IN
9	ANALOG IN
10	NC
11	+V _{SENSE}
12	+V _{REF}
13	GROUND
14	ENCODE
15	+V _S
16	-V _S
17	GROUND
18	+V _S
19	D ₀ (LSB)
20	D ₁
21	D ₂
22	D ₃
23	D ₄
24	NC
25	+V _S
26	NC
27	GROUND
28	-V _S
29	-V _S
30	+V _S
31	GROUND
32	GROUND
33	+V _S
34	+V _S
35	-V _S
36	GROUND
37	+V _S

Device type	01 and 02
Case outline	X and Y
Terminal number	Terminal symbol
38	GROUND
39	GROUND
40	+V _S
41	-V _S
42	-V _S
43	GROUND
44	NC
45	+V _S
46	D ₅
47	D ₆
48	D ₇
49	D ₈
50	D ₉ (MSB)
51	OVERFLOW
52	+V _S
53	GROUND
54	-V _S
55	+V _S
56	-V _{REF}
57	-V _{SENSE}
58	NC
59	LSBs INVERT
60	NC
61	MSB INVERT
62	NC
63	1/4 _{REF}
64	-V _S
65	+V _S
66	GROUND
67	GROUND
68	+V _S

FIGURE 2. Terminal connections.

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4.2.2 Additional criteria for device classes Q and V.

- a. The burn-in test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document revision level control of the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II herein.
- c. Additional screening for device class V beyond the requirements of device class Q shall be as specified in MIL-PRF-38535, appendix B.

4.3 Qualification inspection for device classes Q and V. Qualification inspection for device classes Q and V shall be in accordance with MIL-PRF-38535. Inspections to be performed shall be those specified in MIL-PRF-38535 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4 Conformance inspection. Technology conformance inspection for classes Q and V shall be in accordance with MIL-PRF-38535 including groups A, B, C, D, and E inspections and as specified herein. Quality conformance inspection for device class M shall be in accordance with MIL-PRF-38535, appendix A and as specified herein. Inspections to be performed for device class M shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 5, 6, 7, 8, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.4.2 Group C inspection. The group C inspection end-point electrical parameters shall be as specified in table II herein.

4.4.2.1 Additional criteria for device class M. Steady-state life test conditions, method 1005 of MIL-STD-883:

- a. Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.
- b. $T_A = +125^{\circ}\text{C}$, minimum.
- c. Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

Test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)	Subgroups (in accordance with MIL-PRF-38535, table III)	
	Device class M	Device class Q	Device class V
Interim electrical parameters (see 4.2)	1	1	1
Final electrical parameters (see 4.2)	1, 2, 3, 4, 9 <u>1/</u>	1, 2, 3, 4, 9 <u>1/</u>	1, 2, 3, 4, 9 <u>1/</u>
Group A test requirements (see 4.4)	1, 2, 3, 4, 9	1, 2, 3, 4, 9	1, 2, 3, 4, 9
Group C end-point electrical parameters (see 4.4)	1	1	1, 2, 3, 4, 9
Group D end-point electrical parameters (see 4.4)	1	1	1
Group E end-point electrical parameters (see 4.4)	---	---	---

1/ PDA applies to subgroup 1.

4.4.2.2 Additional criteria for device classes Q and V. The steady-state life test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The test circuit shall be maintained under document revision level control by the device manufacturer's TRB in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

4.4.3 Group D inspection. The group D inspection end-point electrical parameters shall be as specified in table II herein.

4.4.4 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein).

- a. End-point electrical parameters shall be as specified in table II herein.
- b. For device classes Q and V, the devices or test vehicle shall be subjected to radiation hardness assured tests as specified in MIL-PRF-38535 for the RHA level being tested. For device class M, the devices shall be subjected to radiation hardness assured tests as specified in MIL-PRF-38535, appendix A for the RHA level being tested. All device classes must meet the postirradiation end-point electrical parameter limits as defined in table I at $T_A = +25^\circ\text{C} \pm 5^\circ\text{C}$, after exposure, to the subgroups specified in table II herein.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535 for device classes Q and V or MIL-PRF-38535, appendix A for device class M.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.1.1 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor prepared specification or drawing.

6.1.2 Substitutability. Device class Q devices will replace device class M devices.

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6.2 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.3 Record of users. Military and industrial users should inform Defense Supply Center Columbus (DSCC) when a system application requires configuration control and which SMD's are applicable to that system. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0544.

6.4 Comments. Comments on this drawing should be directed to DSCC-VA , Columbus, Ohio 43216-5000, or telephone (614) 692-0547.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535 and MIL-HDBK-1331.

6.6 Sources of supply.

6.6.1 Sources of supply for device classes Q and V. Sources of supply for device classes Q and V are listed in QML-38535. The vendors listed in QML-38535 have submitted a certificate of compliance (see 3.6 herein) to DSCC-VA and have agreed to this drawing.

6.6.2 Approved sources of supply for device class M. Approved sources of supply for class M are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DSCC-VA.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 04-05-25

Approved sources of supply for SMD 5962-94637 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-9463701MXC	24355	AD9020SZ/883
5962-9463701MYA	<u>3/</u>	AD9020SE/883
5962-9463702MXC	24355	AD9020TZ/883
5962-9463702MYA	<u>3/</u>	AD9020TE/883

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ Not available from an approved source of supply.

Vendor CAGE
number

24355

Vendor name
and address

Analog Devices
Rt 1 Industrial Park
PO Box 9106
Norwood, MA 02062
Point of contact:
7910 Triad Center Drive
Greensboro, NC 27409-9605

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